

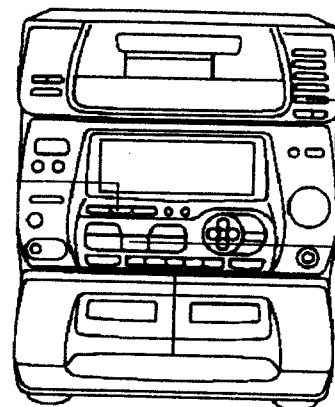
**COMPACT**  
**disc**  
DIGITAL AUDIO

**MASH**\*1  
multi-stage noise shaping

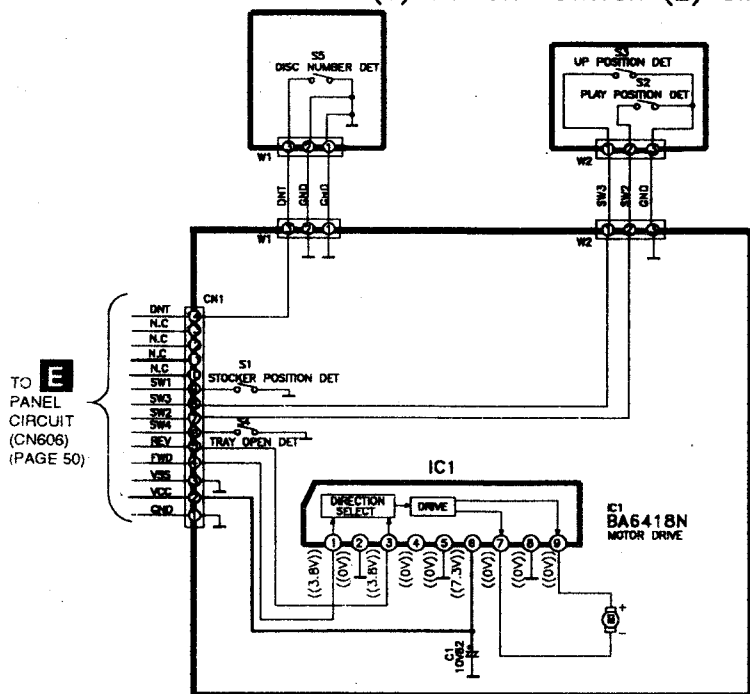
\*2 **DOLBY B NR**

**Panasonic**

CD Stereo System  
**SA-CH75**

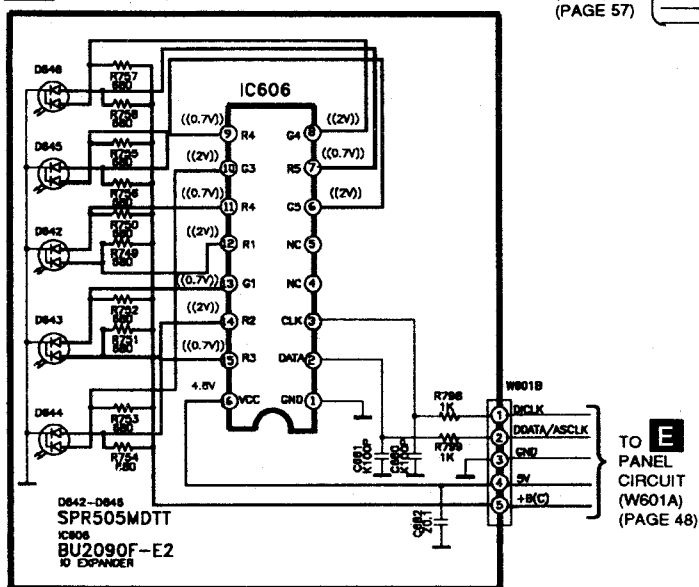


**R** DETECTING SWITCH (1) CIRCUIT      **S** DETECTING SWITCH (2) CIRCUIT

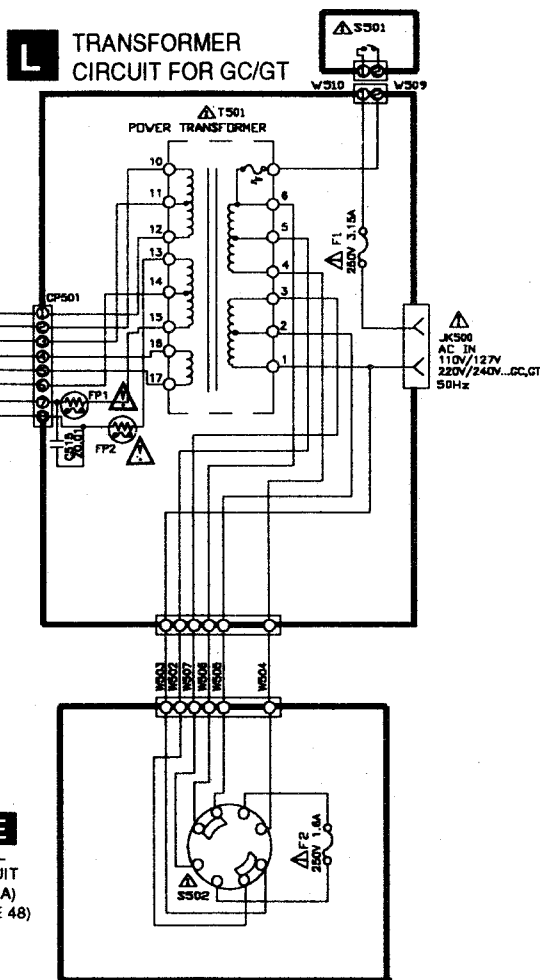


**O** LOADING MOTOR CIRCUIT

**F** LED CIRCUIT



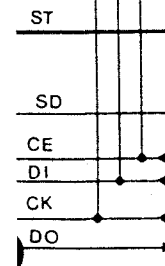
**U** POWER SWITCH CIRCUIT  
**L** TRANSFORMER CIRCUIT FOR GC/GT

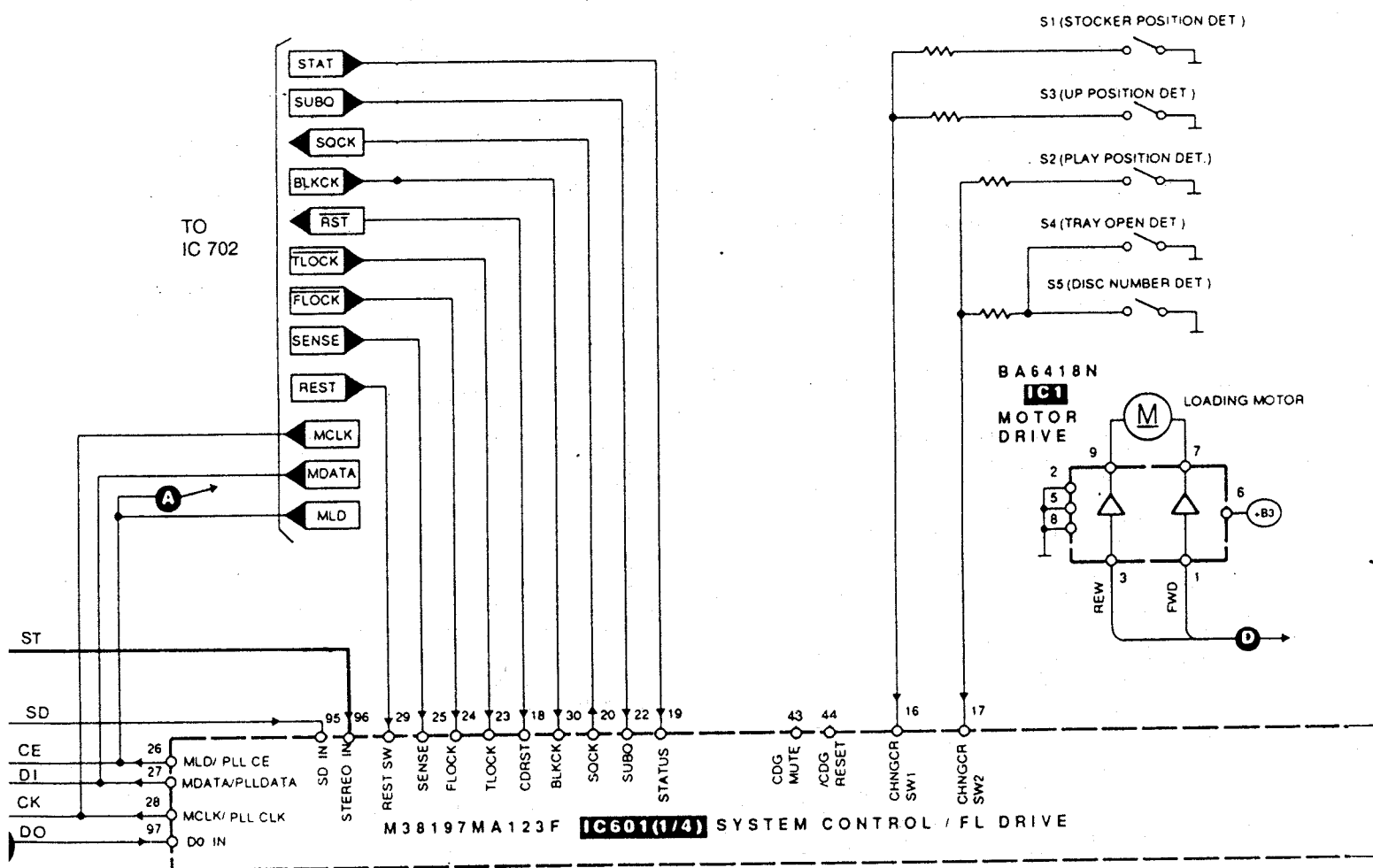


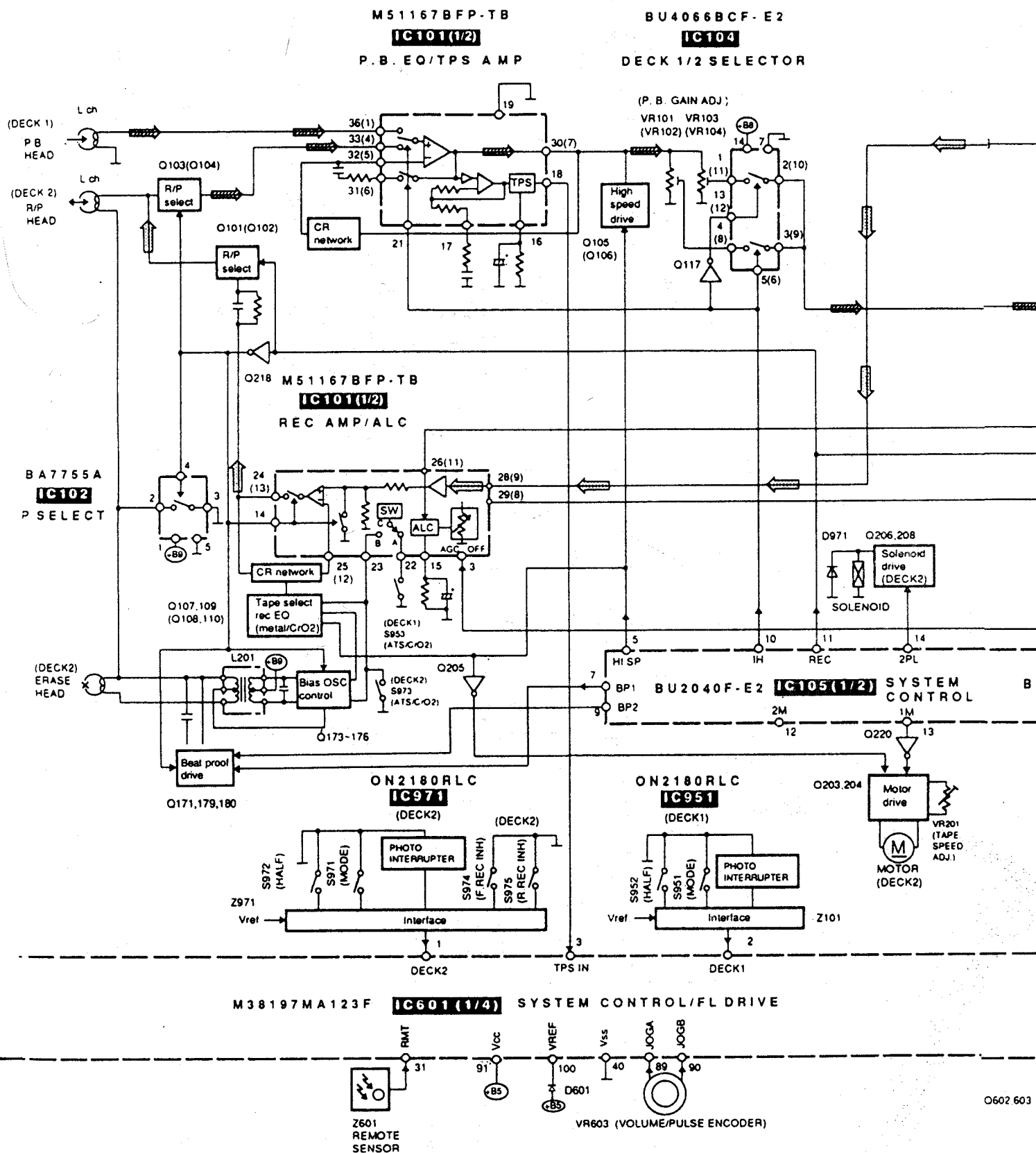
**P** VOLTAGE SELECTOR CIRCUIT

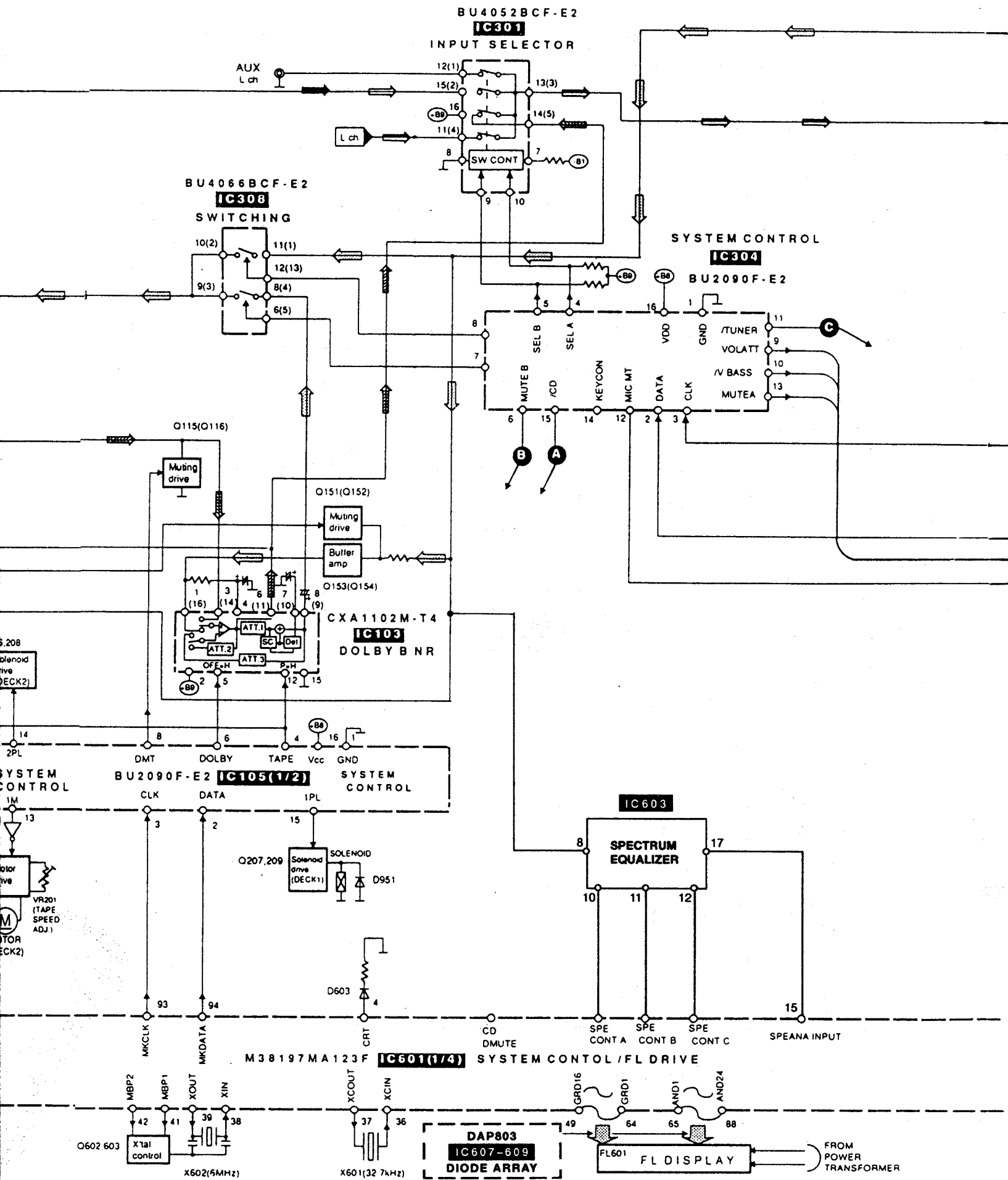
GC,GT

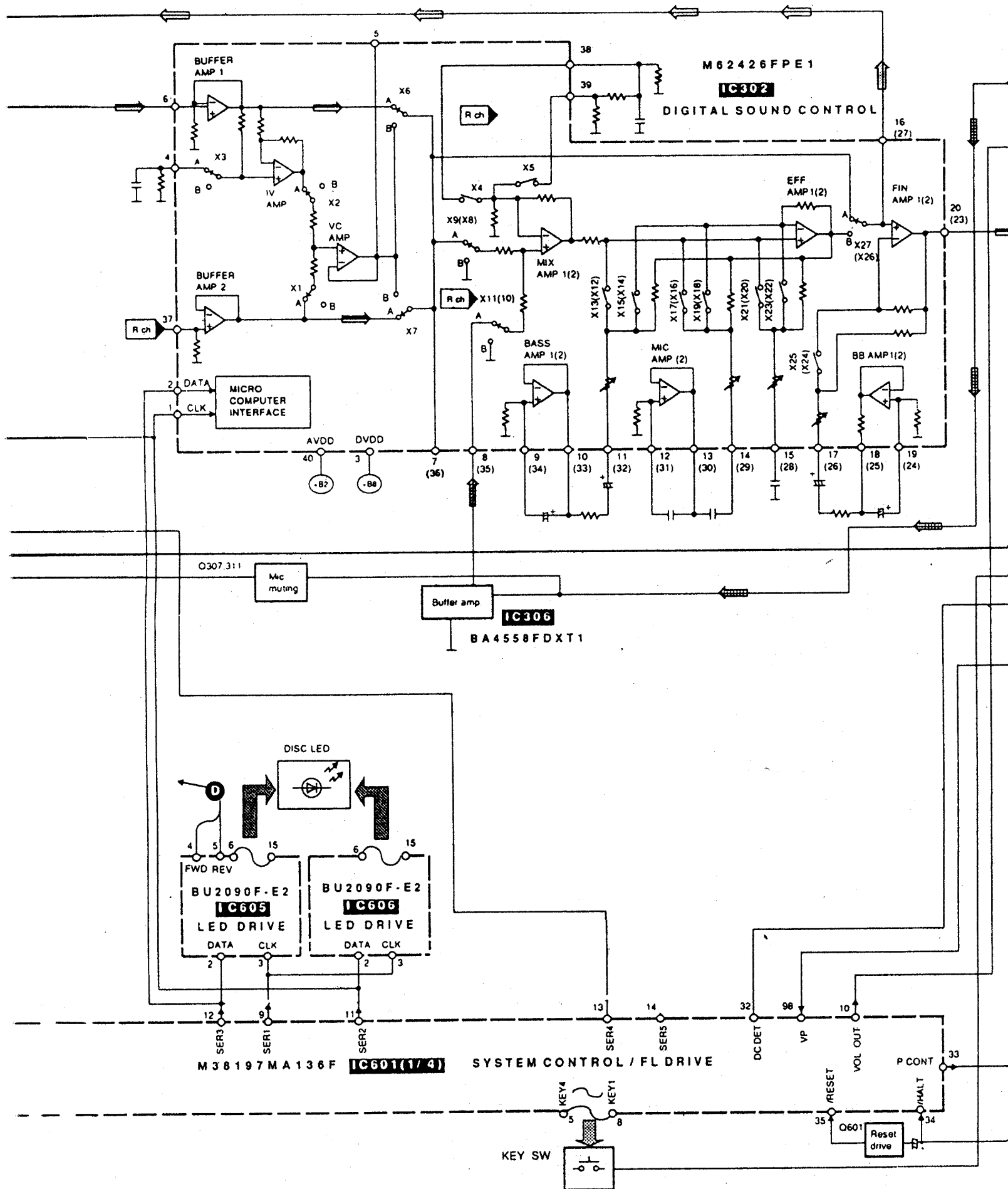


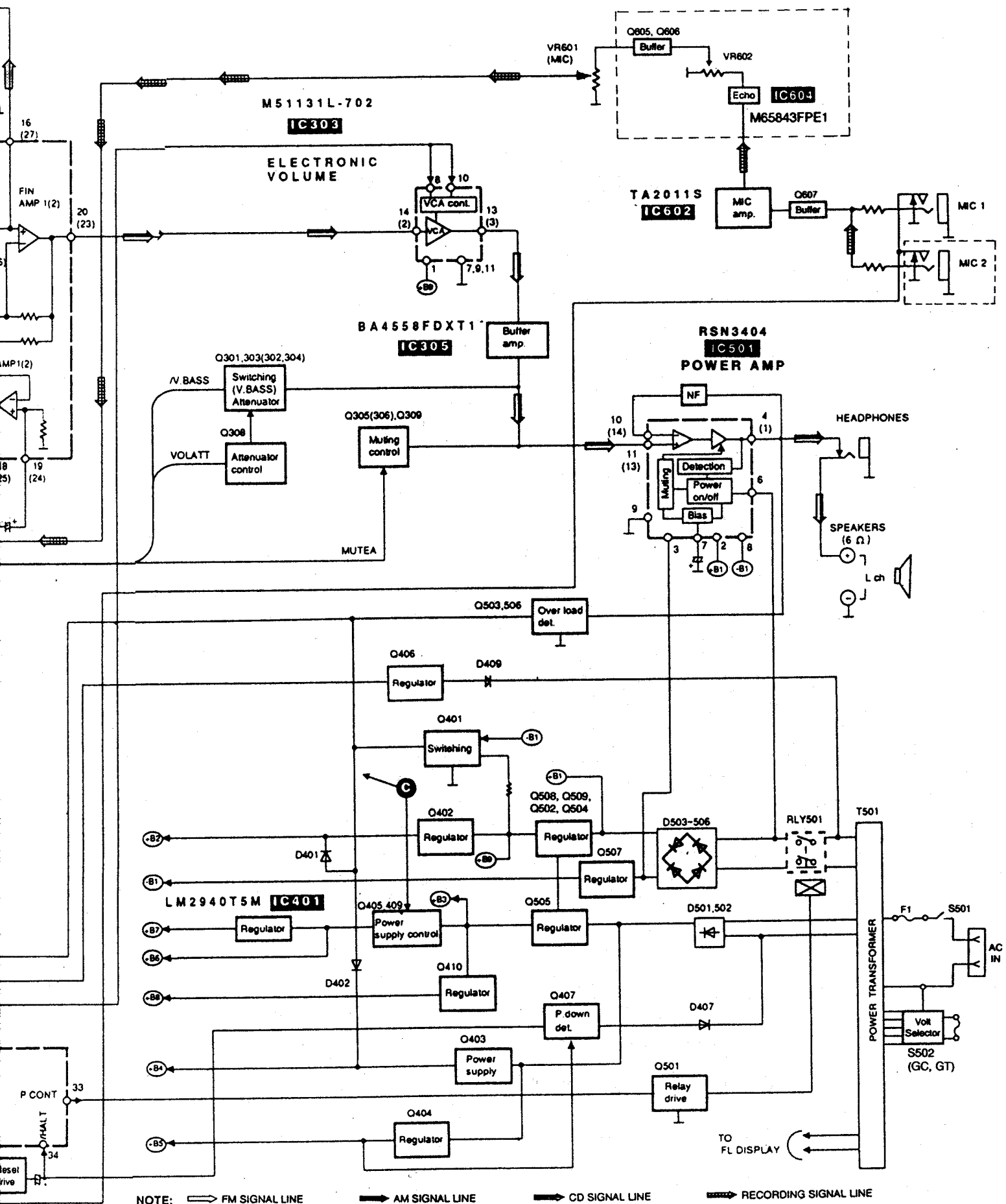












NOTE:   
 FM SIGNAL LINE   
 AM SIGNAL LINE   
 CD SIGNAL LINE   
 RECORDING SIGNAL LINE   
 FM OSC SIGNAL LINE   
 AM OSC SIGNAL LINE   
 CD G SIGNAL LINE   
 PLAYBACK SIGNAL LINE   
 MIC SIGNAL



The schematic diagram illustrates the internal structure of the W8038B timer chip. It features a vertical stack of six functional blocks, each with a 3-pin header at the top and a 4-pin header at the bottom. The blocks and their associated components are as follows:

- Block 1 (Top):** Labeled "S008 EASY EDIT". It contains a 30K resistor connected to the top header pin.
- Block 2:** Labeled "S005 REPEAT". It contains a 30K resistor connected to the top header pin.
- Block 3:** Labeled "S004 RANDOM". It contains a 30K resistor connected to the top header pin.
- Block 4:** Labeled "S003 REC TIMER". It contains a 30K resistor connected to the top header pin, and two resistors (R005 and R006) connected to the bottom header pins, with values of 2.7K and 2.7K respectively.
- Block 5:** Labeled "S002 PLAY TIMER". It contains a 30K resistor connected to the top header pin, and two resistors (R005 and R006) connected to the bottom header pins, with values of 2.2K and 2.2K respectively.
- Block 6 (Bottom):** Labeled "S001 POWER". It contains a 30K resistor connected to the top header pin, and two resistors (R004 and R005) connected to the bottom header pins, with values of 1.0K and 1.0K respectively.

The bottom header pins of the blocks are connected to a common bus, which is then connected to the W8038B chip. The chip is shown with its pinout: 16 pins on the left, 16 pins on the right, and 8 pins on the bottom.

5830  
DISC/DIAGNOSE

5837  
DISC 1

5836  
DISC 2

5835  
DISC 3

5834  
DISC 4

5833  
DISC 5

5832  
DISC CHECK

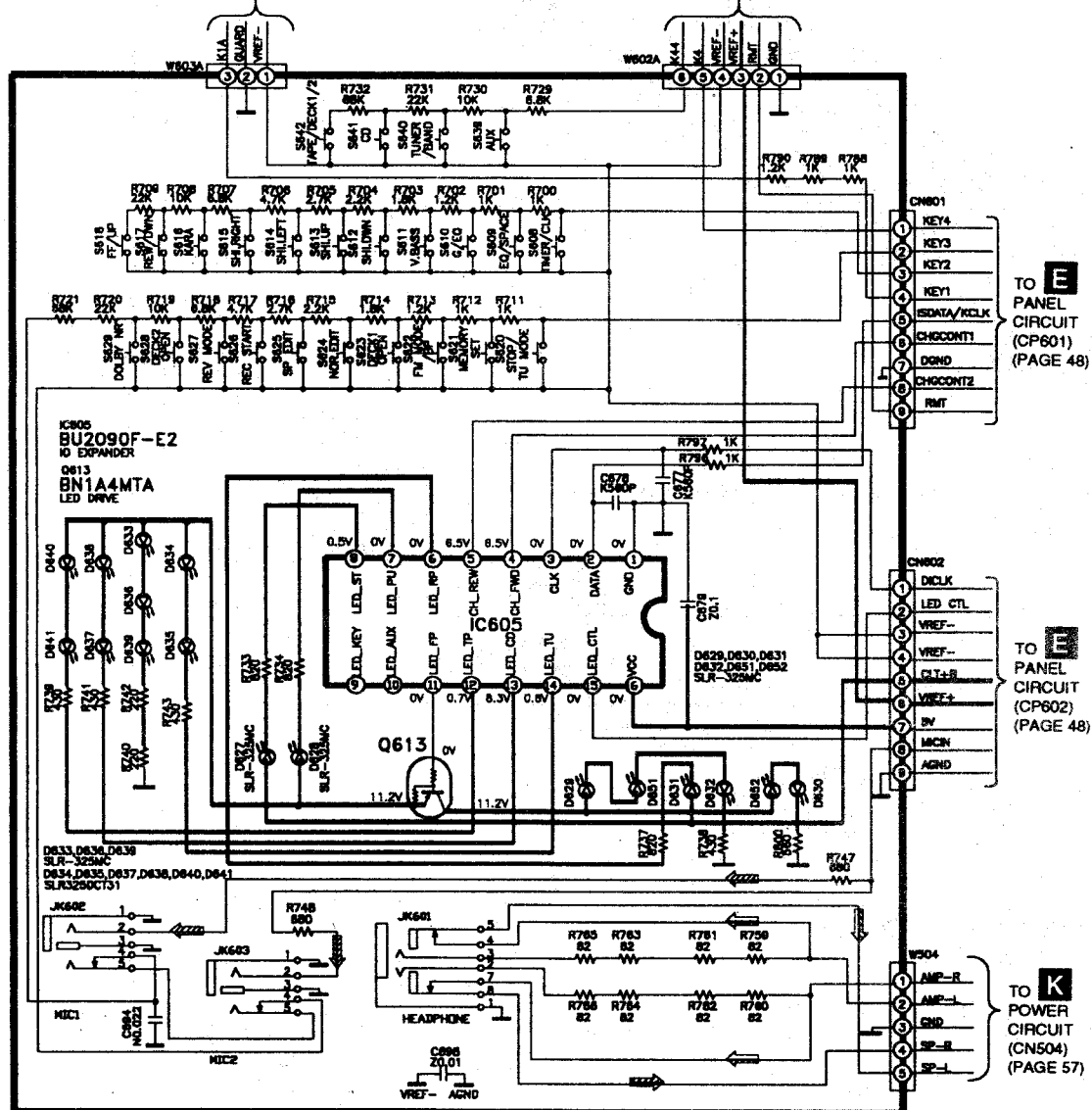
5831  
OPEN/CLOSE

7801

1 2 3

1K 1.2K 1.5K 1.8K 2.2K 2.7K 4.7K

W602B



## **G** OPERATION    CIRCUIT

No mark

(All schematic diagrams may be modified at any time with the development of new technology)

Note :

< for Servo Circuit > (Page 43)

• S701 : Reset switch

< for Voltage Selector Circuit and Power Switch Circuit > (Page 57,58)

• S501 : Power switch

• S502 : Voltage selector switch

< for Panel circuit, Sensor Circuit, Operation Circuit and Operation(Power) circuit >

• S601	:	Power switch	• S624	:	Tape Edit Normal switch
• S602	:	Timer Play switch	• S625	:	Tape Edit High switch
• S603	:	Timer Record switch	• S626	:	Record switch
• S604	:	Random switch	• S627	:	Reverse Mode switch
• S605	:	Repeat switch	• S628	:	Eject Deck 2 switch
• S606	:	Easy Edit switch	• S629	:	DOLBY NR switch
• S608	:	Clock/Timer switch	• S631	:	CD Open/Close switch
• S609	:	EQ. Space switch	• S632	:	CD Disc Check switch
• S610	:	Graphic EQ. switch	• S633	:	CD Disc 5 switch
• S611	:	V. Bass switch	• S634	:	CD Disc 4 switch
• S612	:	EQ. Space Down switch	• S635	:	CD Disc 3 switch
• S613	:	EQ. Space Up switch	• S636	:	CD Disc 2 switch
• S614	:	EQ. Space Left switch	• S637	:	CD Disc 1 switch
• S615	:	EQ. Space Right switch	• S638	:	Display/Demo switch
• S616	:	Karaoke switch	• S639	:	AUX switch
• S617	:	REW/Down switch	• S640	:	Tuner switch
• S618	:	FF/Up switch	• S641	:	CD switch
• S620	:	Stop/tuning mode switch	• S642	:	Deck 1/2 switch
• S621	:	Reverse Play/memory/set switch	• VR601	:	Mic control
• S622	:	Forward Play/FM mode BP switch	• VR602	:	Mic control
• S623	:	Deck 1 Eject switch	• VR603	:	Volume control

< for Deck circuit, Mechanism (Deck 1) circuit and Mechanism (Deck 2) circuit >

• S951	:	Deck 1 Mode detect switch.	• S975	:	Deck 2 Record detect switch.
• S952	:	Deck 1 Tape detect switch.	• VR101	:	Deck 1 Lch playback gain adjustment VR (DOLBY).
• S953	:	Deck 1 CrO <sub>2</sub> detect switch.	• VR102	:	Deck 1 Rch playback gain adjustment VR (DOLBY).
• S971	:	Deck 2 Mode detect switch.	• VR103	:	Deck 2 Lch playback gain adjustment VR (DOLBY).
• S972	:	Deck 2 Tape detect switch.	• VR104	:	Deck 2 Rch playback gain adjustment VR (DOLBY).
• S973	:	Deck 2 CrO <sub>2</sub> detect switch.	• VR201	:	Deck 2 Normal speed adjustment.
• S974	:	Deck 2 Record detect switch.			

< for Loading Motor circuit, Detecting Switch (1) Circuit and Detecting Switch (2) Circuit >

• S1, S4 : Leaf switch.  
• S2, S3, S5 : Mecha switch.

•Signal line

———— : +B line

----- : -B line

➡ : FM/AM signal line

➡ : Main signal line

➡ : Playback signal line

➡ : Record signal line

➡ : CD signal line

➡ : FM signal line

➡ : AM signal line

➡ : AM OSC signal line

➡ : FM OSC signal line

➡ : Aux signal line

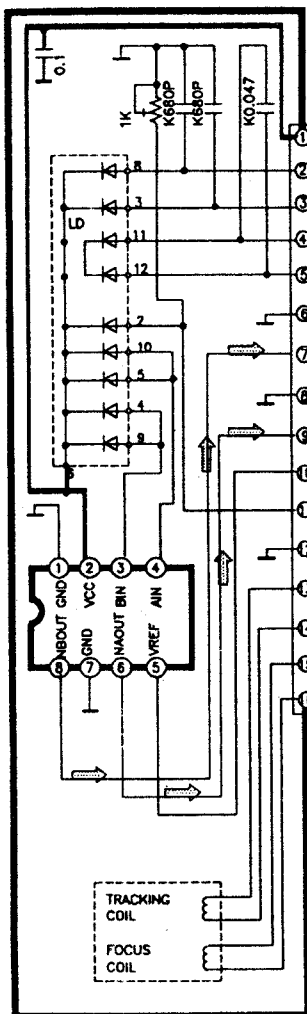
•The voltage value and waveforms are the reference voltage of this unit measured by DC electronic voltmeter (high impedance) and oscilloscope on the basis of chassis.

Accordingly, there may arise some error in voltage values and waveforms depending upon the internal impedance of the tester or the measuring unit.

No mark : Playback << >>.....Rec { } : Tuner (( )) : CD ( ) ..... AM < > ..... FM

# A SERVO CIRCUIT

## OPTICAL PICKUP



M702  
SPINDLE  
MOTOR

M701  
TRAVERSE  
MOTOR

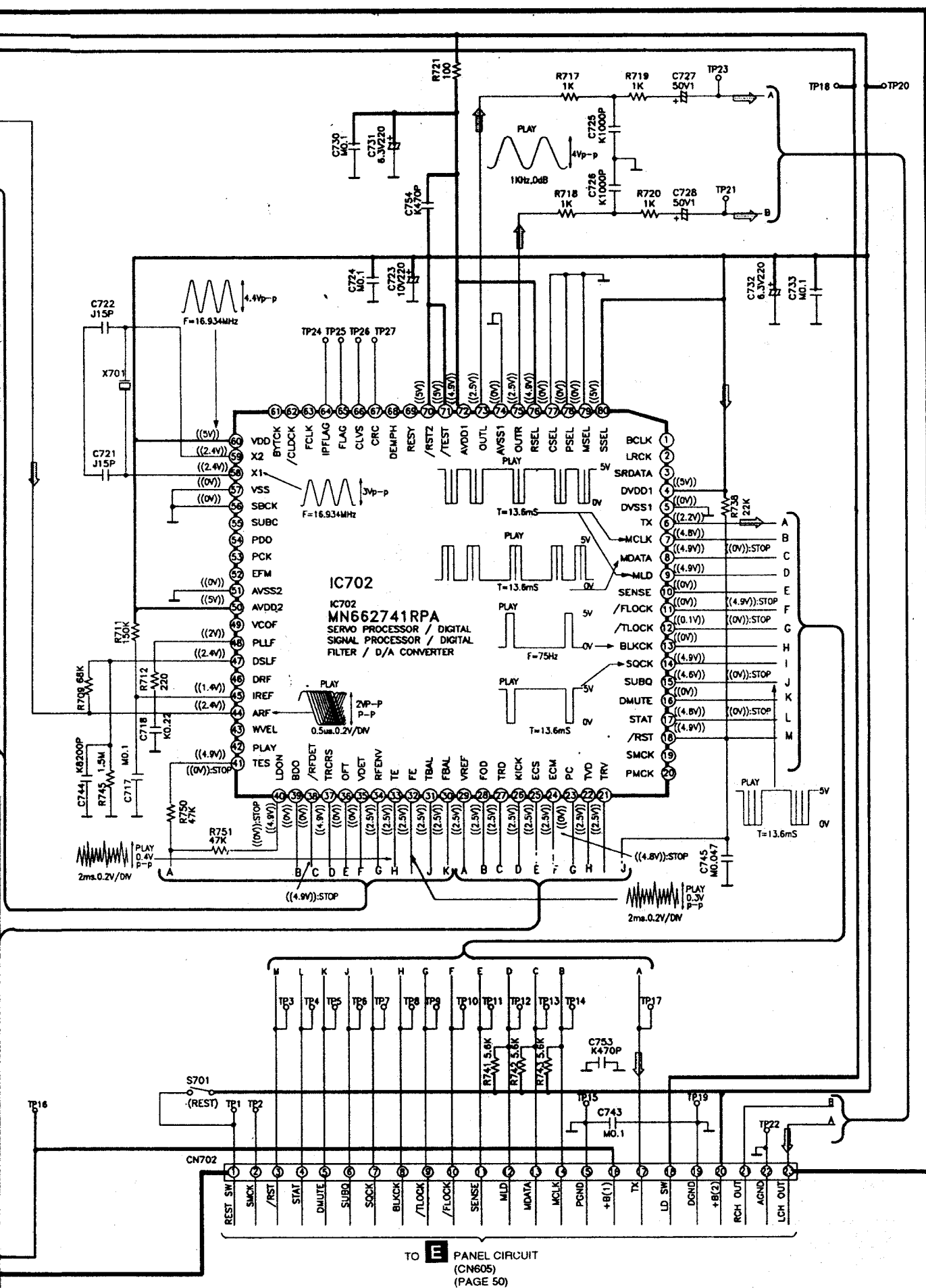
Q701  
25B709S  
LASER POWER  
DRIVE

Q701

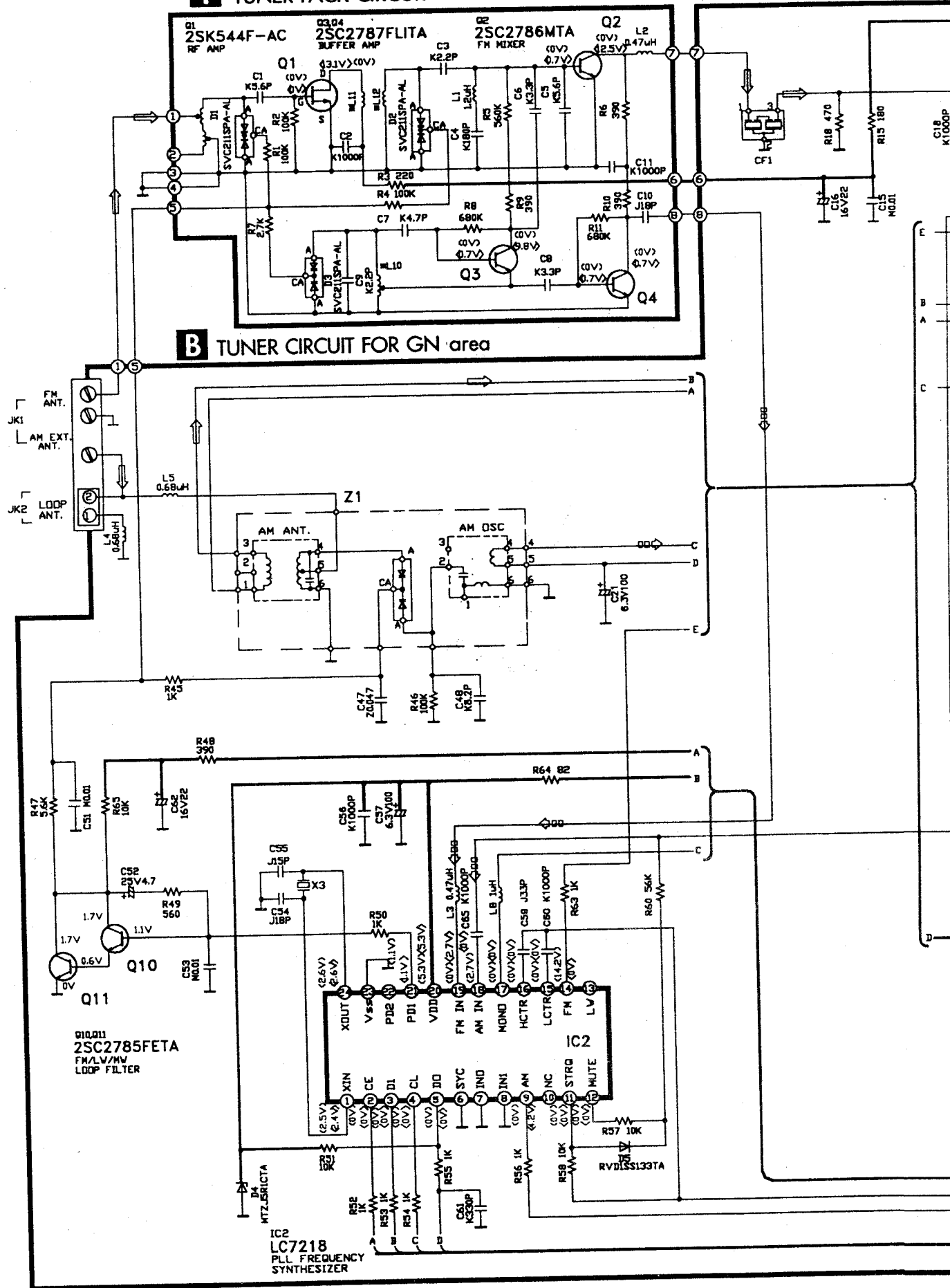
IC701  
AN8835SBE1  
SERVO AMP

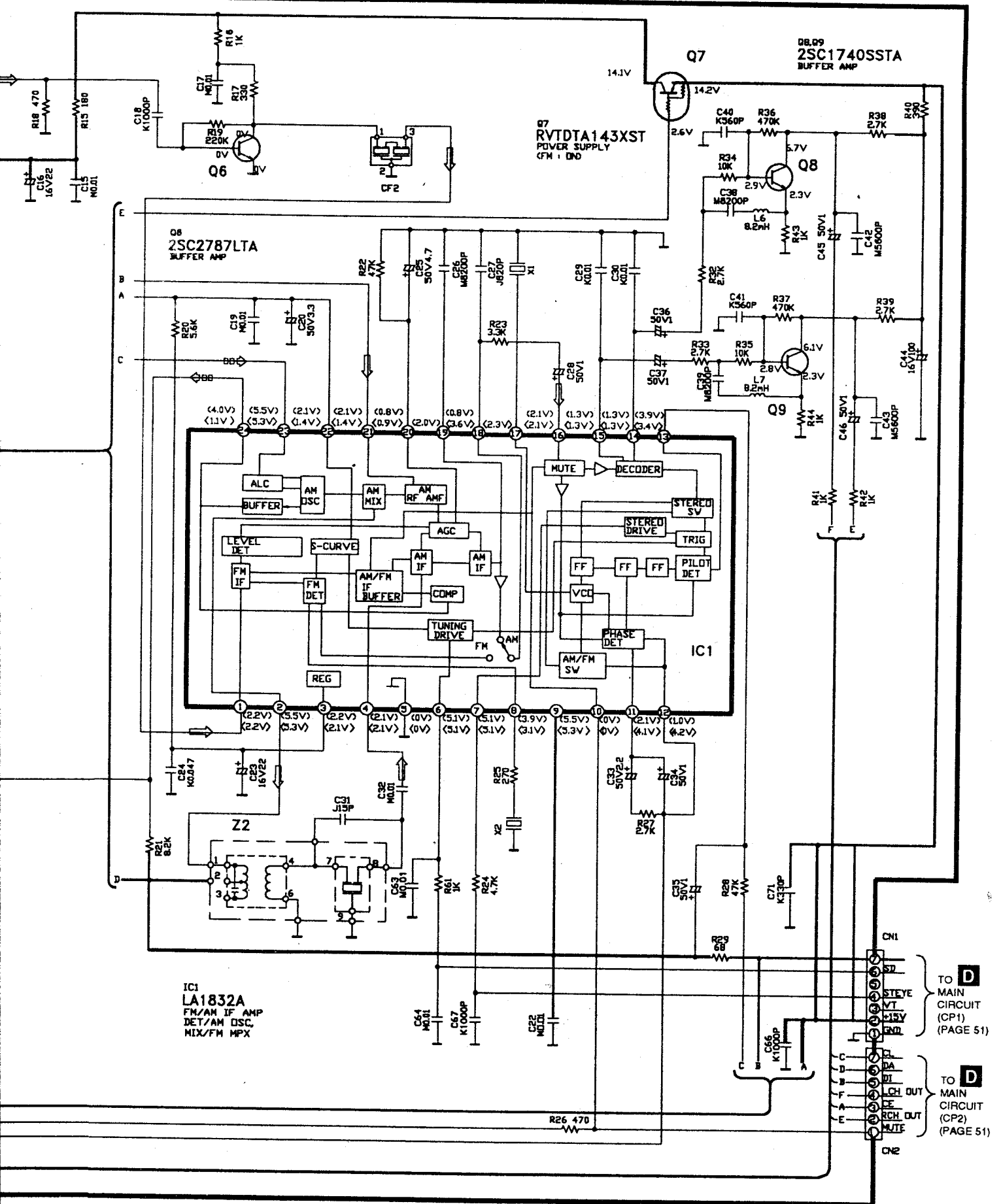
IC703

IC703  
AN8389SE1  
FOCUS COIL / TRACKING COIL /  
TRAVERSE MOTOR /  
SPINDLE MOTOR DRIVE

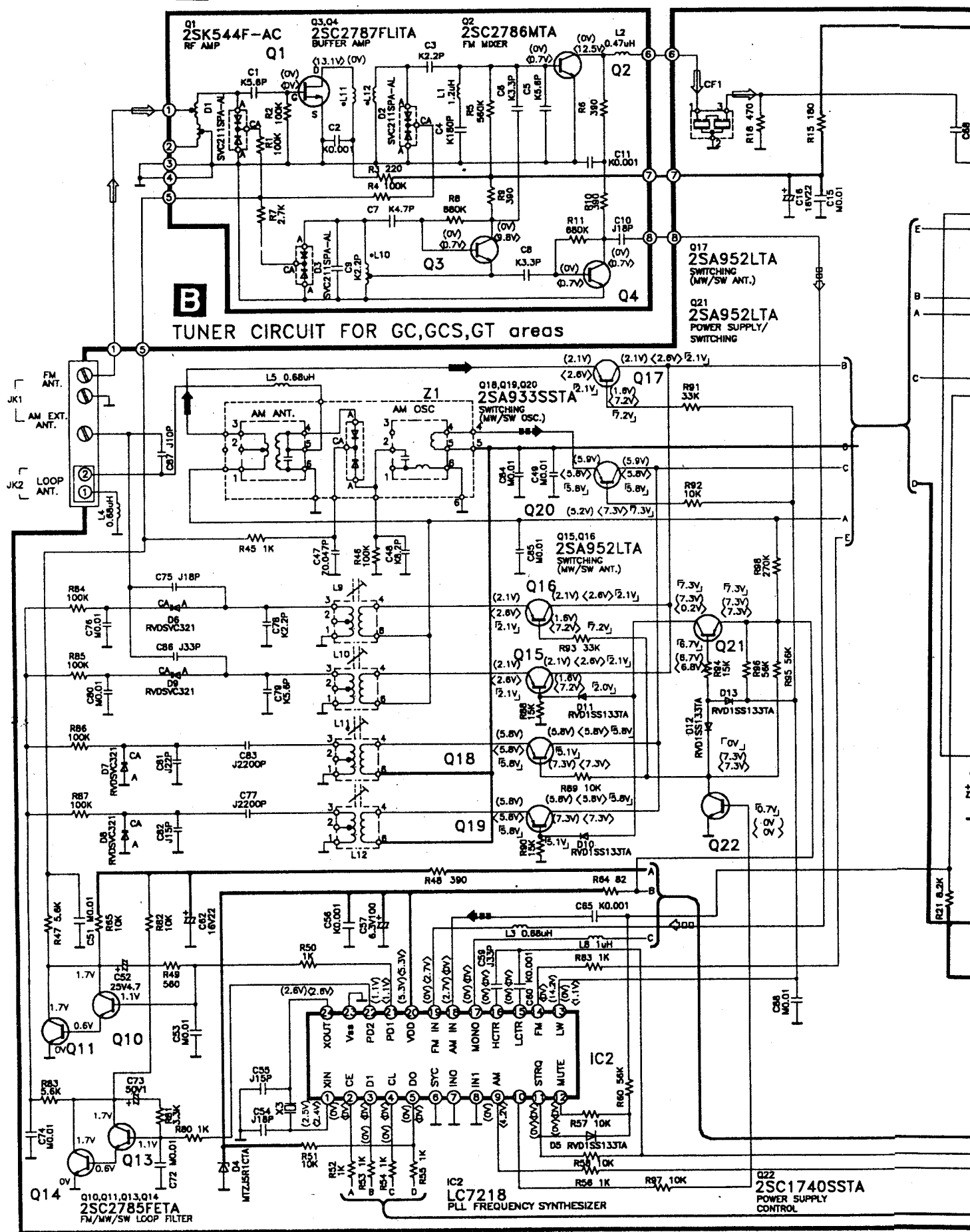


# T TUNER PACK CIRCUIT





# T TUNER PACK CIRCUIT



CVS  
M0.01

B

C

A

E

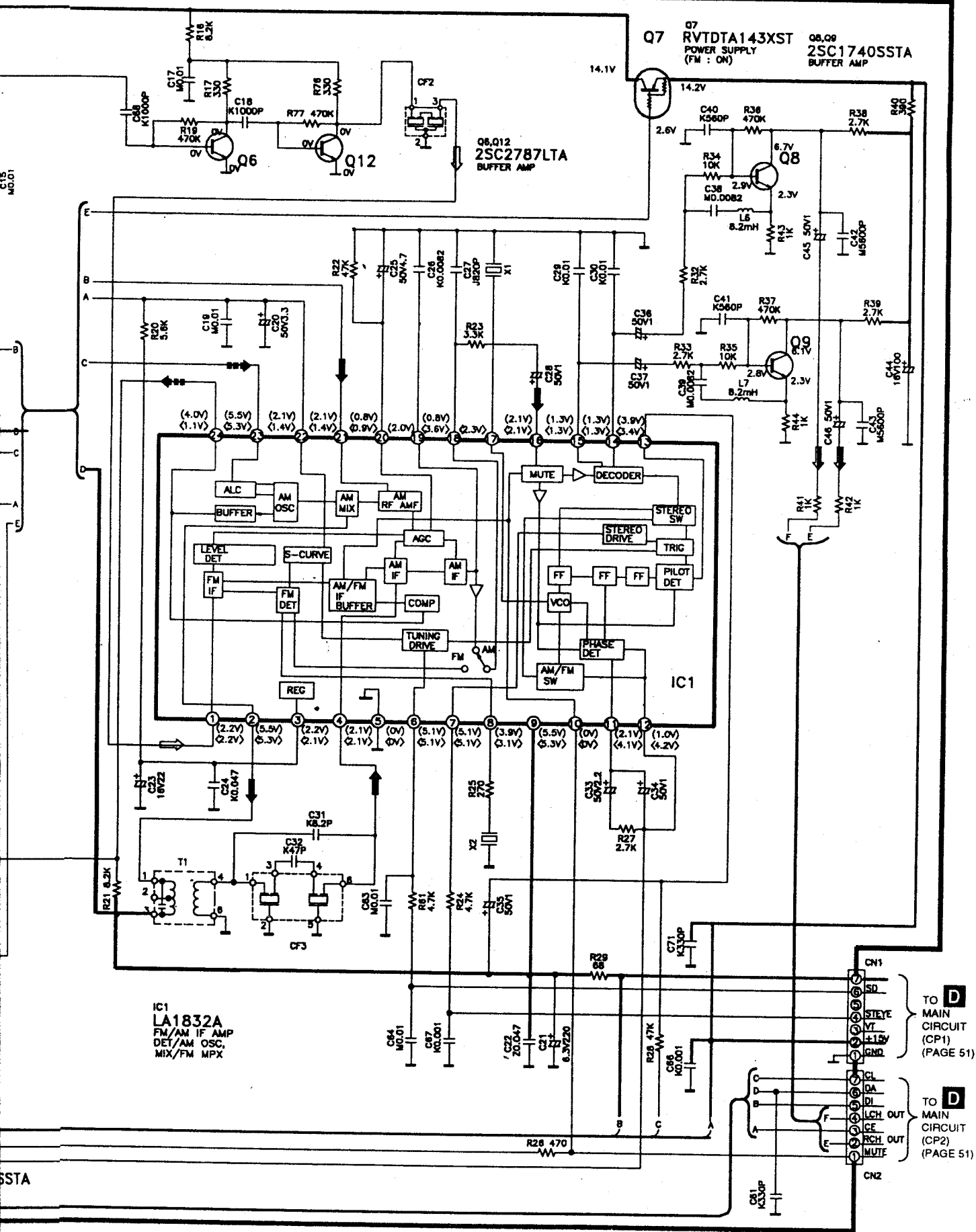
B

C

A

E

STA

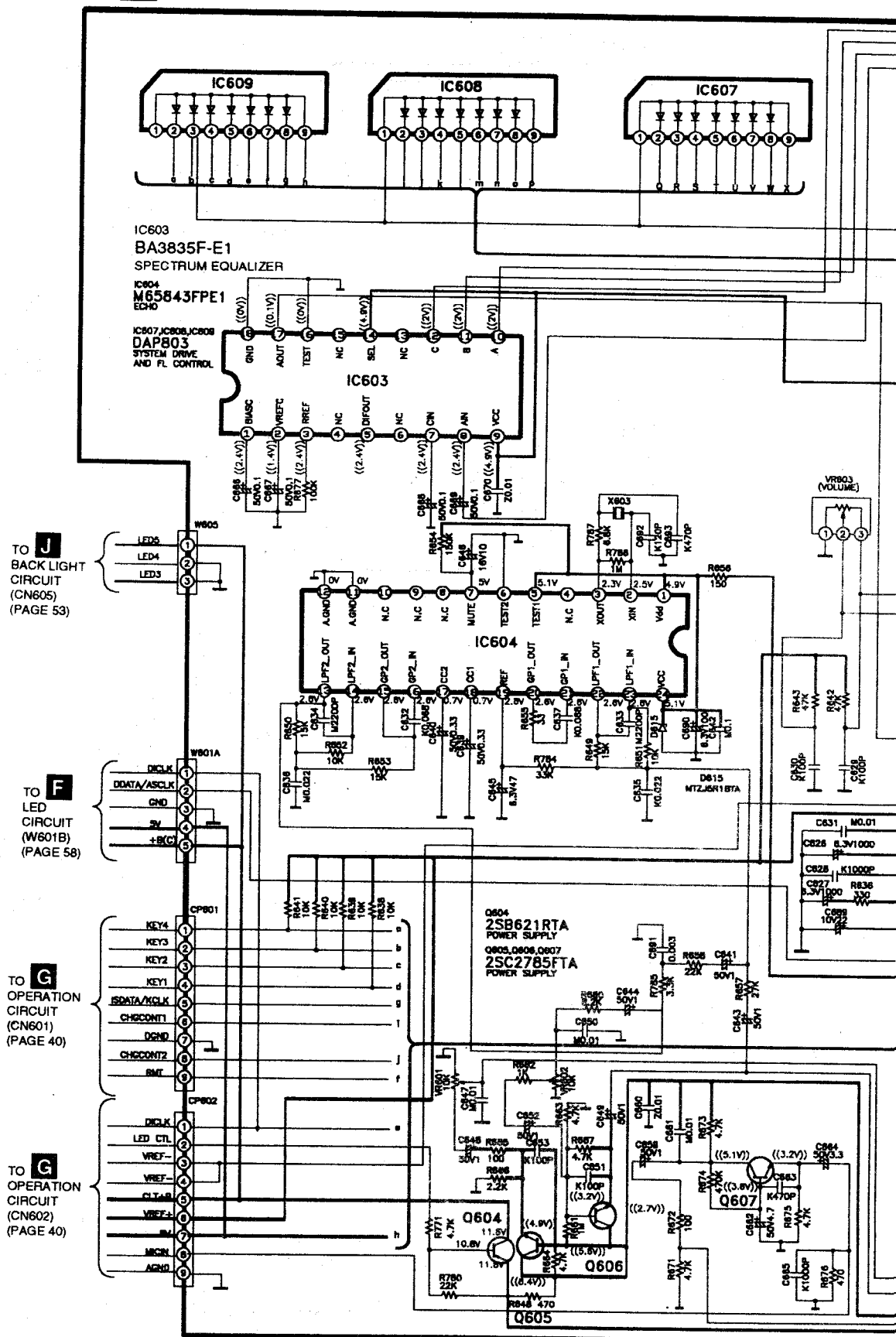


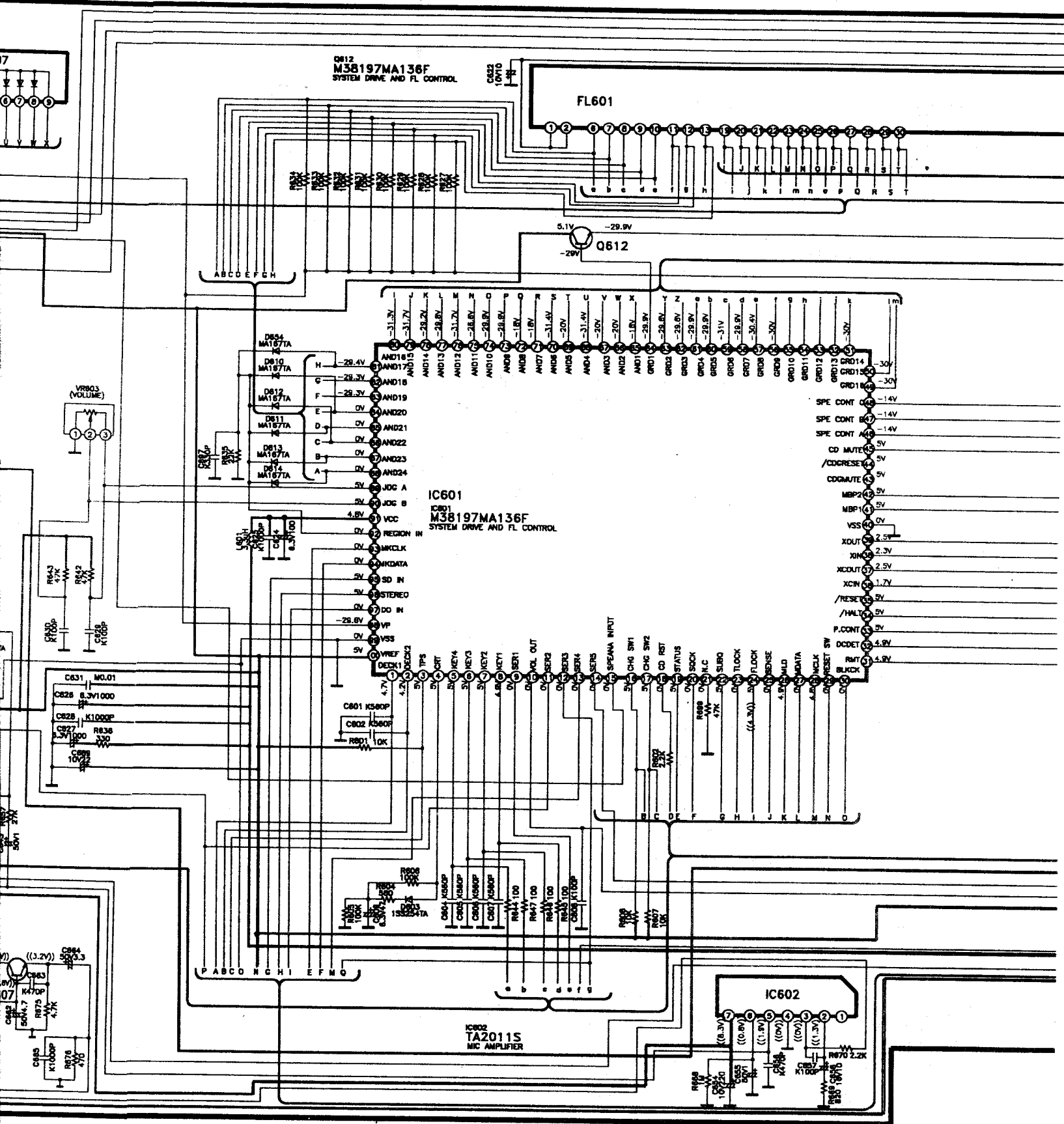
**D**  
TO MAIN  
CIRCUIT  
(CP1)  
(PAGE 51)

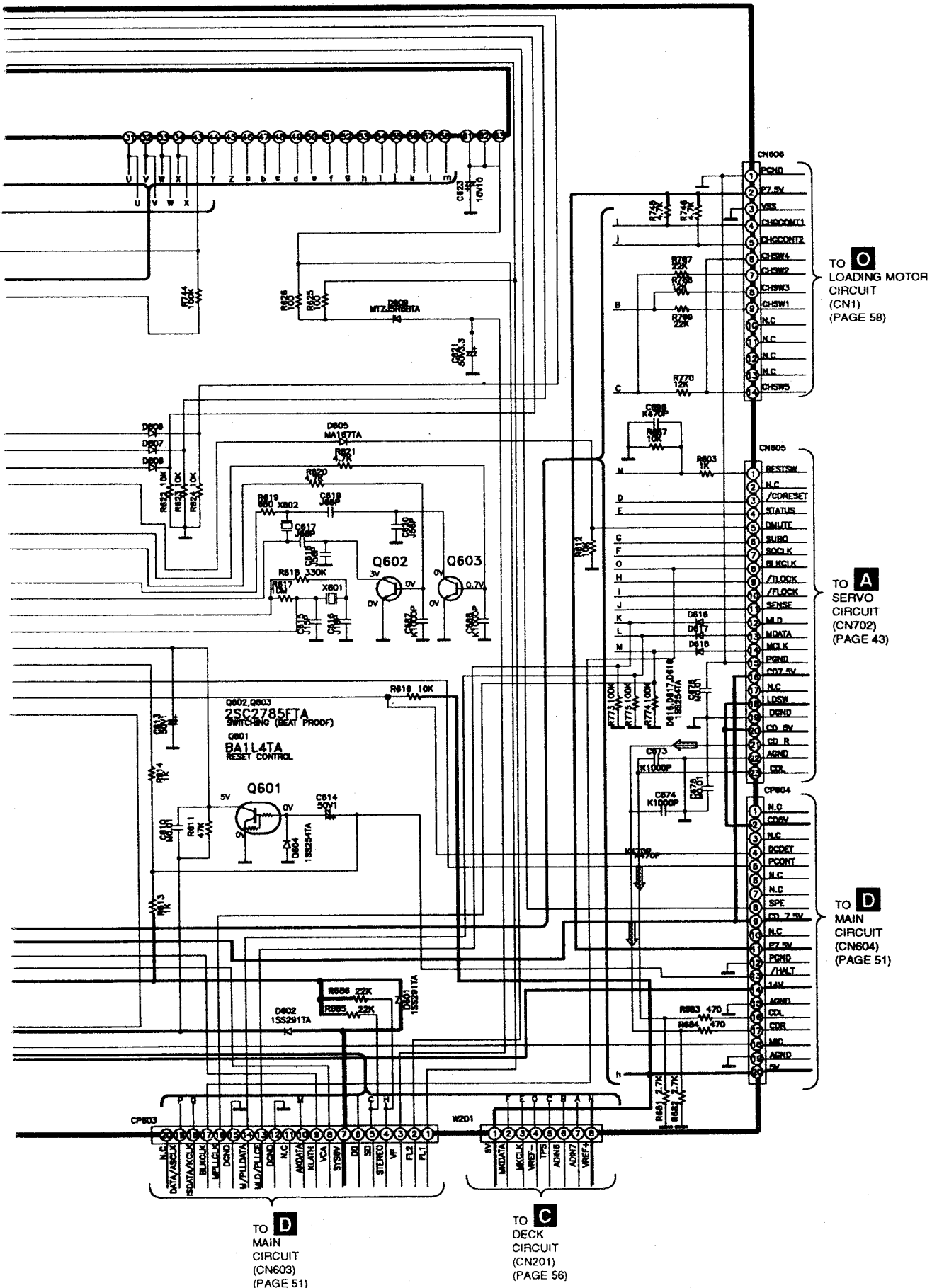
**D**  
TO MAIN  
CIRCUIT  
(CP2)  
(PAGE 51)



## E







TO **O**  
LOADING MOTOR  
CIRCUIT  
(CN1)  
(PAGE 58)

TO **B**  
TUNER  
CIRCUIT  
(CN1)  
(PAGE 45)...GN  
(PAGE 47)...GC,GT,G

TO **B**  
TUNER  
CIRCUIT  
(CN2)  
(PAGE 45)...GN  
(PAGE 47)...GC,GT,GCS

TO **C**  
DECK  
CIRCUIT  
(W100)  
(PAGE 45)

TO **A**  
SERVO  
CIRCUIT  
(CN702)  
(PAGE 43)

TO **E**  
PANEL  
CIRCUIT  
(CP604)  
(PAGE 45)

TO **D**  
MAIN  
CIRCUIT  
(CN604)  
(PAGE 51)

TO **E**  
PANEL  
CIRCUIT  
(CP603)  
(PAGE 50)

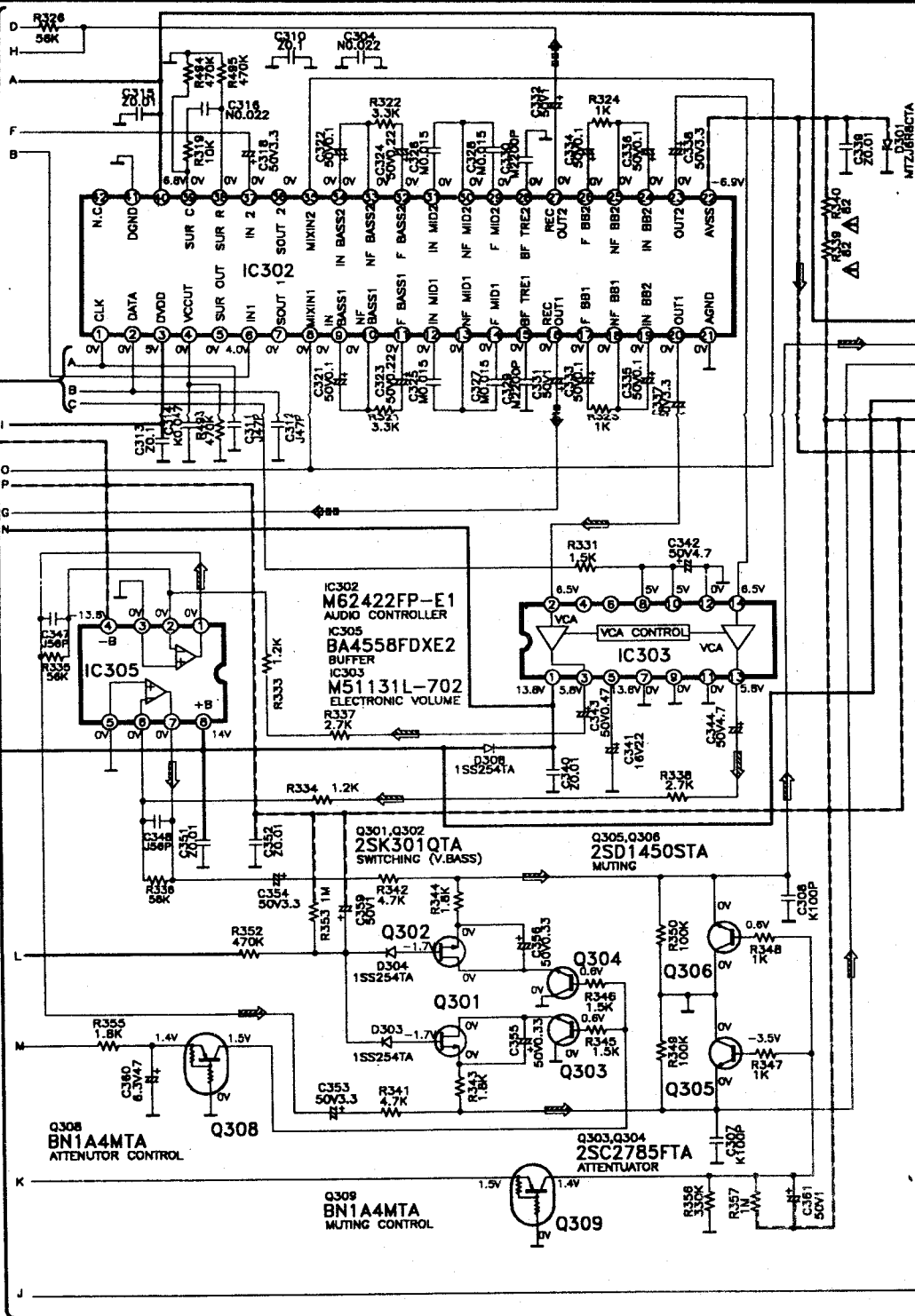
TO **D**  
MAIN  
CIRCUIT  
(CN603)  
(PAGE 51)

TO **C**  
DECK  
CIRCUIT  
(CN201)  
(PAGE 56)

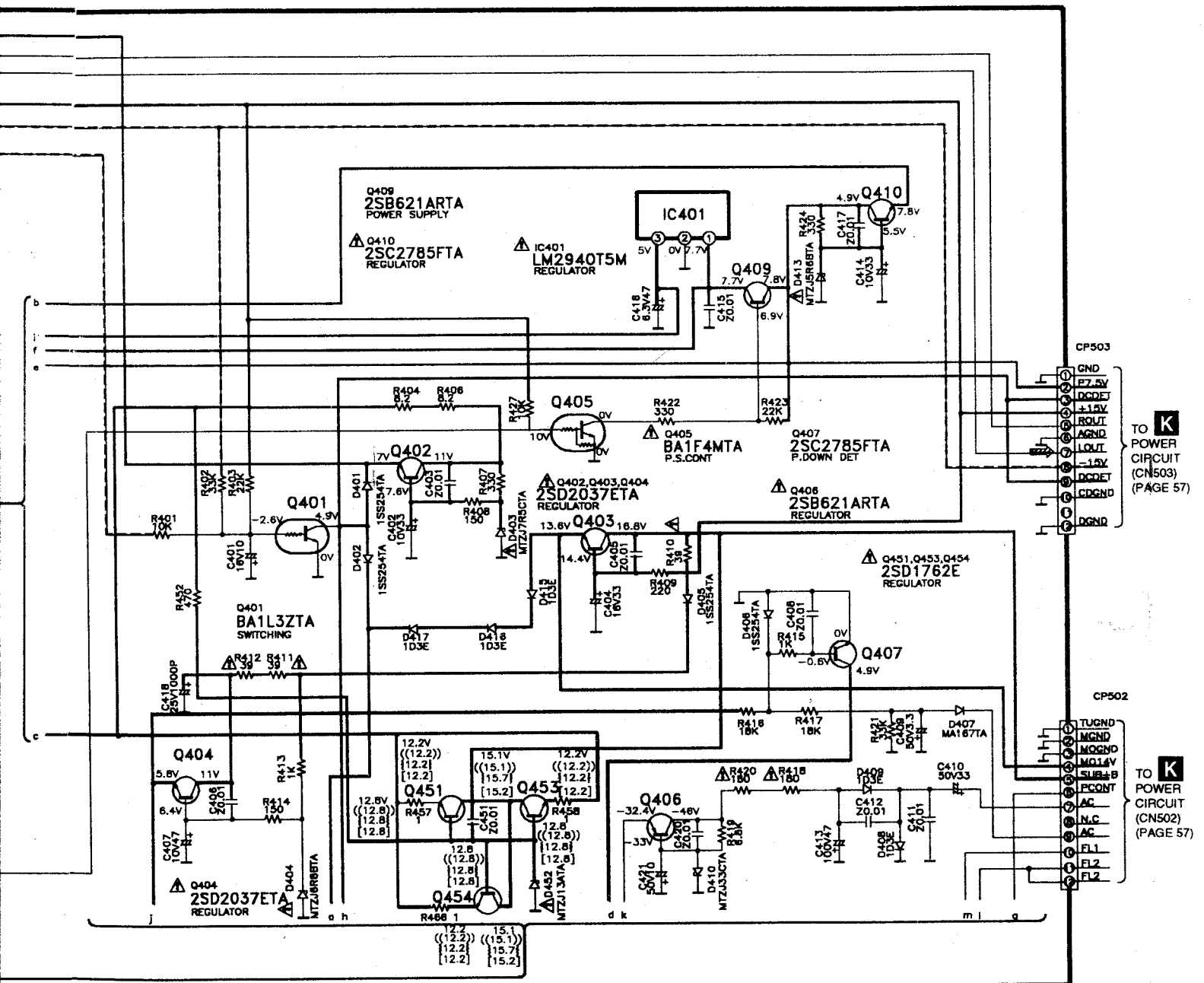
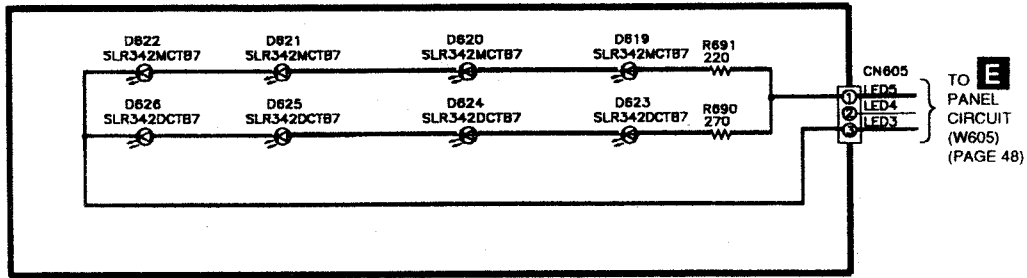
TO **E**  
PANEL  
CIRCUIT  
(W100)  
(PAGE 45)

## MAIN CIRCUIT

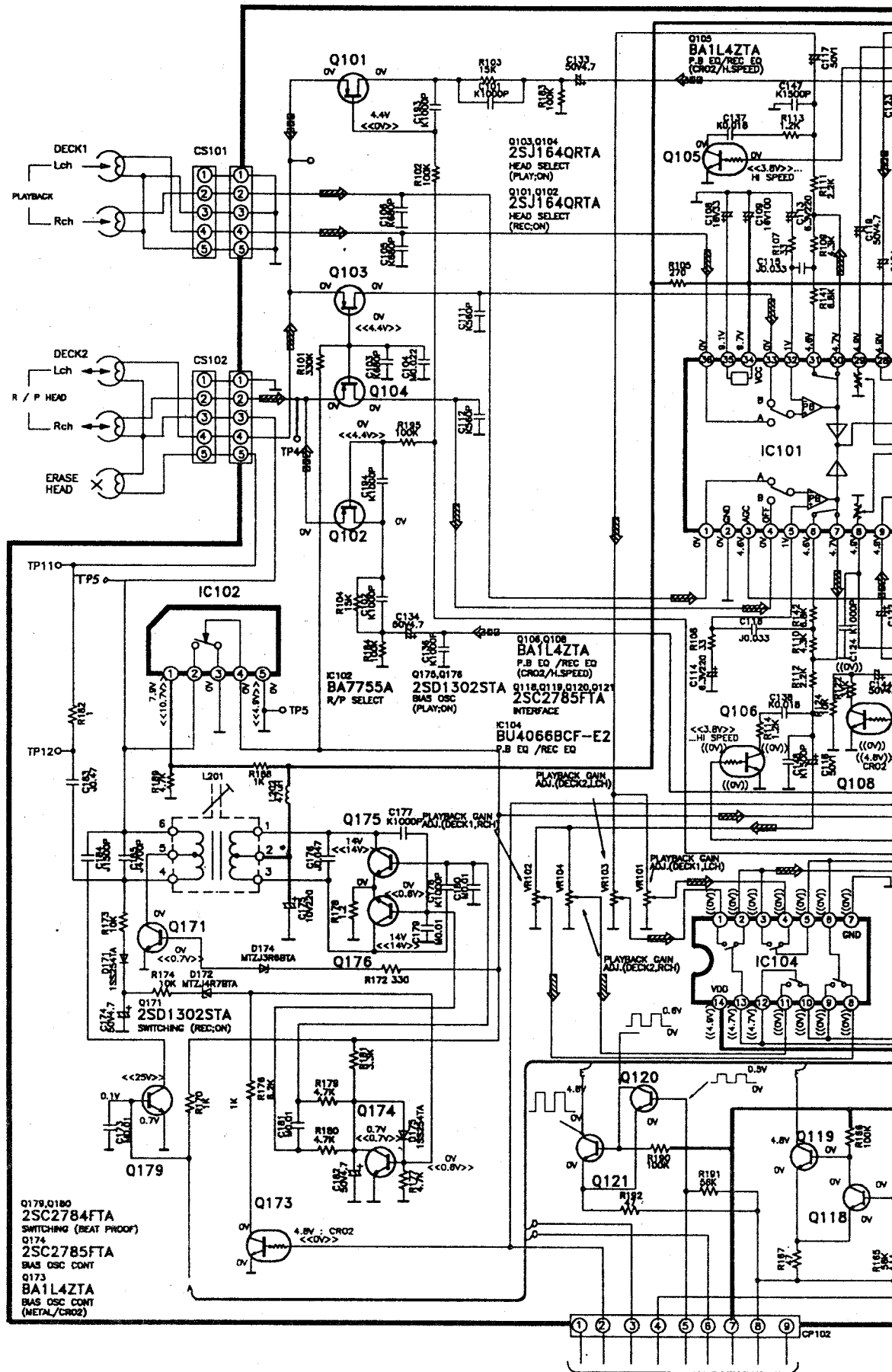




# **J** BACK LIGHT CIRCUIT



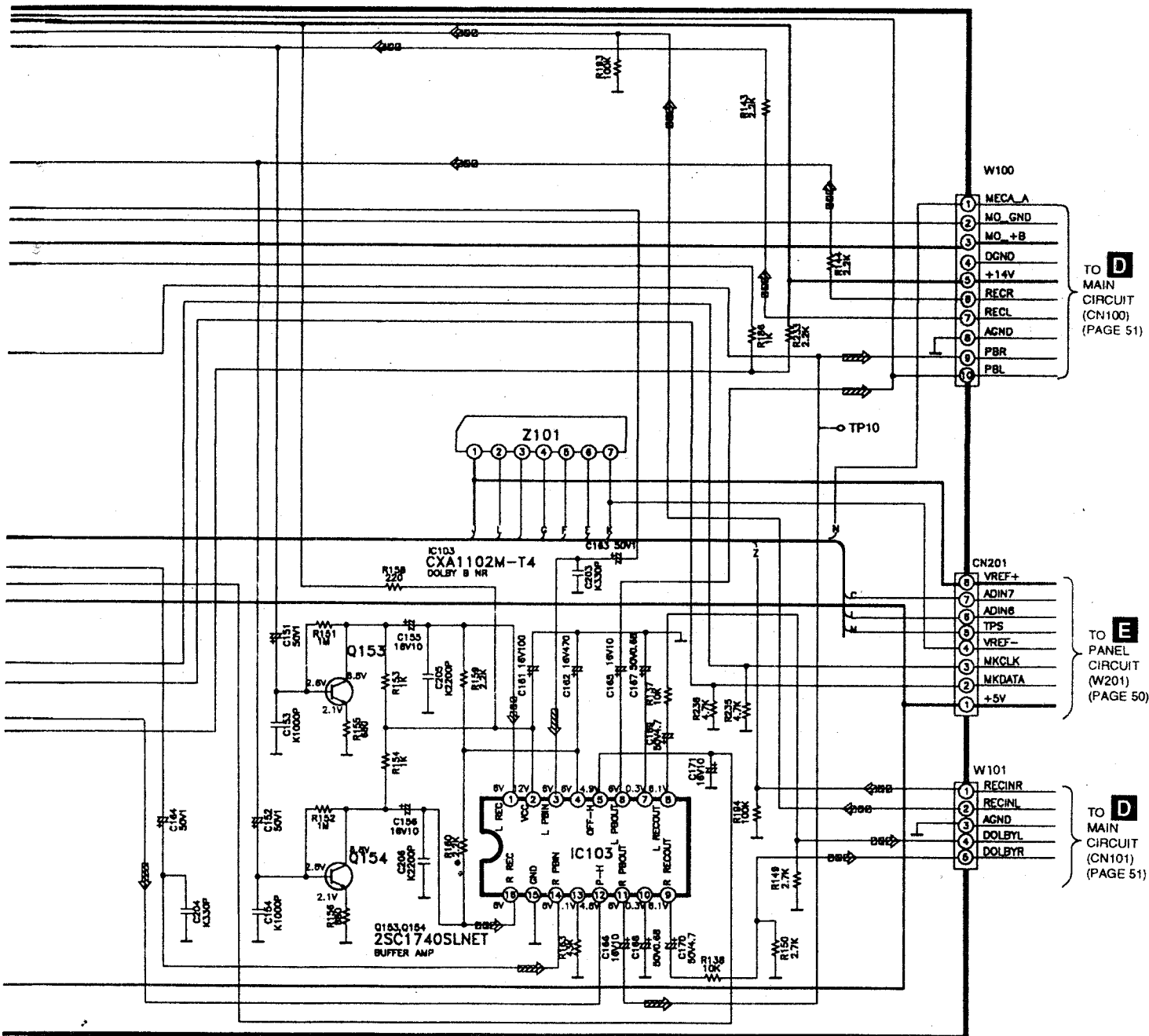
# C DECK CIRCUIT



TO **N**  
MECHANISM (DECK 2) CIRCUIT  
(CS971)  
(PAGE 56)



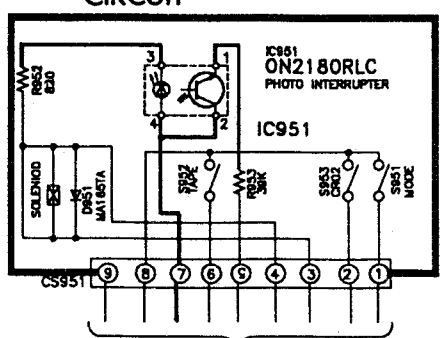




TO **D**  
MAIN  
CIRCUIT  
(CP503)  
(PAGE 53)

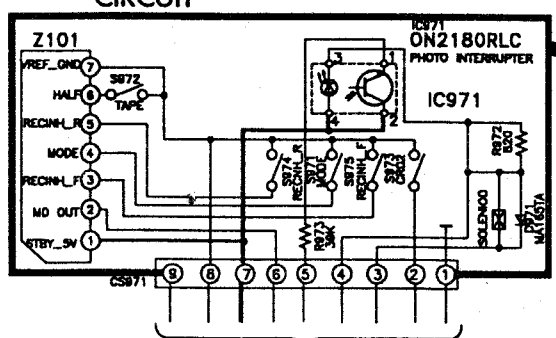
TO **D**  
MAIN  
CIRCUIT  
(CP502)  
(PAGE 53)

**M** MECHANISM (DECK 1)  
CIRCUIT



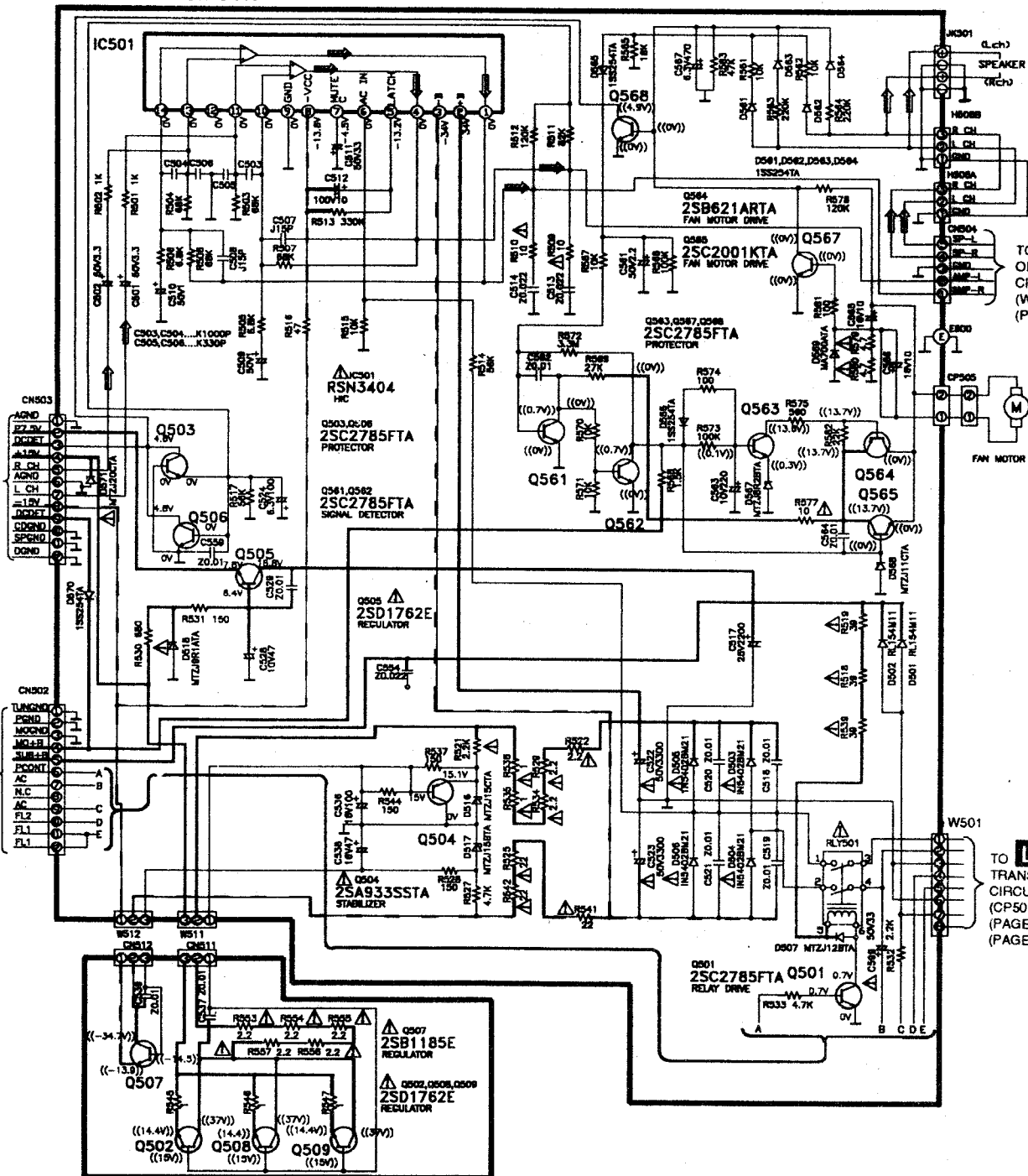
TO **C**  
DECK CIRCUIT  
(CP101)  
(PAGE 55)

**N** MECHANISM (DECK 2)  
CIRCUIT



TO **C**  
DECK CIRCUIT  
(CP102)  
(PAGE 54)

# **K** POWER CIRCUIT



## **Q** REGULATOR TRANSISTOR CIRCUIT

## **L** TRANSFORMER CIRCUIT FOR GN/GCS

## **U** POWER SWITCH CIRCUIT

