Introduction and Precautions

The Yaesu System 600 is carefully designed to allow the knowledgeable operator to make nearly all adjustments required for various station conditions, modes and operator preferences simply from the controls on the front and rear panels, without opening the case of the transceiver. These adjustments, plus certain internal settings, are described in the Yaesu System 600 Operating Manual.

The following procedures cover the sometimes critical and tedious adjustments that are not normally required once the transceiver has left the factory. However, if damage occurs and some parts are subsequently replaced, realignment may be required. If a sudden problem occurs during normal operation, it is likely due to component failure; realignment should not be done until after the faulty component has been replaced.

We recommend that servicing be performed only by authorized Yaesu service technicians, experienced with the circuitry and fully equipped for repair and alignment. So if a fault is suspected, you should contact the selling dealer for instructions regarding repair. Authorized Yaesu service technicians have the latest modification information, and realign all circuits and make complete performance checks to ensure compliance with factory specifications after replacing faulty components.

Those who do undertake any of the following alignments are cautioned to proceed at their own risk. Problems caused by unauthorized attempts at realignment are not covered by the warranty policy. Also, Yaesu reserves the right to change circuits and alignment procedures in the interest of improved performance, without notifying owners.

Under no circumstances should any alignment be attempted unless the normal function and operation of the transceiver are clearly understood, the cause of the malfunction has been clearly pinpointed and any faulty components replaced, and the need for realignment determined to be absolutely necessary.

The following test equipment (and thorough familiarity with its correct use) is necessary for complete realignment. Correction of problems caused by misalignment resulting from use of improper test equipment is not covered under the warranty policy. While most steps do not require all of the equipment listed, the interactions of some adjustments may require that more complex adjustments be performed afterwards. Do not attempt to perform only a single step unless it is clearly isolated electrically from all other steps. Rather, have all test equipment ready before beginning, and follow all of the steps in a section in the order they are presented.

Required Test Equipment

- ○ Digital DC Voltmeter (high-Z, >1 M ohm/V)
- ○ DC Ammeter
- ○ AC Voltmeter
- ○ RF Voltmeter
- ○ RF Standard Signal Generator w/calibrated output and dB scale, 0 dBμ = 0.5μV
- ○ AF Signal Generator with calibrated output
- ○ AF Voltmeter
- ○ Frequency Counter
- ○ 50-ohm Dummy Load (150 - 250 watts)
- ○ 16.6-ohm Dummy Load (150 watts)
- ○ In-Line Wattmeter (150 - 250 watts, 50-ohm)
- ○ RF Attenuator (150 watts, 40-dB) or Coupler

Alignment

Alignment Preparation & Precautions

A 50-ohm dummy load and in-line wattmeter must be connected to the antenna jack in all procedures that call for transmission, except where specified otherwise. Correct alignment is not possible with an antenna.

Except where specified otherwise, the transceiver should be tuned to 14.200 MHz,

USB mode, and these controls set as indicated:

○ SQL & VOL fully ccw (minimum)

○ CLARIFIER to 12 o'clock

After completing one step, read the following step to determine whether the same test equipment will be required. If not, remove the test equipment (except the dummy load and wattmeter, if connected) before proceeding.

Correct alignment requires that the ambient temperature be the same as that of the transceiver and test equipment, and that this temperature be held constant between 20

and 30 ºC (68 - 86 ºF). If the transceiver is brought into the shop from hot or cold air it

should be allowed some time for thermal equalization with the environment before alignment.

Alignments must only be made with oscillator shields and circuit boards firmly affixed in place. Only one extender board should be installed at a time for access to the board being aligned. Also, the test equipment must be thoroughly warmed up before beginning.

Alignment values assume a DC supply voltage of 13.5V DC.

Note: Signal levels in dB referred to in alignment are based on 0 dBμ = 0.5 μV.

Local Unit

Refer to the Local Unit Alignment Diagram on page 2-11. To prevent PLL unlock from interfering with the Local Unit alignment process, connect TP2004 to ground. Remember to disconnect TP2004 when finished with the alignment!

Reference Oscillator

❒ Connect the frequency counter to JP2001, and the voltmeter to TP2005.

❒ If the counter frequency differs by more than 3 Hz from 5.242880 MHz, adjust the TCXO-4 trimmer (if the TCXO is installed) or TC2701.

❒ Confirm a level of at least 900 mVrms on the RF voltmeter.

DDS 1 Check

❒ Tune the transceiver to 7.045.1 MHz, and select LSB mode.

❒ Connect the voltmeter to Q2036, pin 2, and confirm that the RF voltmeter indicates at least 110 mVrms.

DDS 2 Check

❒ Tune the transceiver to 7.045.1 MHz, and select LSB mode.

❒ Connect the RF voltmeter to Q2044, pin 2, and confirm an indication of at least 65 mVrms.

PLL BPF

❒ Connect the RF voltmeter to TP2001.

❒ Tune the transceiver to 7.005.6 MHz, and select LSB mode.

❒ Adjust T2001, T2002 and T2003 alternately several times for peak indicated RF voltage (at least 190 mVrms).

Carrier Level Adjust & Frequency Check

❒ Connect RF voltmeter to TP2015.

❒ Alternately adjust T2004 and T2005 several times for peak indicated RF voltage (at least 320 mVrms).

❒ Replace the voltmeter with a frequency counter.

❒ Select the indicated mode and frequency, according to the following table. Press the PTT-button to transmit.

Frequency	Mode
8.213.50 MHz	J3E (LSB)
8.216.50 MHz	J3E (USB)
8.215.80 MHz	A1A
8.215.80 MHz	A3E
8.213.00 MHz	J2B (LSB)
8.217.00 MHz	J2B (USB)

❒ Confirm that the carrier frequency is within ± 20 Hz of the displayed counter frequency.

PLL Main Loop VCO

❒ Connect the DC voltmeter to TP2013, then adjust and check the VCO voltage according to Table 1 as shown below.

Table 1. VCO Voltage Alignment

Tune to:	Adjust:	for indicated voltage of:
30.000.0 MHz	L2027	7.9 to 8.1 V
21.500.0 MHz	confirm	2.0 to 3.0 V
21.499.9 MHz	L2019	7.9 to 8.1 V
14.500.0 MHz	confirm	2.0 to 3.0 V
14.499.9 MHz	L2017	7.9 to 8.1 V
7.500.0 MHz	confirm	2.0 to 3.0 V
7.499.9 MHz	L2011	7.9 to 8.1 V
0.100.0 MHz	confirm	2.0 to 3.0 V

❒ Tune the transceiver to 30.000.0 MHz, USB mode, connect the voltmeter to L2005, and confirm a level of more than 60 mVrms.

1st L.O. Output Level Check

❒ Connect the RF voltmeter to TP2014.

❒ Tune the transceiver to 30.000.0 MHz, and select USB mode. Confirm a level of at least 600 mVrms.

Receiver Circuits

Refer to the Main Unit Alignment Diagram on page 2-12 for location of components.

IF Interstage Transformers (1)

(coarse alignment - Rx Carrier Balance)

❒ Connect the RF signal generator to the ANT jack, and an AF voltmeter with speaker to the EXT-SPKR jack.

❒ Set VR1003 & IF SHIFT to the 12 o'clock position, then set VR1004 and VR1007 fully clockwise. Inject a +100 dB signal at 14.200.0 MHz, then adjust transformers, T1001 to T1008 and T1010 to T1012 in succession several times for peak indication on the voltmeter.

❒ Reduce the SG output level for an S-meter indication of 3 to 5-meter, then adjust VR1006 for peak AF output or max. 3-meter indication.

IF Interstage Alignment

❒ Inject a +0 dBμ Signal at 14.200.0 MHz. Connect the AF voltmeter across an 4-ohm load to the EXT-SPKR jack on the rear panel.

❒ Preset VR1004 fully clockwise, and adjust transformers T1001 to T1008 and T1010 to T1012 in succession several times for peak AF output or S-meter indication (adjust the SG

level as necessary to keep the meter approximately mid-scale).

1st Mixer Balance

❒ Preset VR1003 fully clockwise, then, in LSB mode, tune to the internal heterodyne around 15.310 MHz, and adjust VR1003 alternately several times for a null in AF output.

IF Gain

❒ Inject a +8 dBμ signal at 14.200.0 MHz to the antenna jack and adjust VR1004 for an S-1 meter deflection.

Squelch Threshold

❒ In the USB mode, with no signal at the antenna jack, set the SQL control to the 10 o'clock position, and adjust VR1005 so that the squelch just closes.

NB

❒ Inject a +20 dBμ signal at 14.200.0 MHz, then connect a voltmeter to TP1001. Press the NB key, then adjust T1022 and T1023 alternately several times for a minimum indication on the DC voltmeter.

Cellcall Tone Decord

❒ In the USB mode, with no signal at the antenna jack, connect a Frequency Counter to the VR1022 center pin.

❒ Adjust VR1022 on the Main Unit for 1850 Hz ± 10 Hz.

Transmitter

Connect the 50-ohm dummy load to the antenna jack for all procedures. Refer to the Main Unit Alignment Diagram on page 2-12 for location of components.

PA Unit Idling Current

❒ Connect a DC Ammeter to TP5001(-) and TP5002(+), and select USB mode.

❒ Key the transceiver and adjust VR5001 for 400 mA ± 50 mA.

Transmitter IF Interstage Transformers (2)

❒ Connect an in-line wattmeter and 50-ohm RF dummy load to the ANT jack.

❒ Tune to 14.200.0 MHz, and select the USB mode. Key the transceiver and adjust transformers, T1016 to T1021 several times for peak indication on the wattmeter (approx. 100 W).

CM Coupler Balance

❒ With the 50-ohm dummy load connected to the antenna jack, tune the transceiver to 14.200.0 MHz, and select the USB mode. Connect the DC voltmeter between pins 2 (-) and 3 (+) of J1022, key the transmitter and adjust TC4001 on the LPF Unit for minimum indication on the DC voltmeter.

RF Power Output Adjust

❒ With the in-line wattmeter and dummy load still connected, set the VR1010 fully counter clockwise .

❒ Select A1A mode and tune to 14.200.0 MHz. Key the transmitter and adjust VR1010 for 100 W ±5 W as indicated on the in-line wattmeter.

50W RF Power Output Adjust

❒ Set switch S1001 to the 50 W position, then set the RF PWR control fully clockwise .

❒ Select A1A mode and tune to 14.200.0 MHz.

Key the transmitter and adjust VR1016 for 50 W ±2.5 W as indicated on the in-line wattmeter.

Ext. Antenna Tuner RF Power Adjust

❒ Turn the transceiver off, then jumper TP1002 to chassis ground.

❒ Set switch S1001 to the "100 W" position, tune to 14.200.0 MHz, A1A mode.

❒ Turn the transceiver on, key the transmitter and adjust VR1011 for 10 W.

TX IF Gain

❒ Connect an AF signal generator and voltmeter to the MIC jack, and inject a 0.5 mV signal at 1kHz, and tune to 14.200.0 MHz, USB mode.

❒ Key the transmitter and adjust VR1007 for 50 W (increase AF SG level to 1 mVrms, if necessary, to permit deflection).

PO Meter Calibration

❒ With the in-line wattmeter and dummy load connected to the antenna jack, tune to 14.200.0 MHz, USB mode.

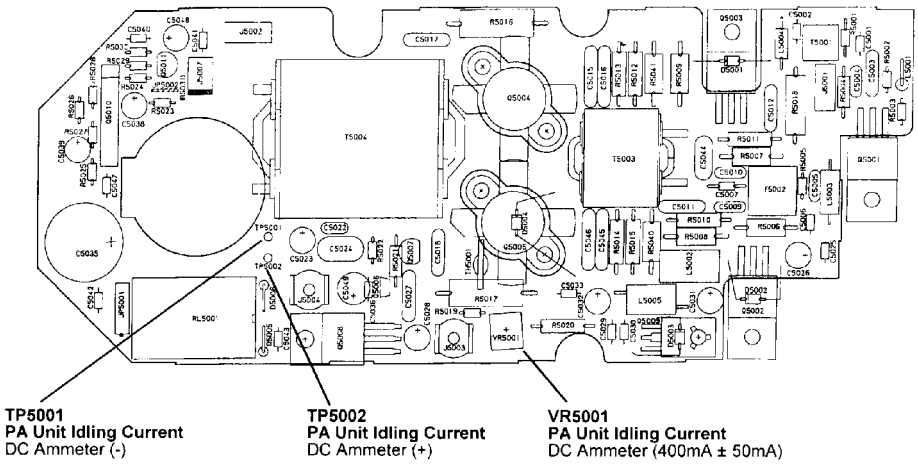
❒ Then adjust VR1019 so the PO meter also indicates 10 position.

S-Meter Reverse PO Indication

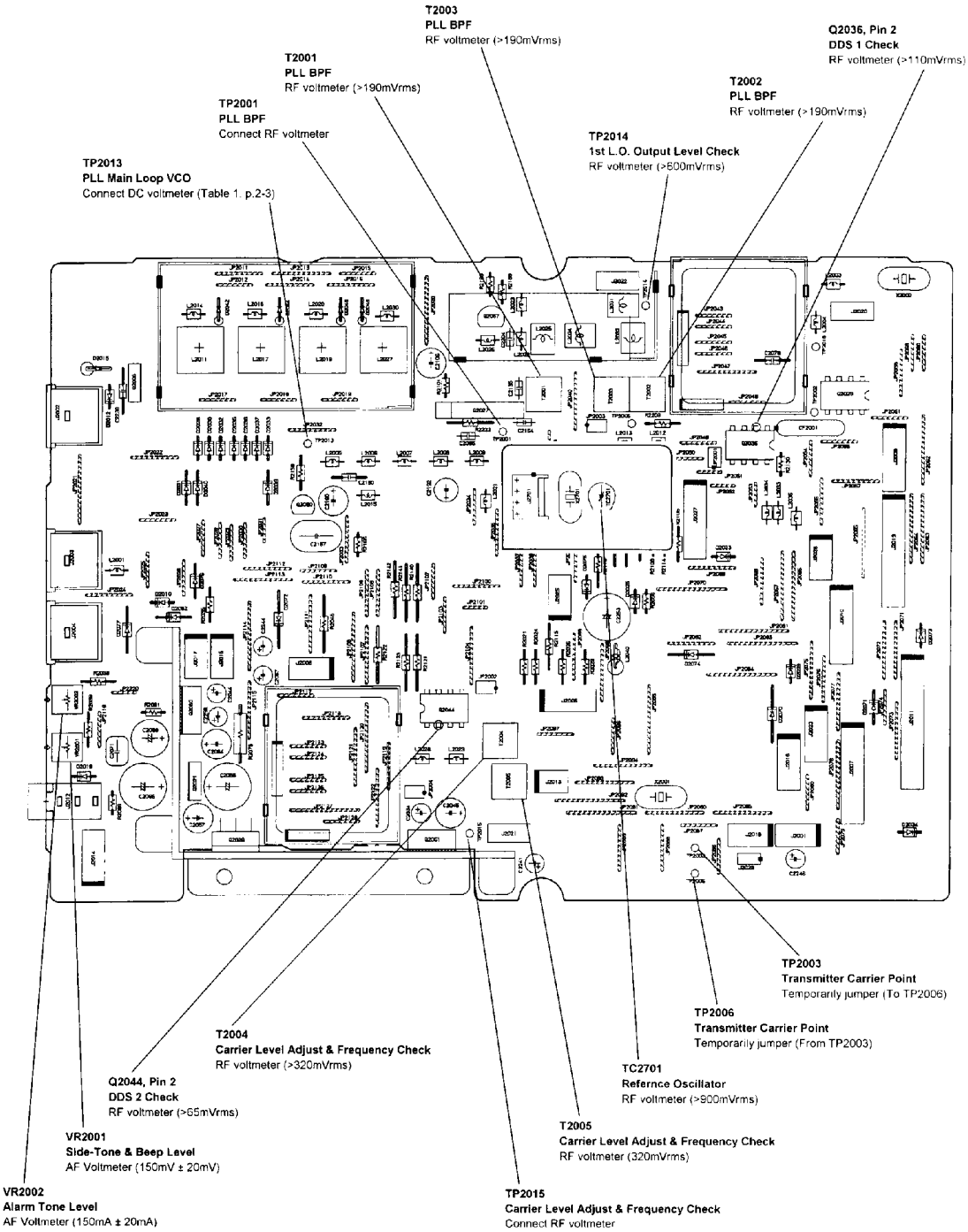
Reverse ALC (SWR Turndown)

❒ Connect the in-line wattmeter and a 16.6-ohm dummy load (or three 50-ohm loads in parallel) to the antenna jack.

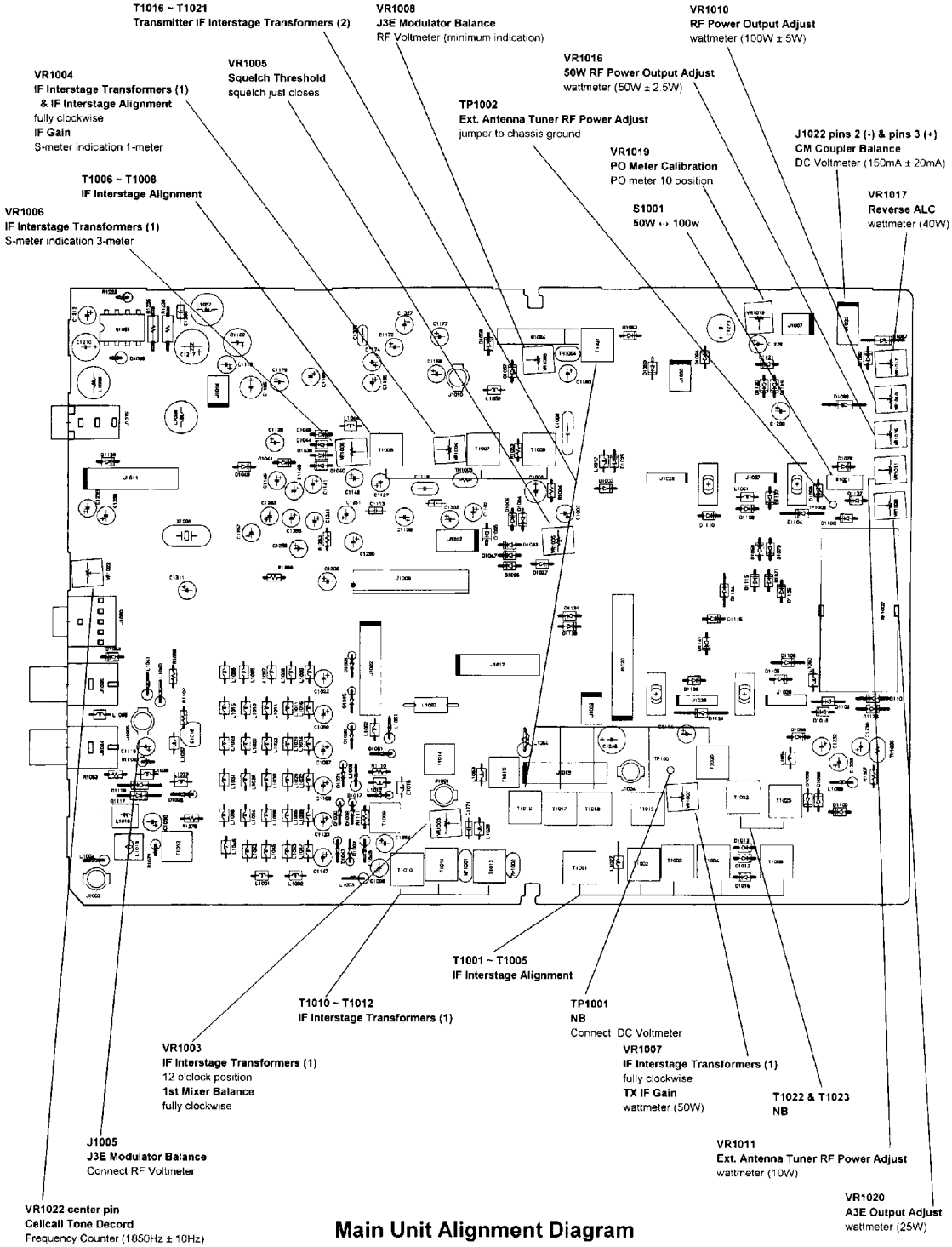
❒ With the transceiver set to the A1A mode on 14.200.0 MHz, key the transmitter and adjust VR1017 for 40 W on the wattmeter.



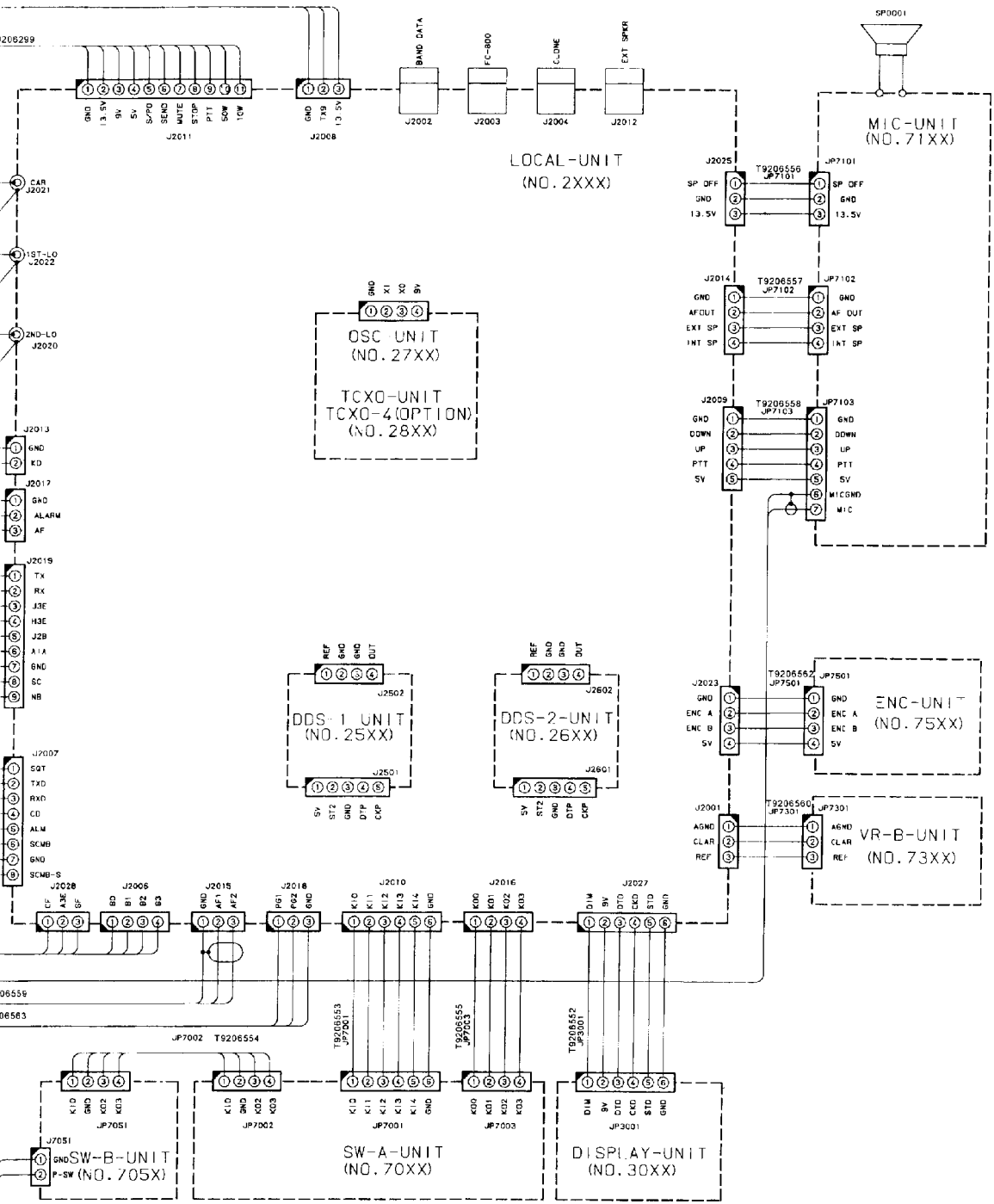
PA Unit Alignment Diagram



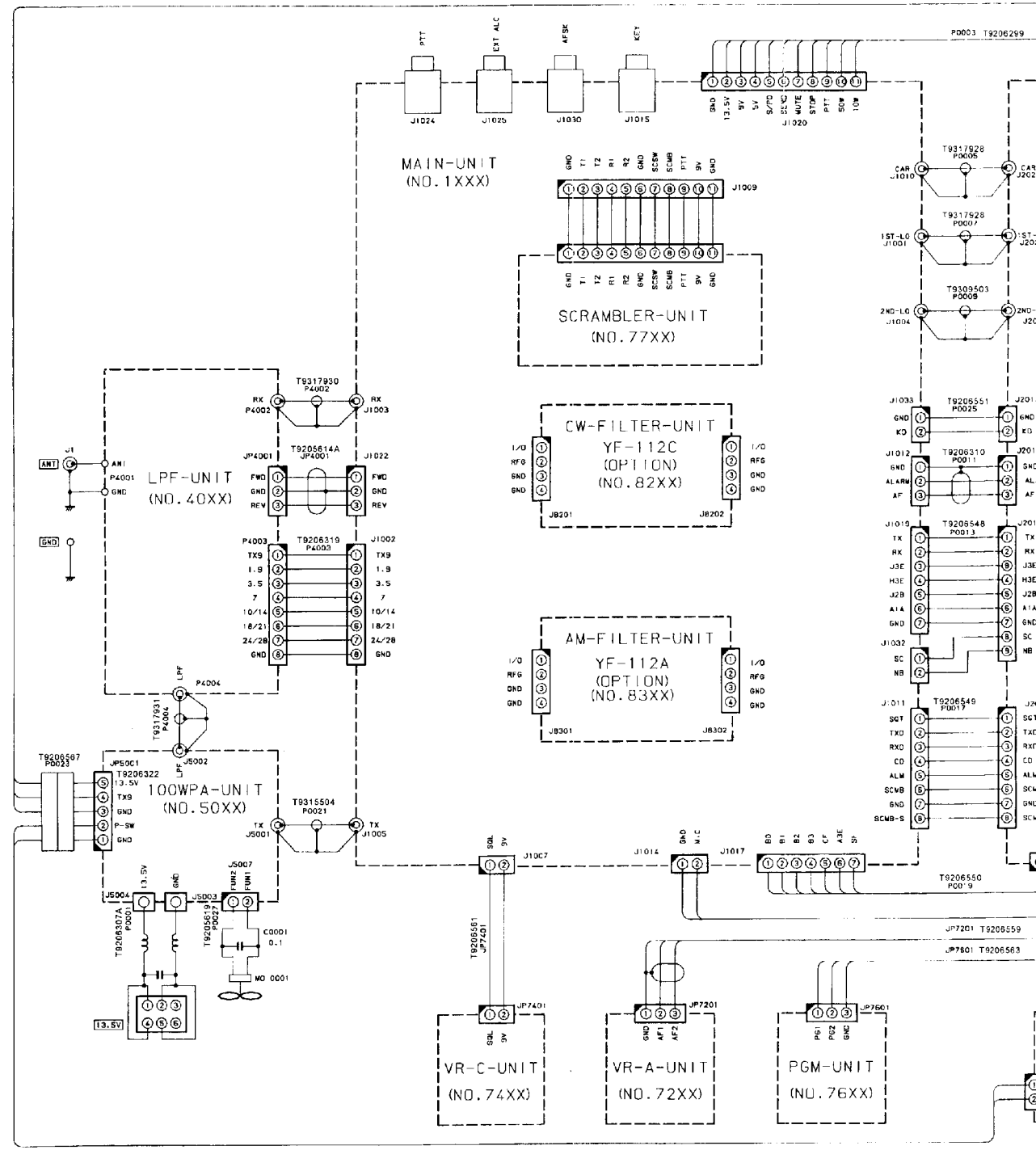
Local Unit Alignment Diagram



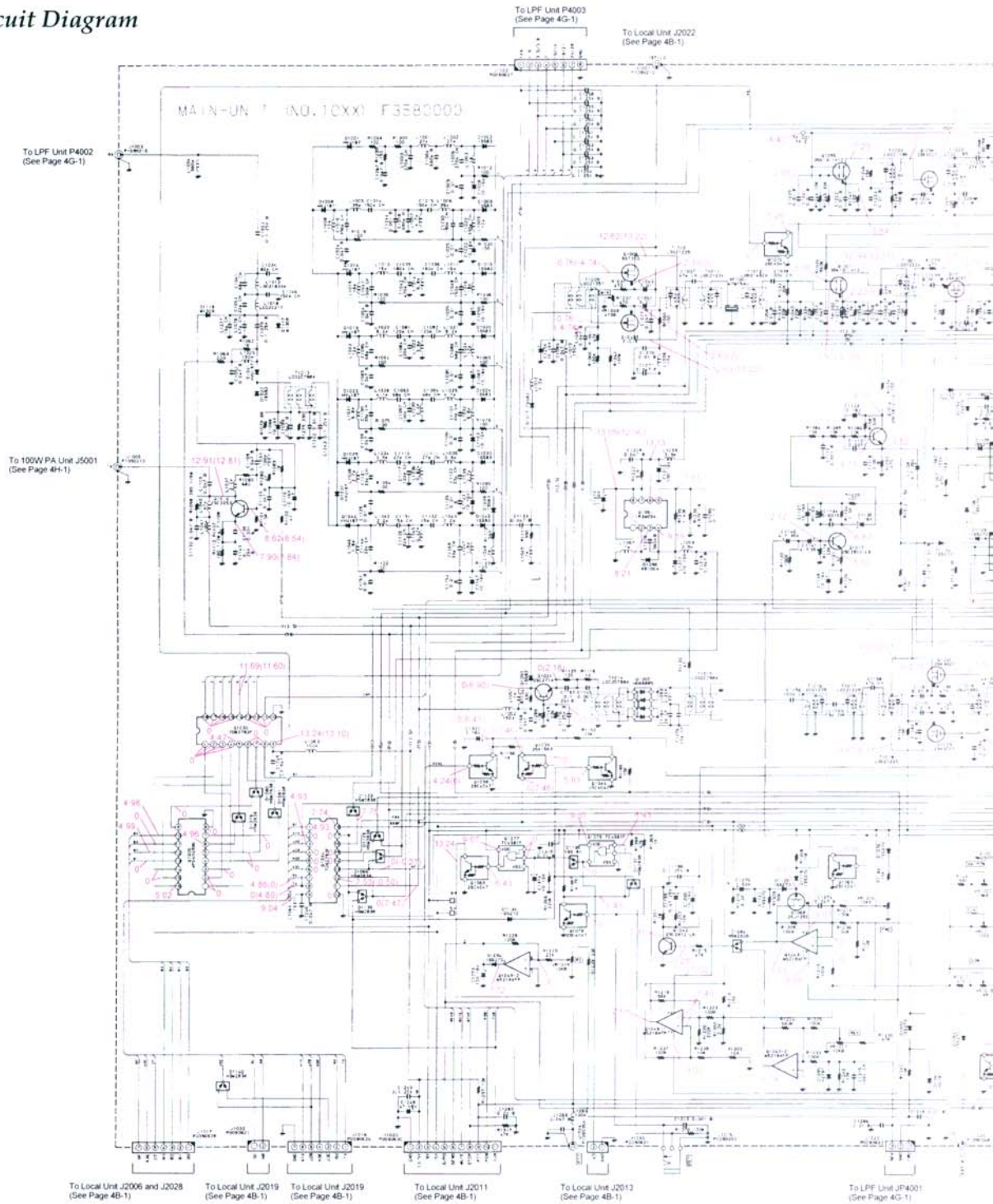
Main Unit Alignment Diagram

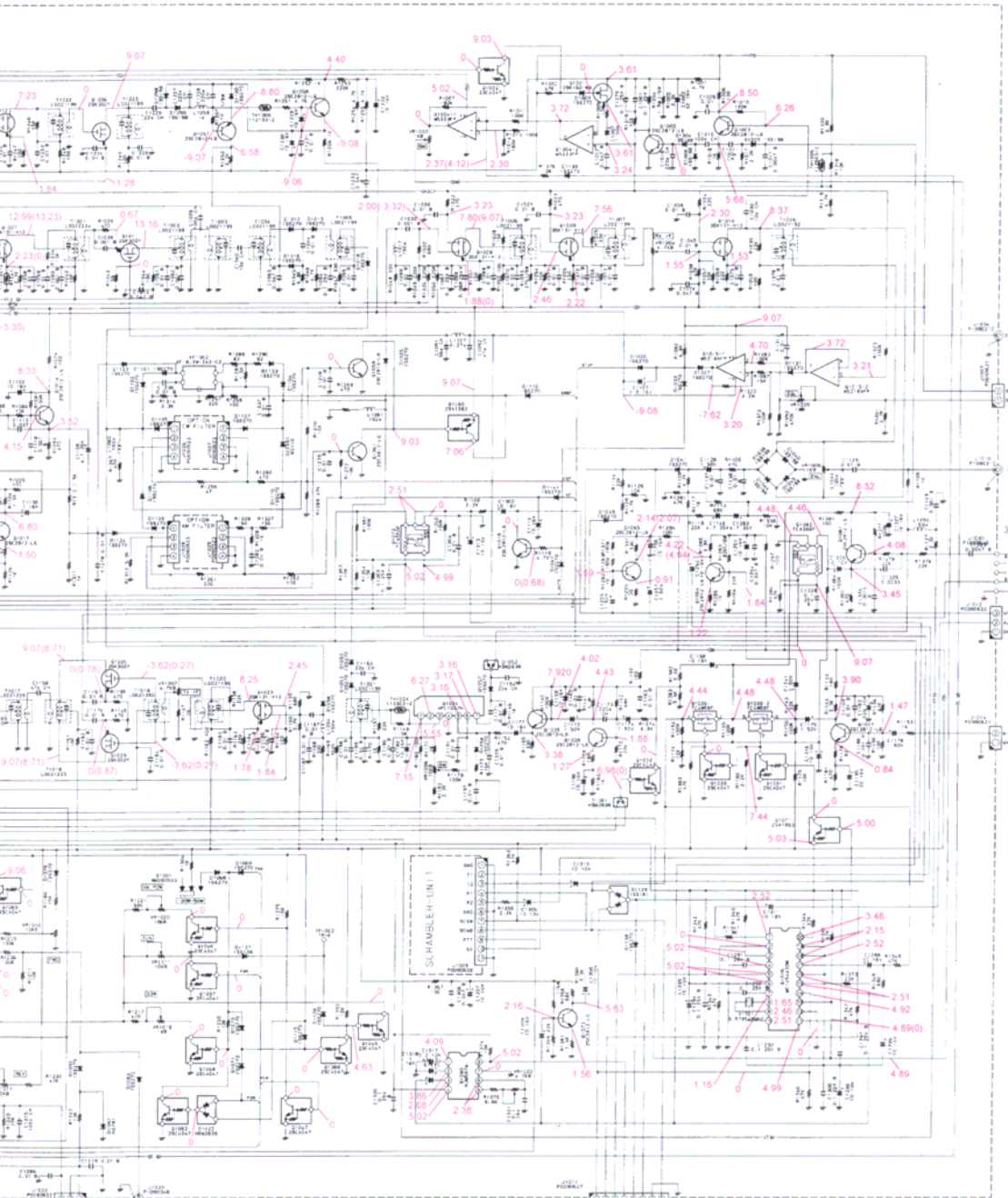

LOCAL-UNIT
(NO.2XXX)
MIC-UNIT
(NO.71XX)
OSC UNIT
(NO.27XX)
TCXO-UNIT
TCXO-4(OPTION)
(NO.28XX)
DDS-1 UNIT
(NO.25XX)
DDS-2-UNIT
(NO.26XX)
ENC-UNIT
(NO.75XX)
VR-B-UNIT
(NO.73XX)
SW-B-UNIT
(NO.705X)
SW-A-UNIT
(NO.70XX)
DISPLAY-UNIT
(NO.30XX)
BAND DATA
FC-800
CLONE
EXT SPKR
SP0001
CAR J2021
1ST-LO J2022
2ND-LO J2020
J2011
J2008
J2002
J2003
J2004
J2012
J2025
J2014
J2009
J2023
J2001
J2013
J2017
J2019
J2007
J2028
J2006
J2015
J2018
J2010
J2016
J2027
J2502
J2501
J2602
J2601
JP7101
JP7102
JP7103
JP7501
JP7301
JP7002
JP7051
JP7052
JP7001
JP7003
JP3001
T9206556
T9206557
T9206558
T9206562
T9206560
T9206553
T9206555
T9206552
T9206554

nterconnection Diagram

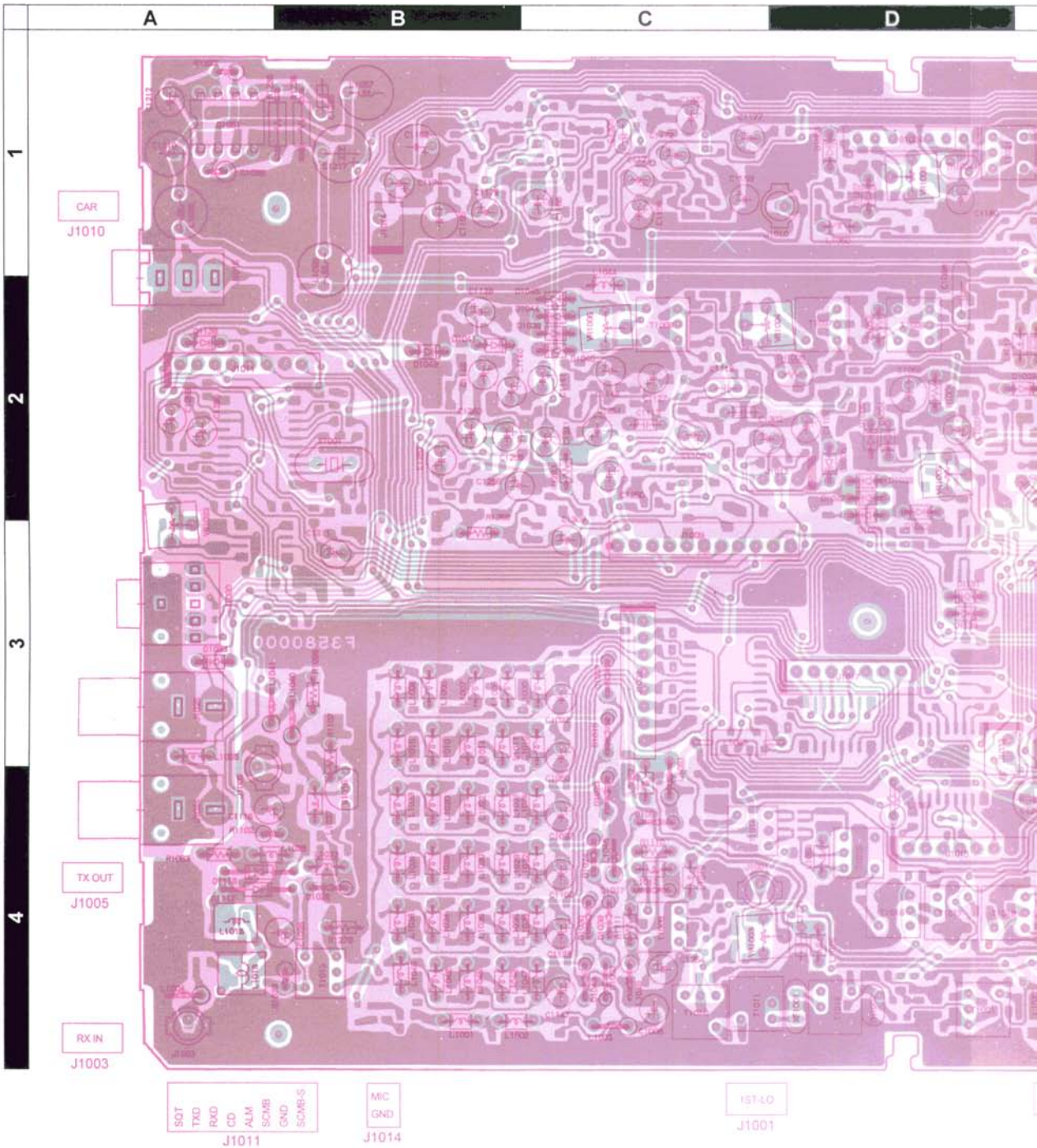


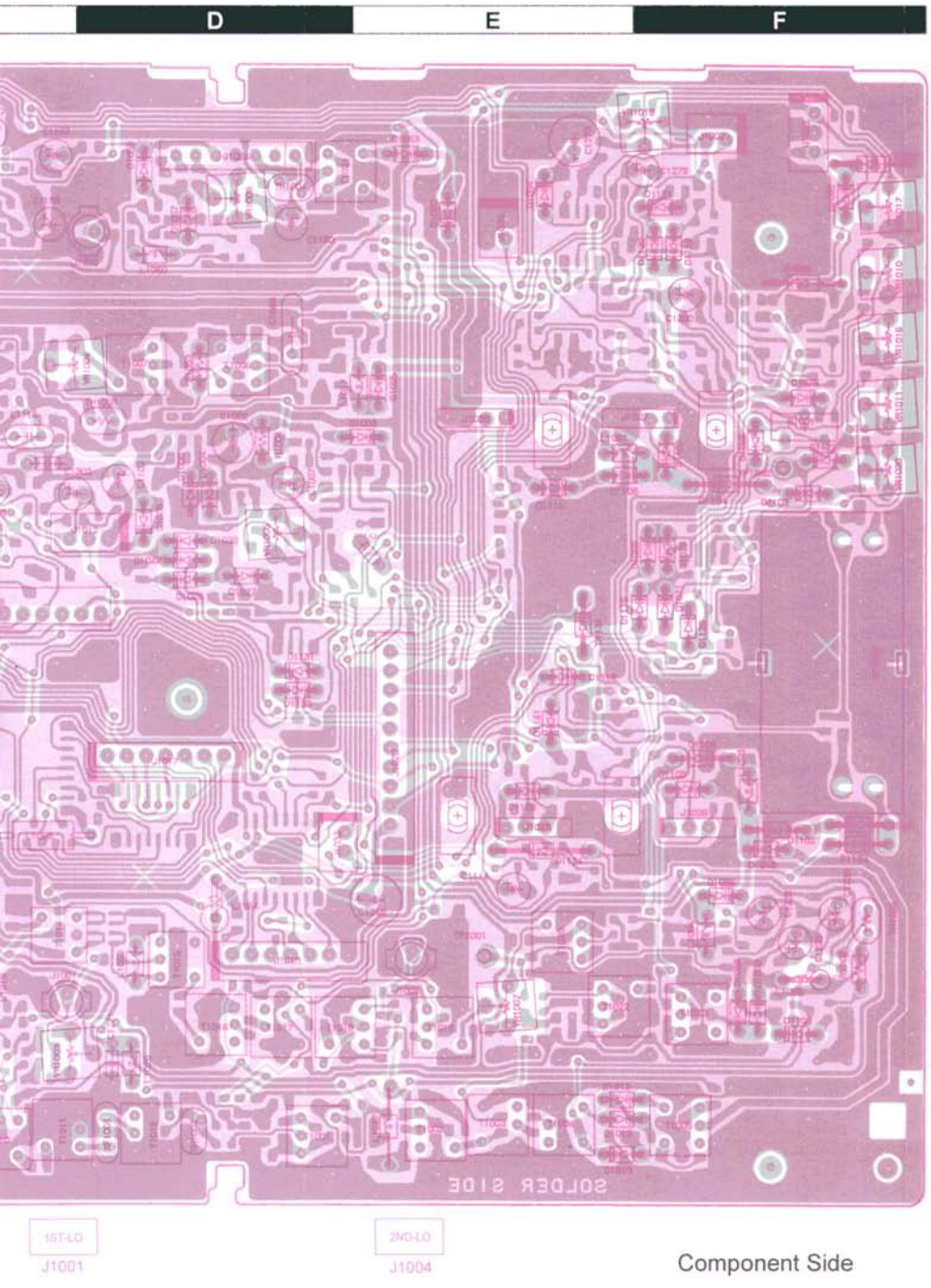
Circuit Diagram



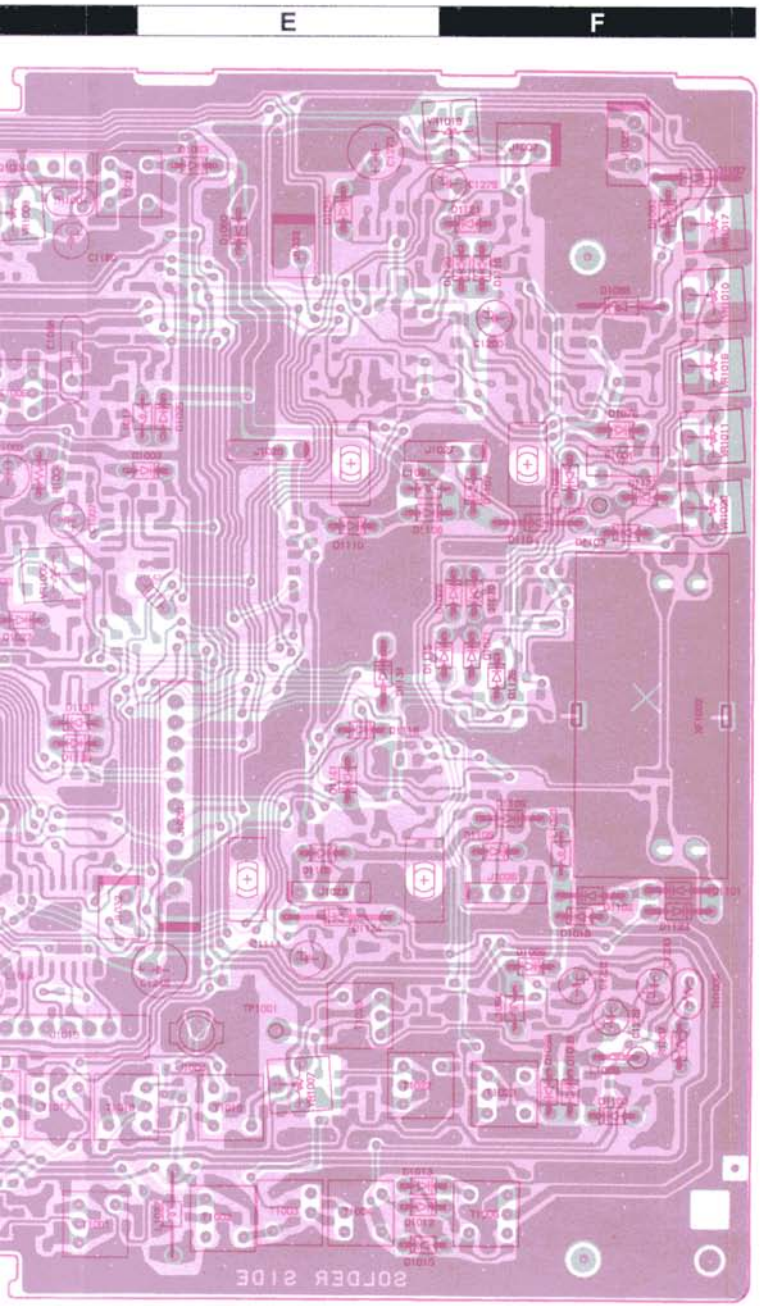

To Local Unit J2020
(See Page 4B-1)
To VR-C Unit JP7401
(See Page 4N-1)
To Local Unit J2021
(See Page 4B-1)
To Local Unit J2017
(See Page 4B-1)
To MIC Unit JP7103
(See Page 4K-1)
SCRAMBLER UNIT
RX (TX)
RX: 14.25 MHz
TX: 14.25 MHz, 100 W (Connected to a 50-W dummy load)
To LPF Unit JP4001
(See Page 4G-1)
To Local Unit J2007
(See Page 4B-1)

Parts Layout





Component Side



2ND-LO
J1004

Component Side

J1007
SQL
9V

J1022
FWD
GND
REV

J1033
GND
KD

2SC2053
(Q1016)
Emitter
Collector
Base

J1029
GND
GND
RFG
I/O

J1027
GND
GND
RFG
I/O

μPC1037H
(Q1024)
Pin 1
Pin 7

J1012
AF
ALARM
GND

IR3M03A
(Q1051)
Pin 8
Pin 1

J1009
GND
T1
T2
R1
R2
GND
SCSW
SCMB
PTT
9V
GND

J1032
SC
NB

J1028
I/O
RFG
GND
GND

J1026
I/O
RFG
GND
GND

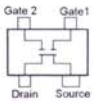
J1017
B0
B1
B2
B3
CF
A3E
SF

J1020
10W
50W
PTT
STOP
MUTE
SENO
S/PO
5V
9V
13.5V
GND

J1019
TX
RX
J3E
H3E
J2B
A1A
GND

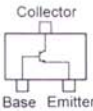
J1002
TX9
1.9
3.5
7
10/14
18/21
24/28
GND

Main Unit

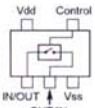


3SK131 (V12)
(Q1007,1008,1009,1010
1023,1055)

M5223FP
(Q1004)
M5218AFP
(Q1015,1043,1049)
NJM567M
(Q1080)

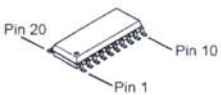


2SC2812 (L6)
(Q1003,1005,1014,1017
1019,1025,1027,1028
1042,1057,1058,1059
1061,1064,1065,1073
1081)
2SC2714Y (QY)
(Q1021)



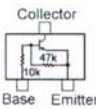
TC4S66F (C9)
(Q1072,101082)

UPD4028BG
(Q1036)

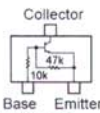


MC145442DWEL
(Q1076)

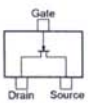
TC4W66F
(Q1026)



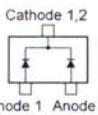
2SA1563 (RL)
(Q1039,1060,1071)



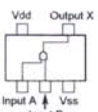
2SC4047 (ZY)
(Q1002,1031,1032,1033
1037,1038,1044,1045
1046,1047,1048,1052
1063,1066,1069,1070
1078)



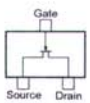
SST310 (Z0)
(Q1006,1012)



HSM2838 (A6)
(D1028,1052,1061,1066
1079,1080,1081,1084
1122,1128,1133,1136
1137,1140)

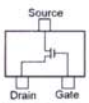


TC4S81F ()
(Q1077,1079)

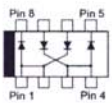


2SK160 (K6)
(Q1001)

1SS181 (A3)
(D1129,1135)

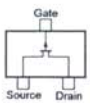


2SK302Y (TY)
(Q1011,1020,1029,1056)

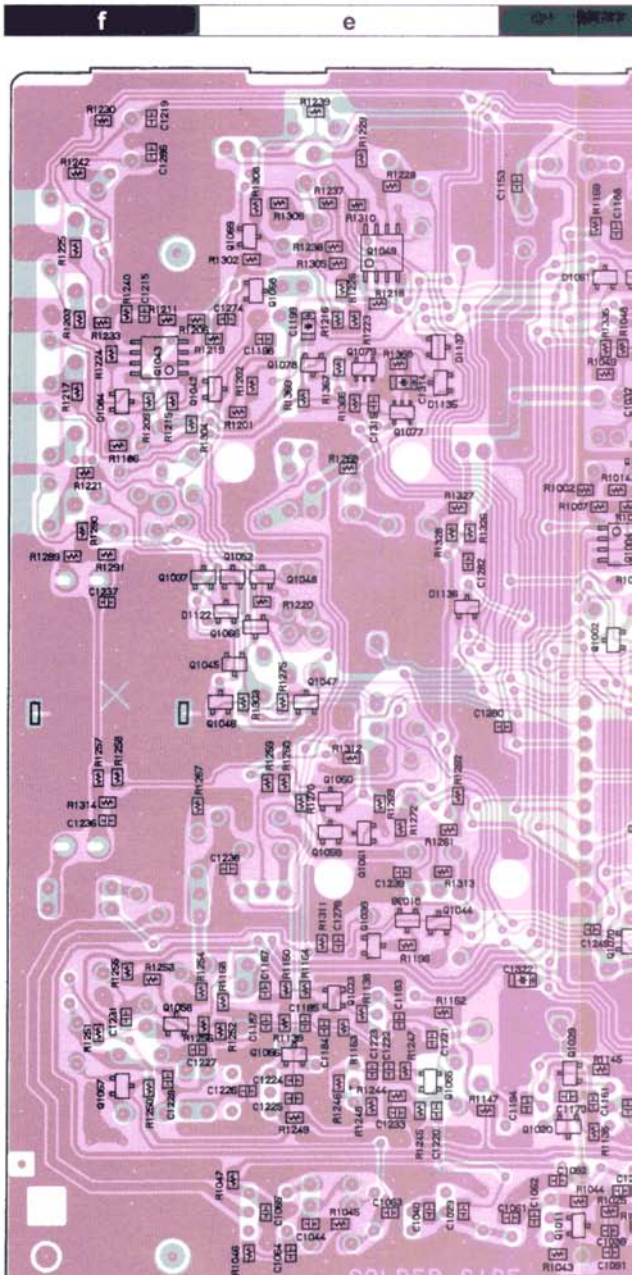


HSB88WSTR
(D1055)

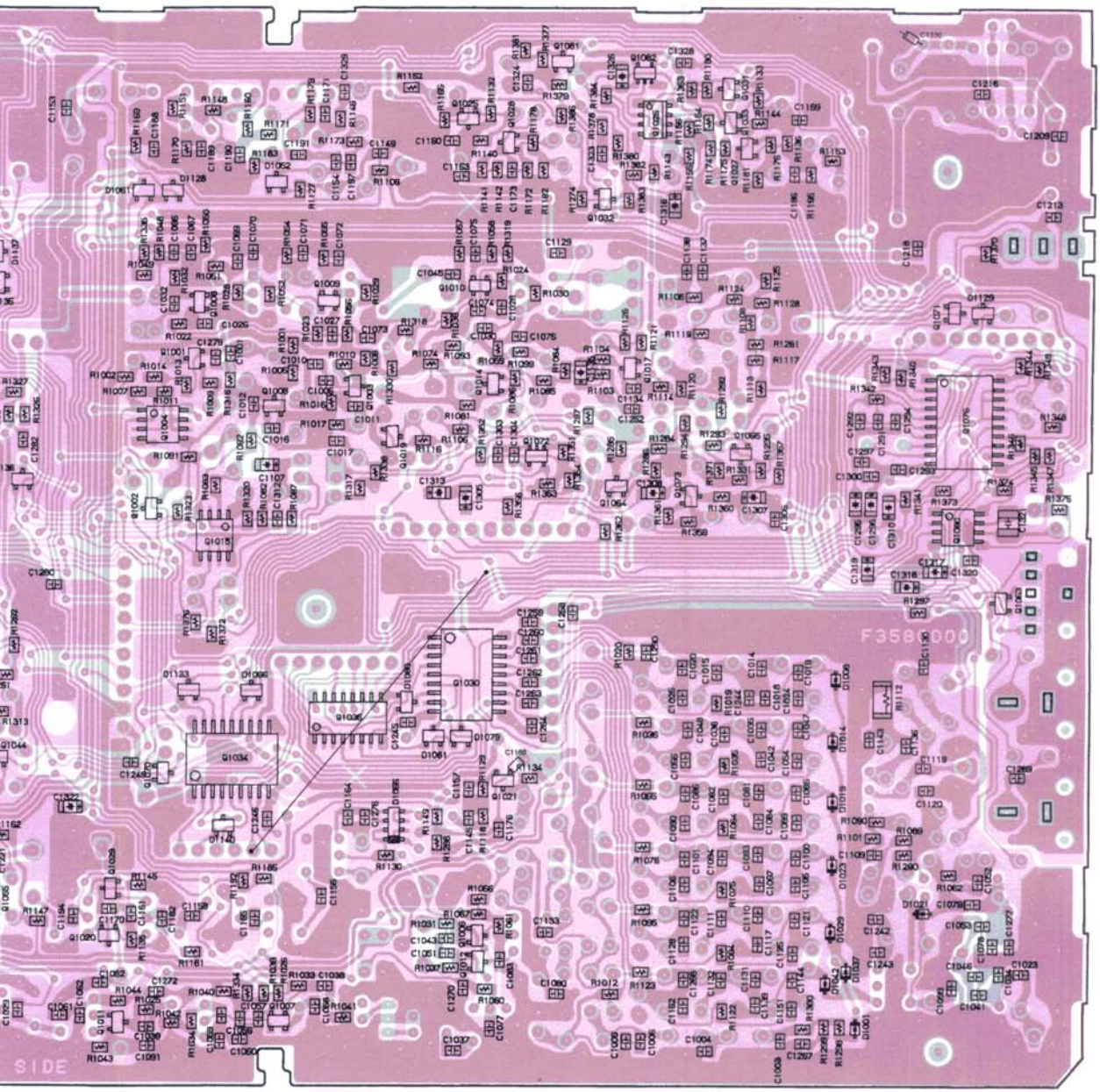
TD62783F
(Q1030,1034)



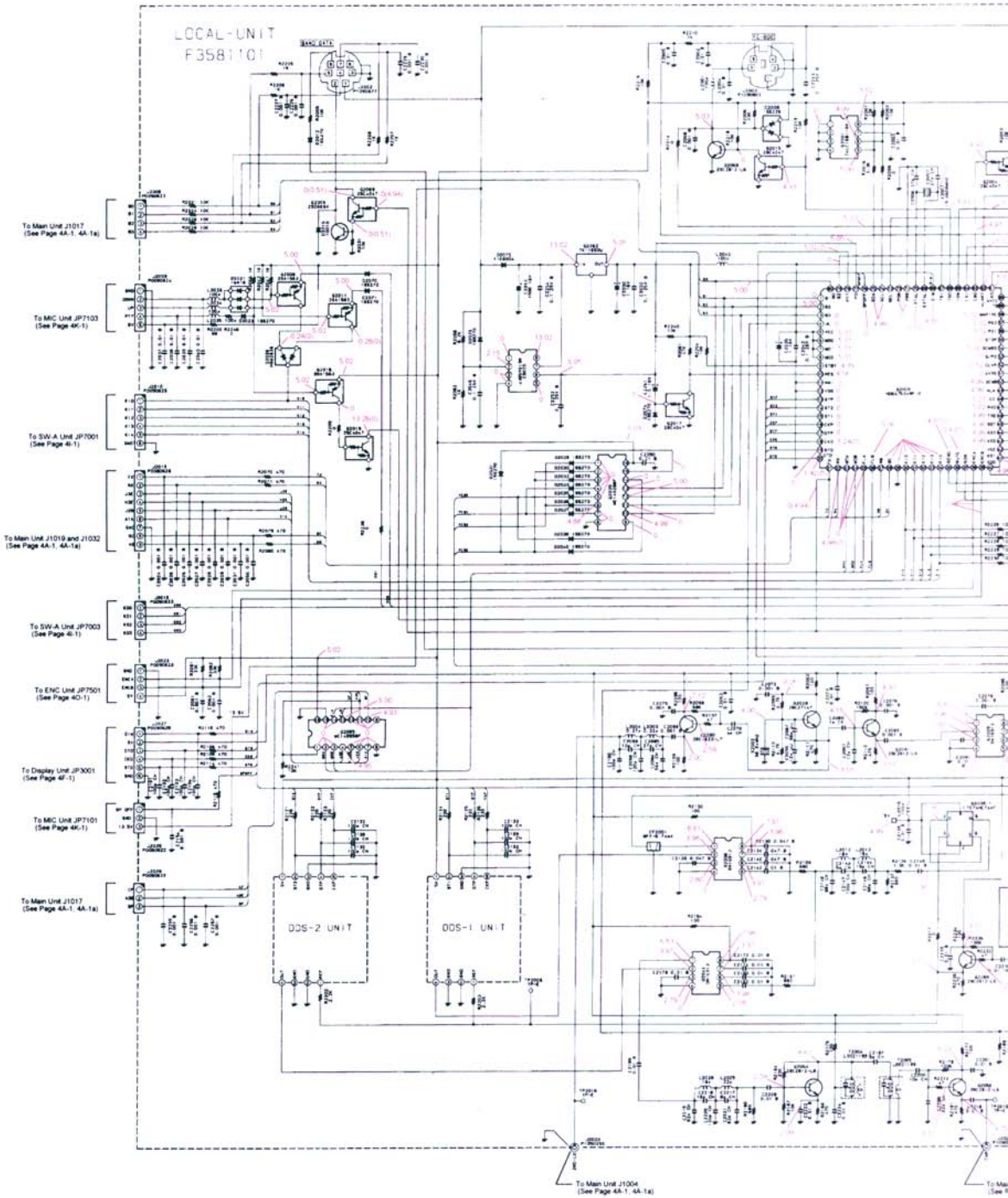
2SJ125D (JD)
(Q1068)

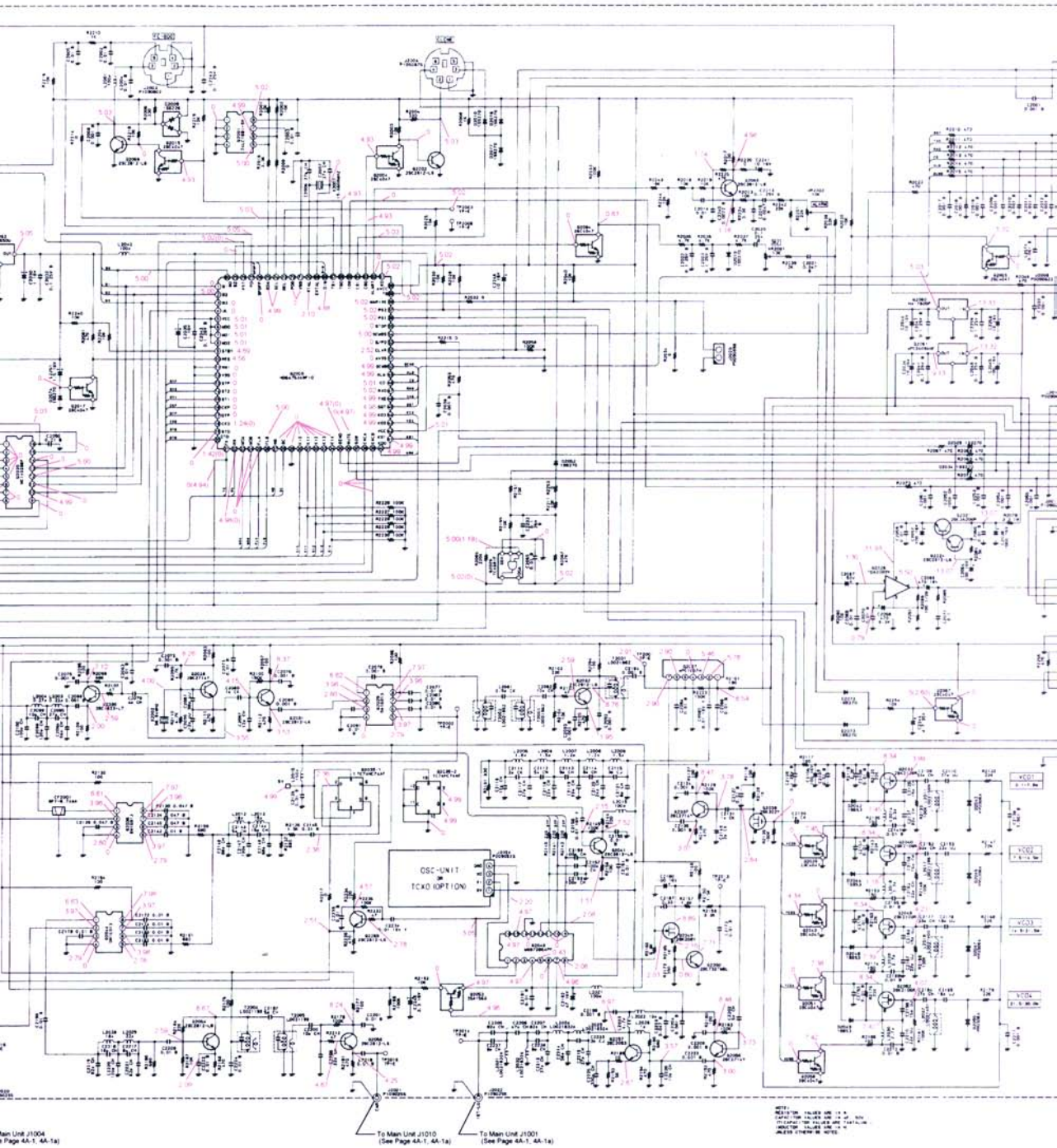


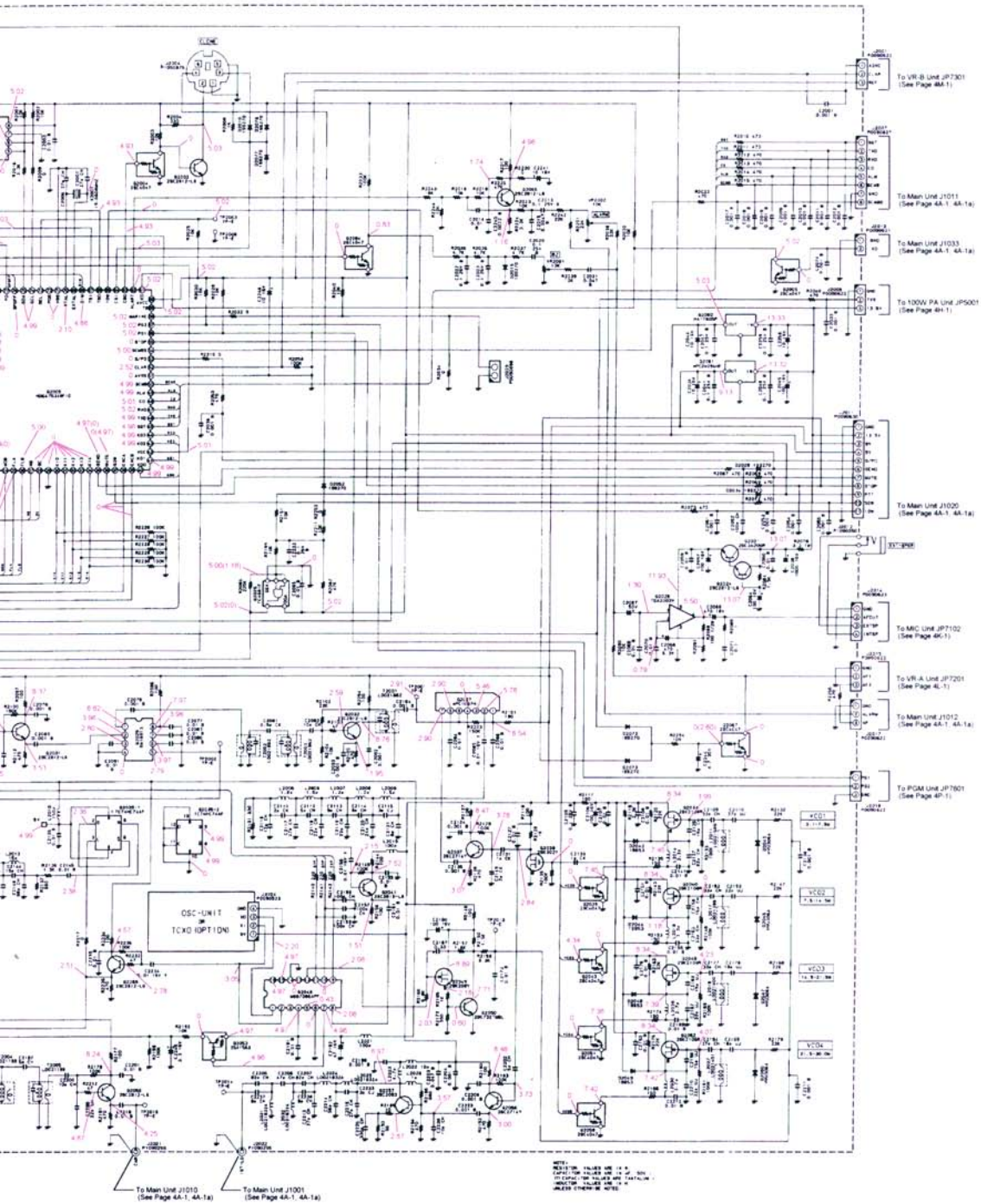
d c b a



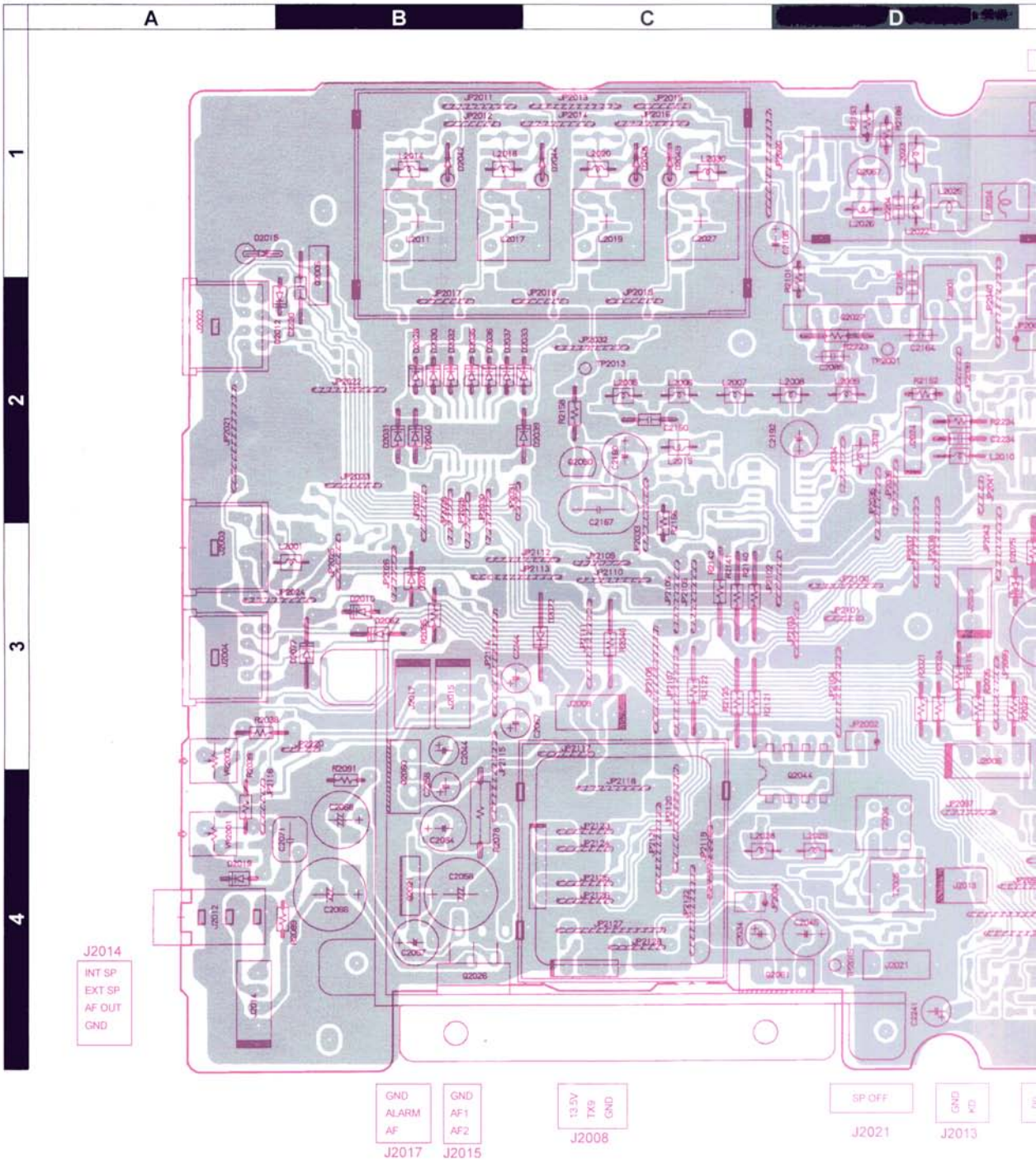
Chip Side

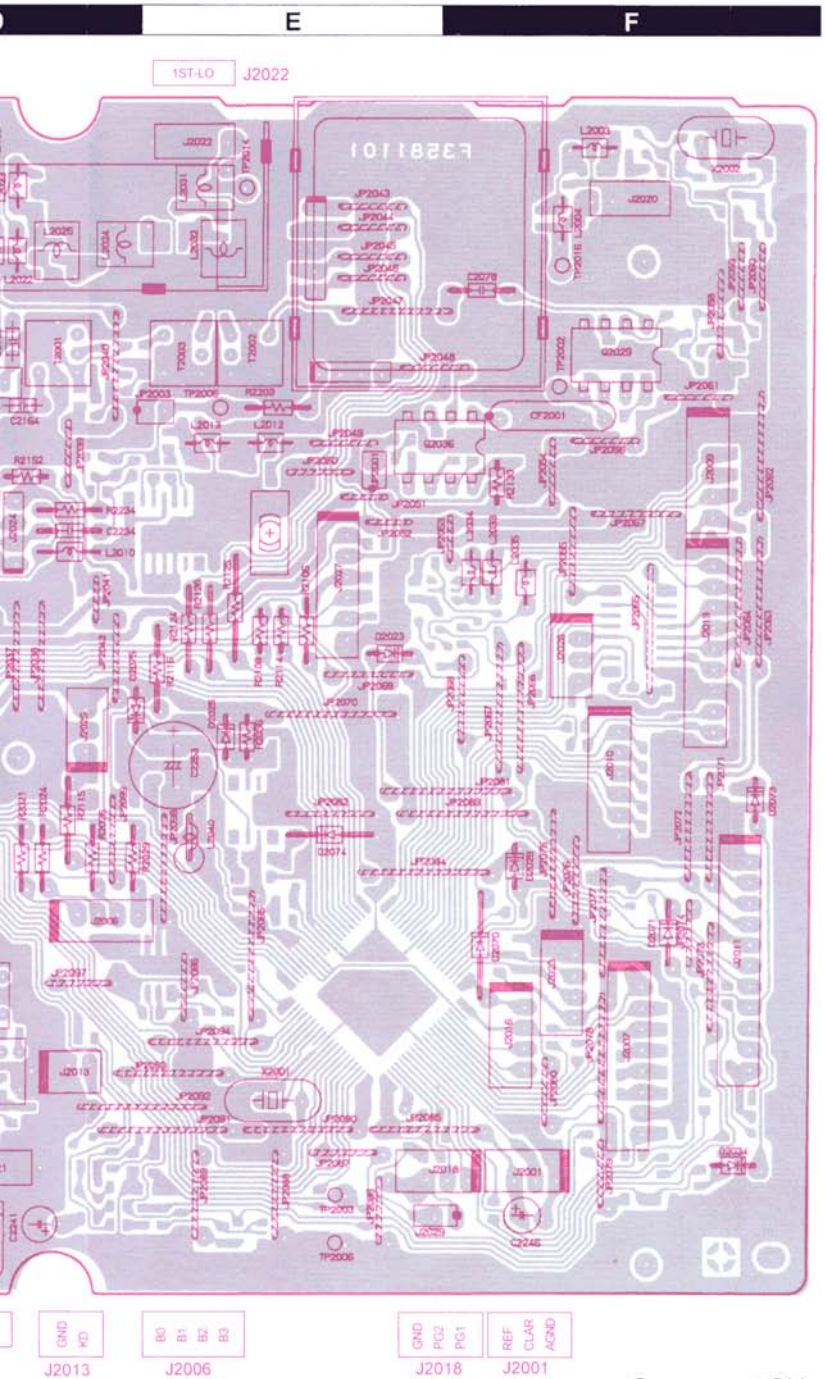

LOCAL-UNIT
F3581101
To Main Unit J1017
(See Page 4A-1, 4A-1a)
To MIC Unit JP7103
(See Page 4K-1)
To SW-A Unit JP7001
(See Page 4I-1)
To Main Unit J1019 and J1032
(See Page 4A-1, 4A-1a)
To SW-A Unit JP7003
(See Page 4I-1)
To ENC Unit JP7501
(See Page 4O-1)
To Display Unit JP3001
(See Page 4F-1)
To MIC Unit JP7101
(See Page 4K-1)
To Main Unit J1017
(See Page 4A-1, 4A-1a)
DDS-2 UNIT
DDS-1 UNIT
To Main Unit J1004
(See Page 4A-1, 4A-1a)


OSC-UNIT
TCXO (OPTION)
To Main Unit J1010
(See Page 4A-1, 4A-1a)
To Main Unit J1001
(See Page 4A-1, 4A-1a)

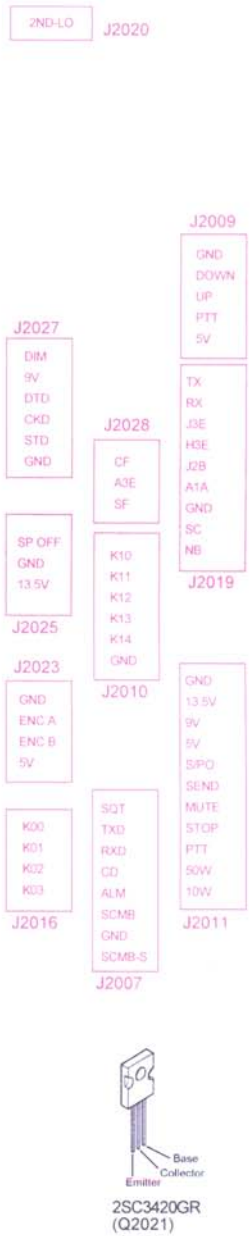

To VR-B Unit JP7301
(See Page 4M-1)
To Main Unit J1011
(See Page 4A-1, 4A-1a)
To Main Unit J1033
(See Page 4A-1, 4A-1a)
To 100W PA Unit JPS001
(See Page 4H-1)
To Main Unit J1020
(See Page 4A-1, 4A-1a)
To MIC Unit JP7102
(See Page 4K-1)
To VR-A Unit JP7201
(See Page 4L-1)
To Main Unit J1012
(See Page 4A-1, 4A-1a)
To PGM Unit JP7601
(See Page 4P-1)
OSC-UNIT
or
TCXO (OPTION)
VCO1
VCO2
VCO3
VCO4
To Main Unit J1010
(See Page 4A-1, 4A-1a)
To Main Unit J1001
(See Page 4A-1, 4A-1a)

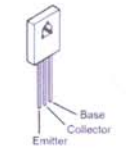
Parts Layout



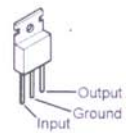


Component Side



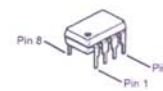


2SD669A
(Q2006)

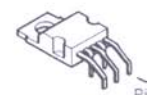


HA17805P
(Q2060)

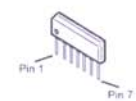
2SC2053
(Q2057)



SN16913P
(Q2029,2036,204

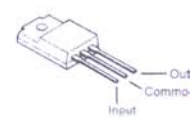


TDA2003
(Q2026)



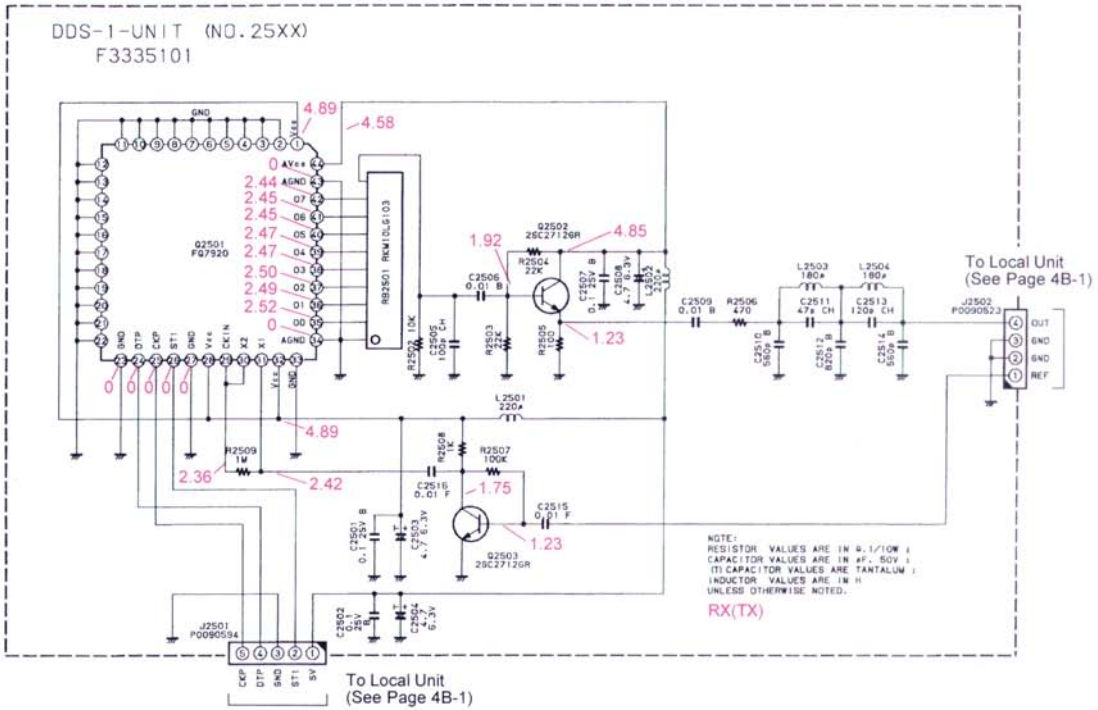
μPC1037H
(Q2027)

2SC3420GR
(Q2021)

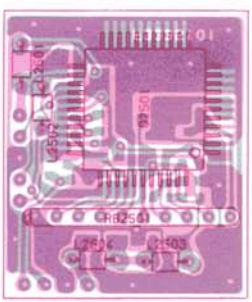


KIA7809PI
(Q2061)

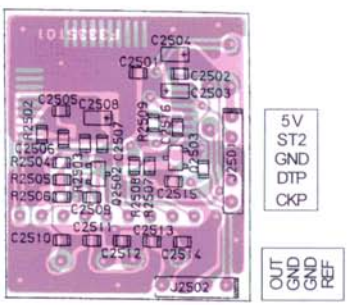
Circuit Diagram



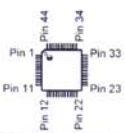
Parts Layout



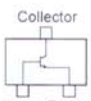
Component Side



Connector Side

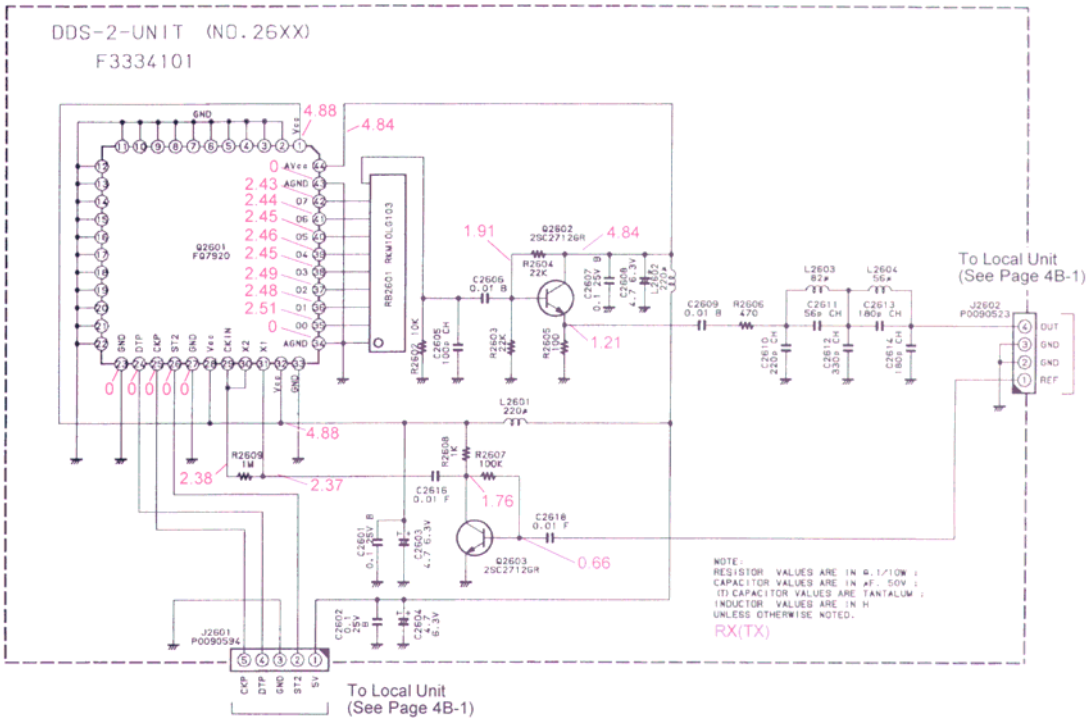


FQ7920 (Q2501)

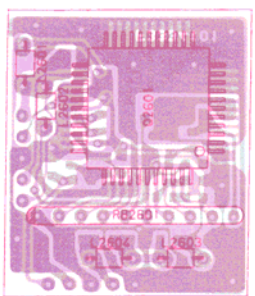


2SC2712GR (LG)
(Q2502,2503)

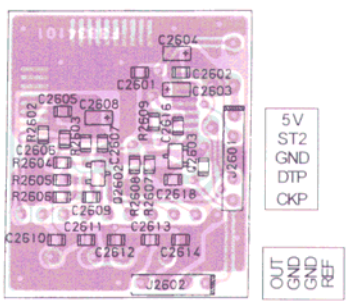
Circuit Diagram



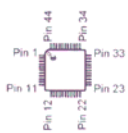
Parts Layout



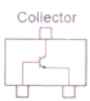
Component Side



Connector Side

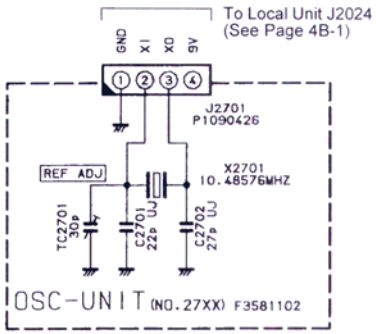


FQ7920 (Q2601)



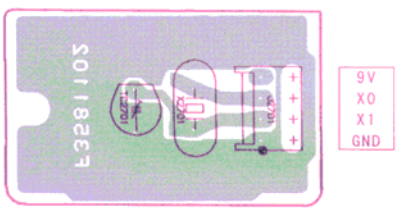
2SC2712GR (LG)
(Q2602,2603)

Circuit Diagram

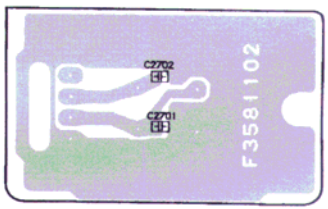


NOTE:
CAPACITOR VALUES ARE IN μF, 50V ;
UNLESS OTHERWISE NOTED.

Parts Layout



Component Side

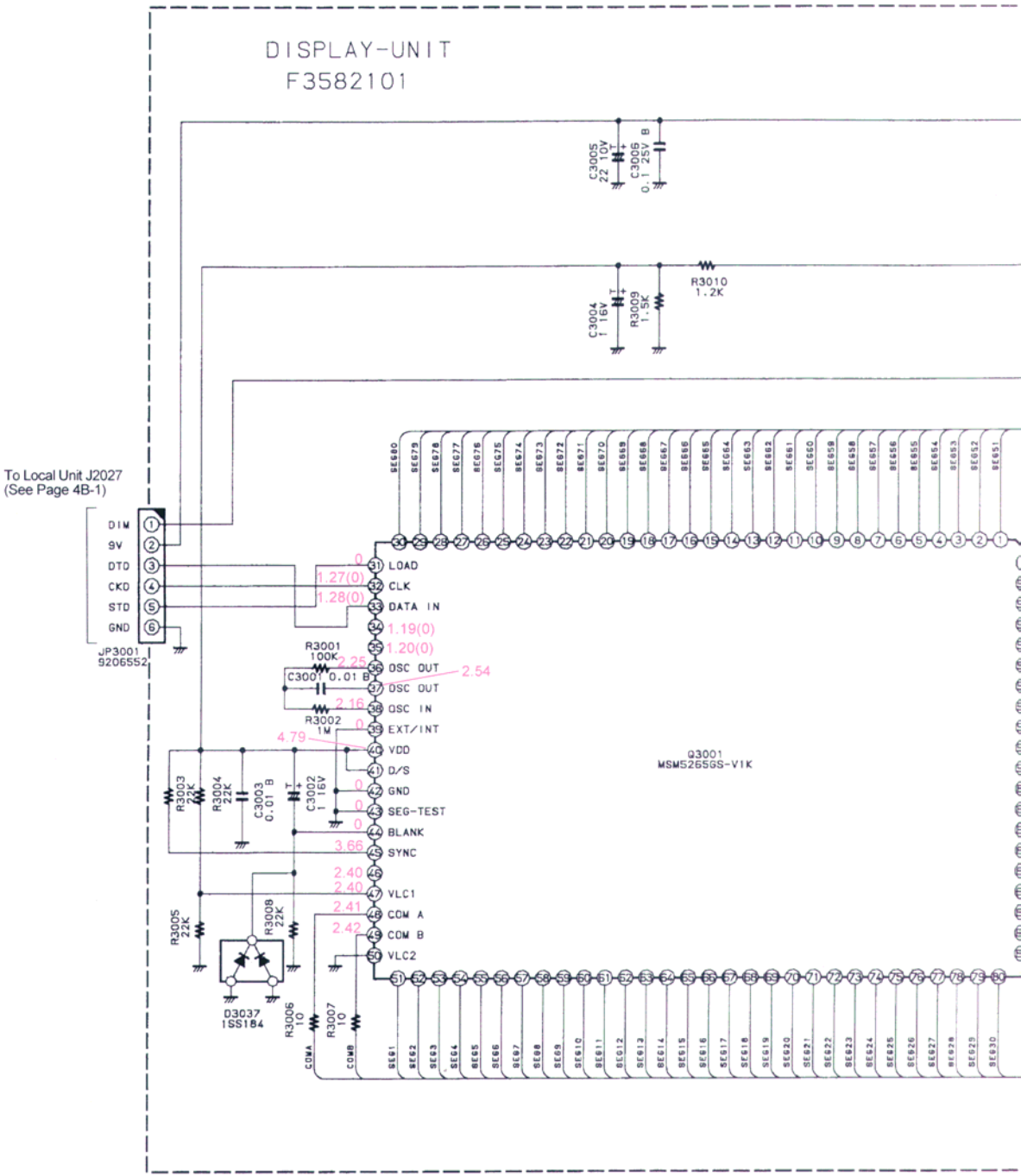


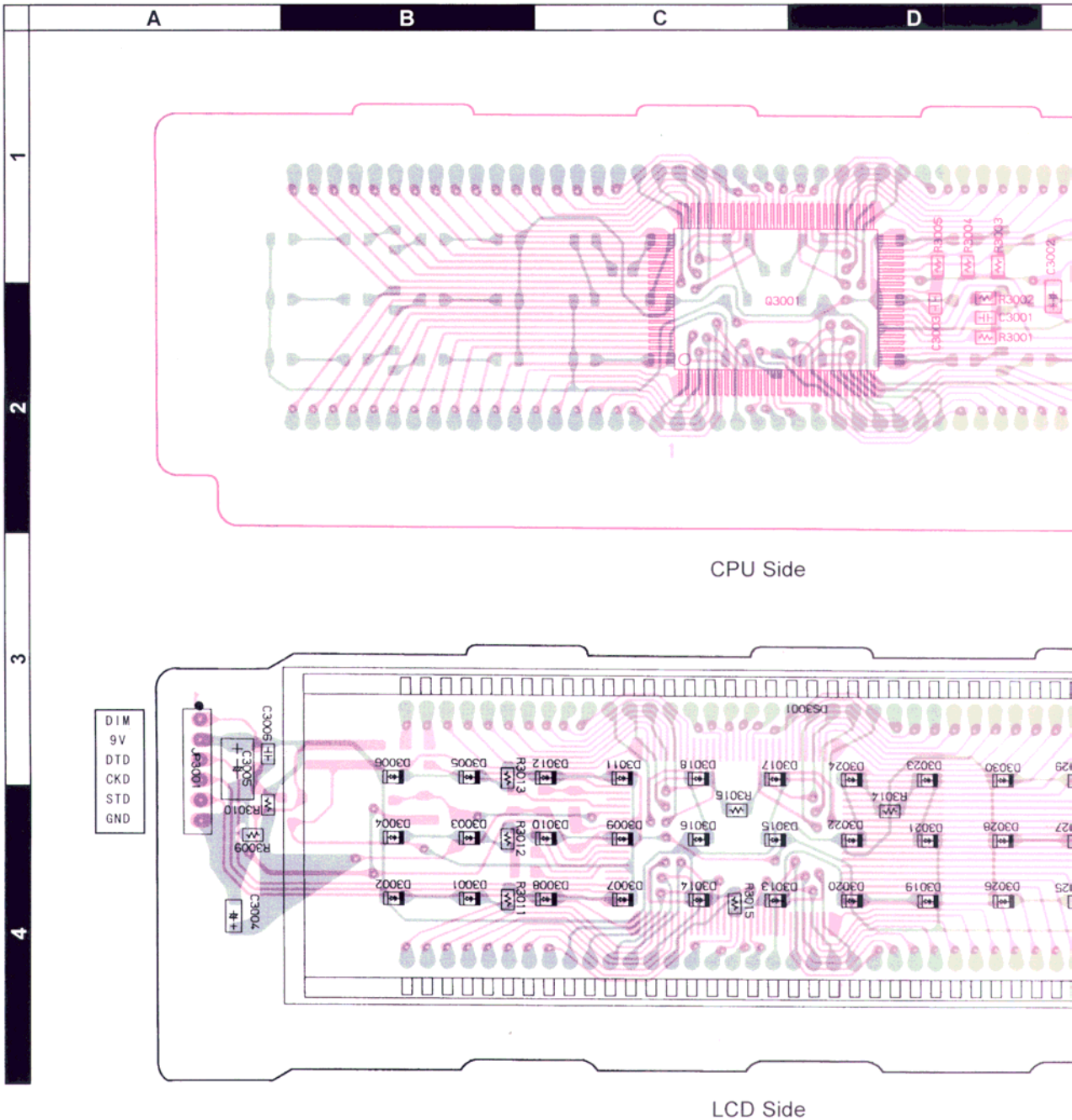
Chip Side

Parts List

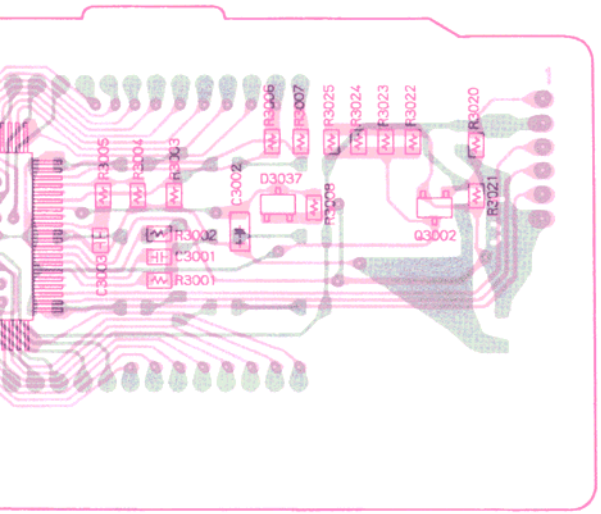
REF.	DESCRIPTION	VALUE	WV	TOL.	MFGR'S DESIG	YAESU P/N	VERS.	LOT.	LAY ADR
	*** OSC UNIT ***								
	PCB with Components					CA1583001			
	Printed Circuit Board					F3581102			
C 2701	CHIP CAP.	22pF	50V	UJ	GRM40UJ220J50PT	K22170319			
C 2702	CHIP CAP.	27pF	50V	UJ	GRM40UJ270J50PT	K22170321			
J 2701	CONNECTOR				5124-04BHPB	P1090426			
TC2701	TRIMMER CAP.	30pF			VCT51F	K91000093			
X 2701	XTAL	10.48576MHz				H0102990			

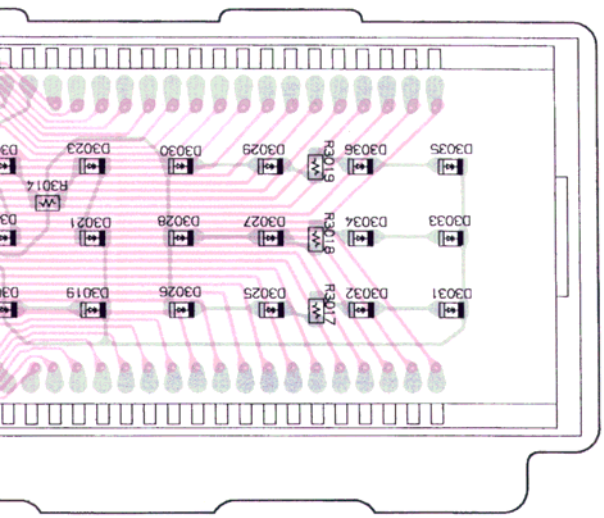
Circuit Diagram

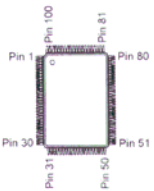



A
B
C
D
1
2
3
4
Q3001
R3005
R3004
R3003
C3002
R3002
C3001
R3001
CPU Side
DS3001
DIM
9V
DTD
CKD
STD
GND
JP3001
C3006
C3005
C3004
R3010
R3009
LCD Side

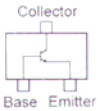
D E



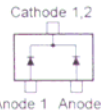




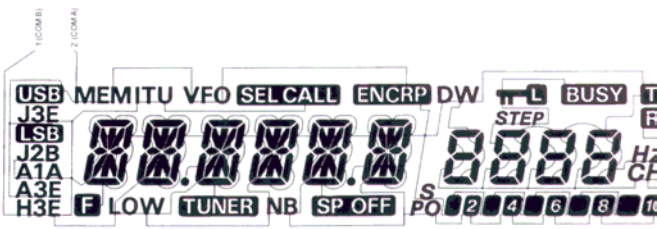
MSM5265GS
(Q3001)



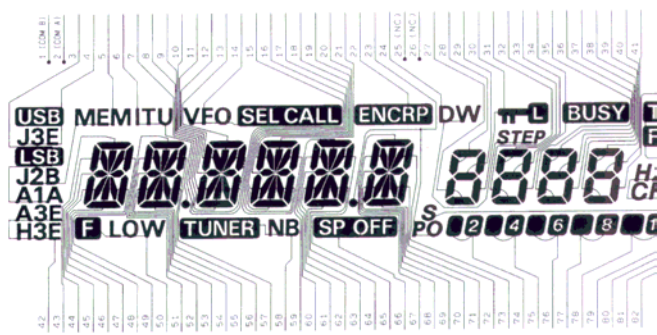
2SC2812 (L6)
(Q3002)



1SS184 (B3)
(D3037)

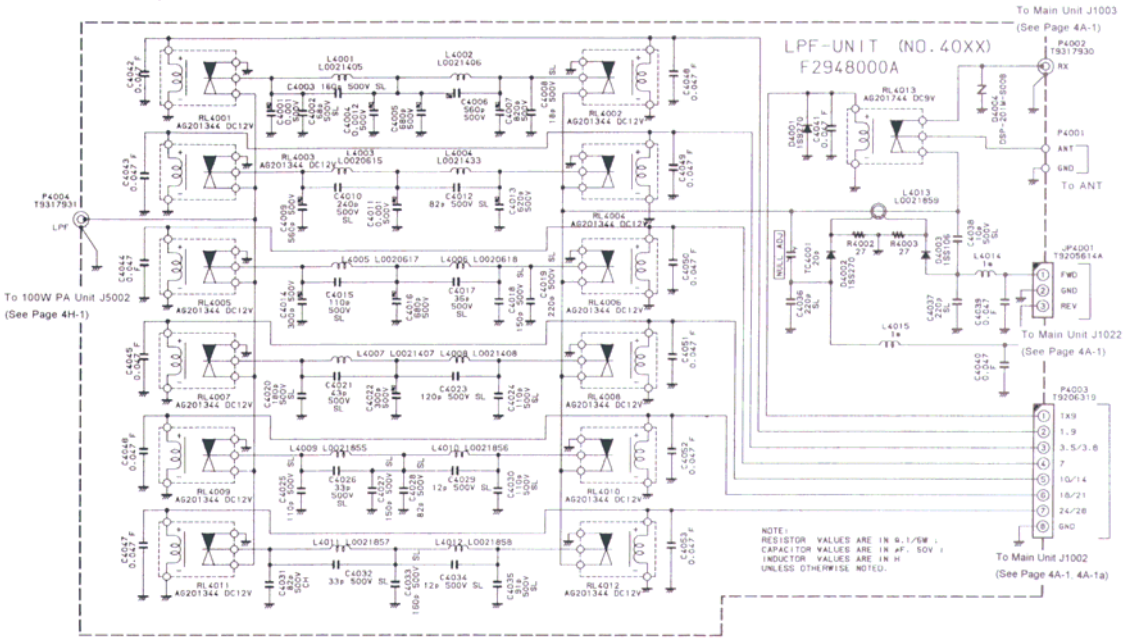


LCD Backplane Circuit Diagram

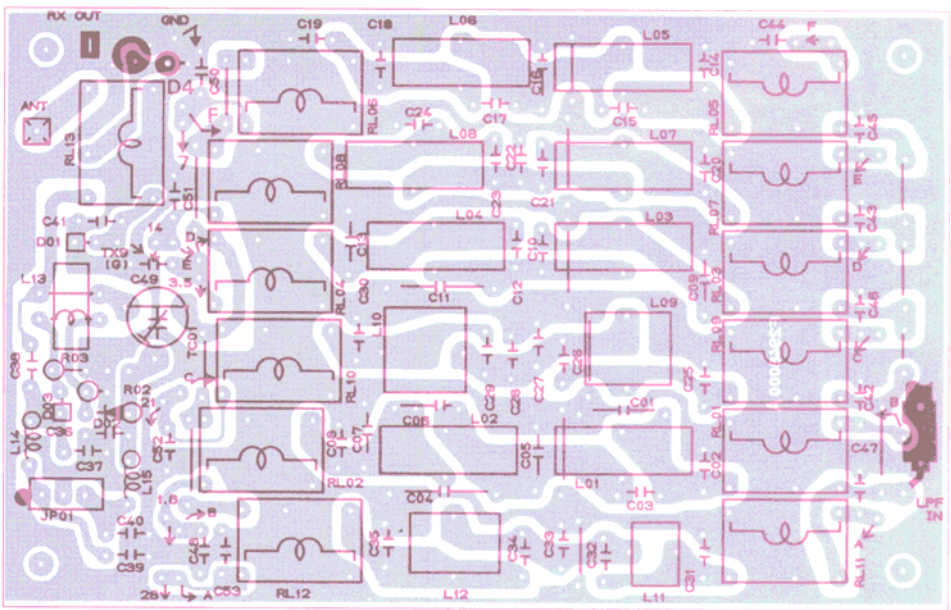


LCD Segmentation Circuit Diagram

Circuit Diagram

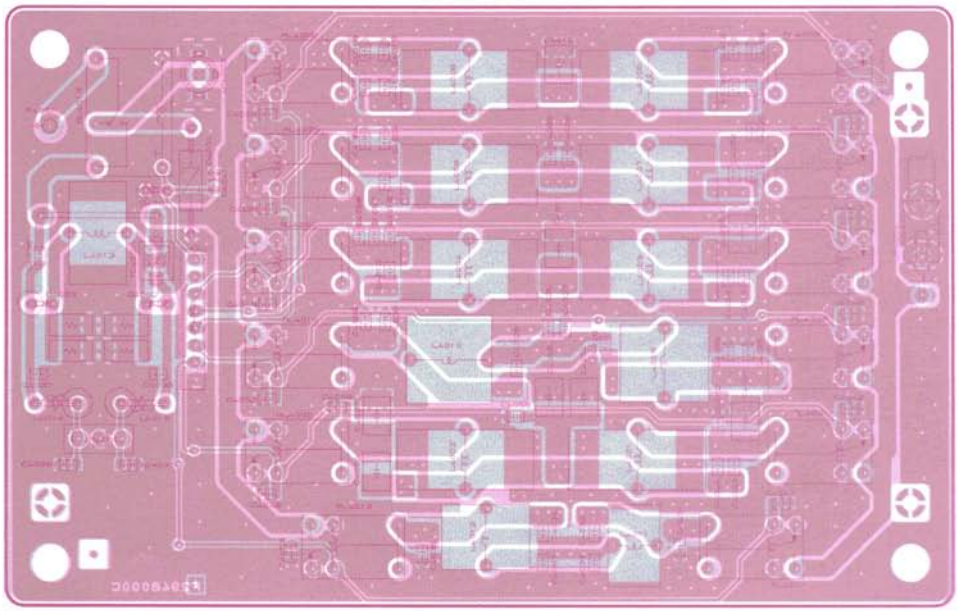


Parts Layout

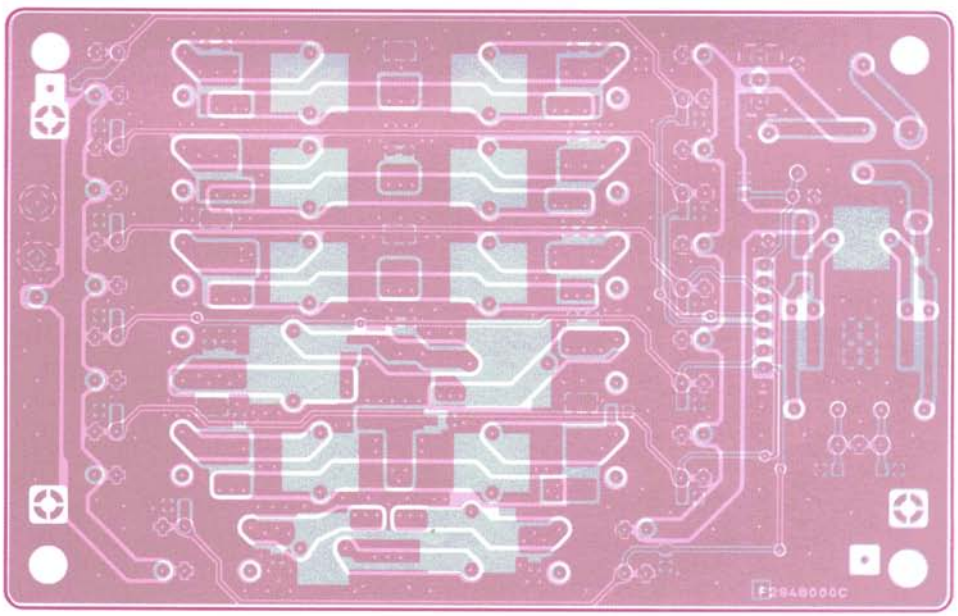


Component Side

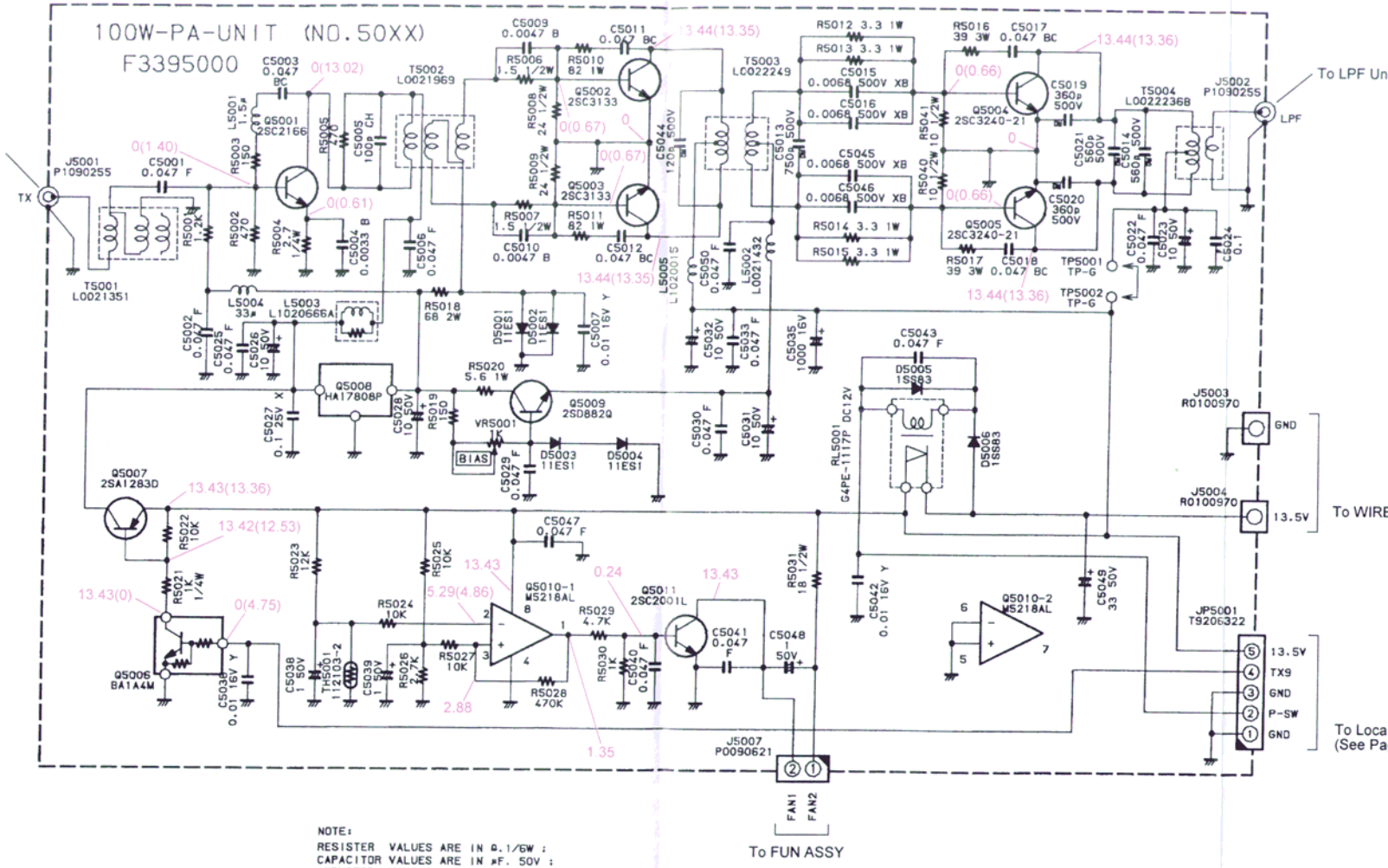
Parts Layout



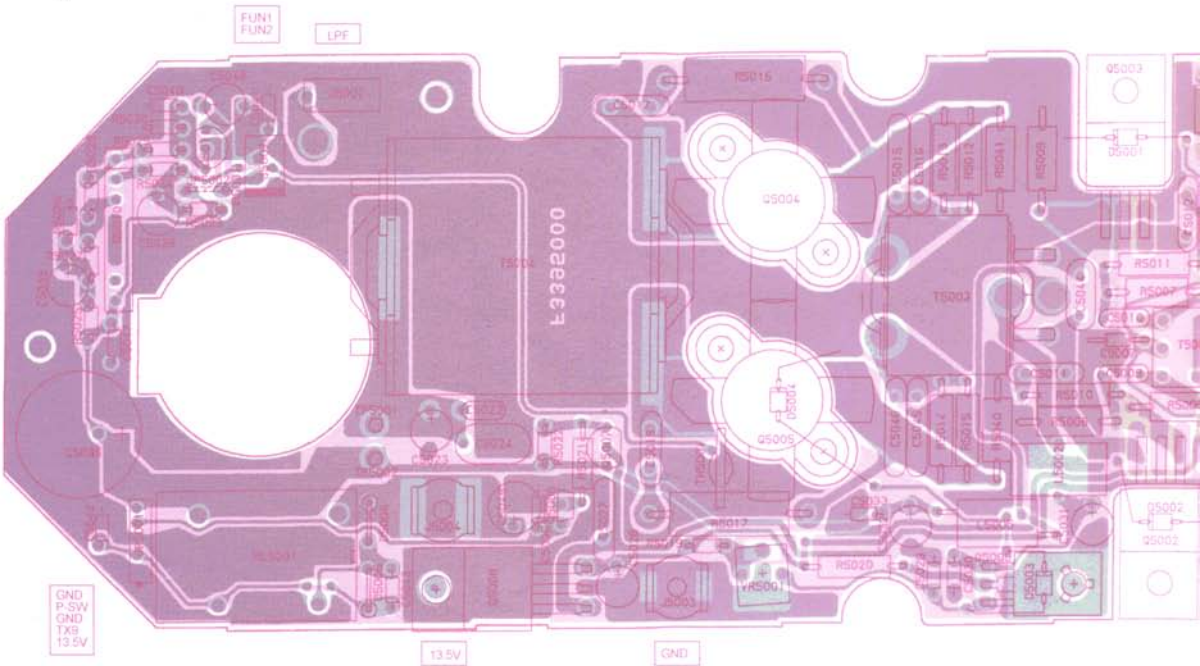
Component Side

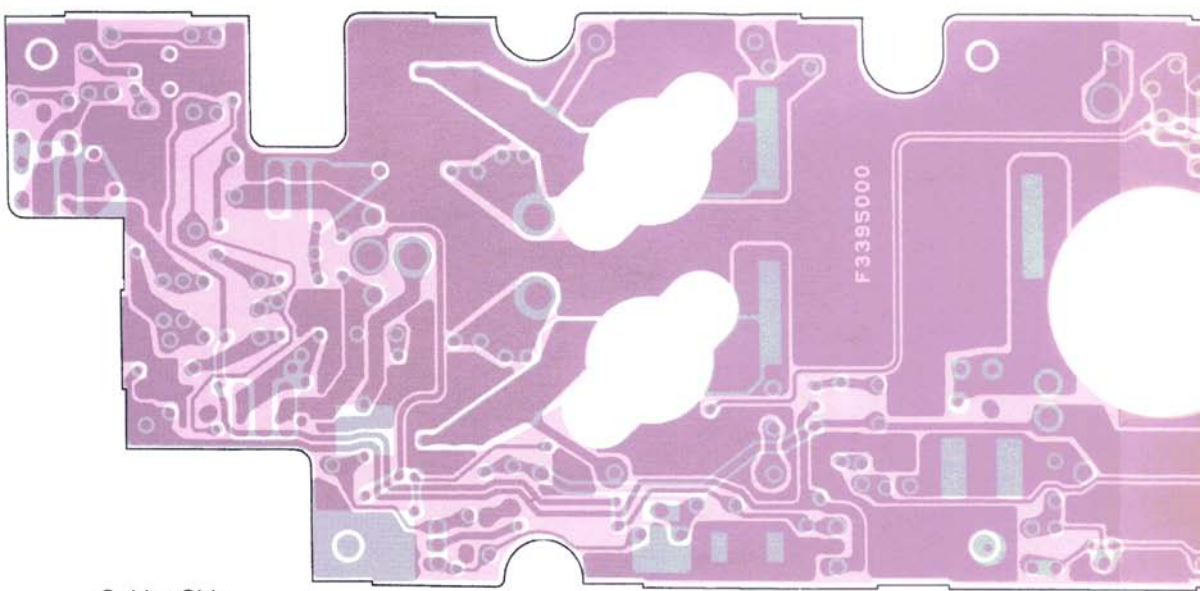


Solder Side

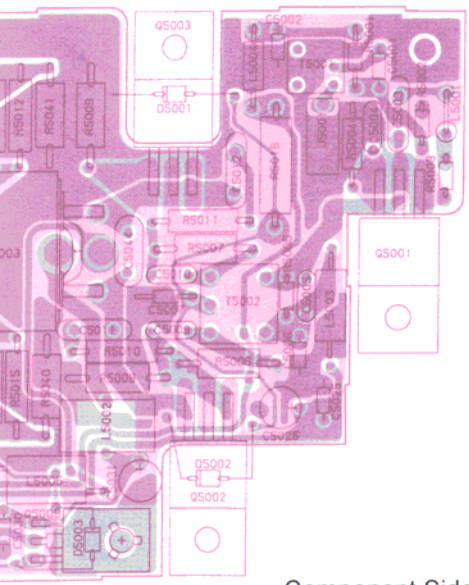

100W-PA-UNIT (NO.50XX)
F3395000
TX
J5001 P1090255
C5001 0.047 F
T5001 L0021351
C5003 0.047 BC
Q5001 2SC2166
TS002 L0021969
Q5002 2SC3133
Q5003 2SC3133
C5009 0.0047 B
C5010 0.0047 B
C5011 0.047 BC
C5012 0.047 BC
C5044 125 500V
L5005 L1020015
TS003 L0022249
C5050 0.047 F
L5002 L0021432
C5013 750p 500V
C5045 0.0068 500V XB
C5046 0.0068 500V XB
C5016 0.0068 500V XB
R5012 3.3 1W
R5013 3.3 1W
R5014 3.3 1W
R5015 3.3 1W
R5016 39 3W
R5017 39 3W
C5017 0.047 BC
C5018 0.047 BC
Q5004 2SC3240-21
Q5005 2SC3240-21
C5019 360p 500V
C5020 360p 500V
TS004 L0022368
C5021 560p 500V
C5014 560p 500V
J5002 P1090255
LPF
To LPF Un
TPS001 TP-G
TPS002 TP-G
C5022 0.047 F
C5023 10 50V
C5024 0.1
T5001 L0021351
L5004 33μ
L5003 L1020666A
C5002 0.047 F
C5025 0.047 F
C5026 10 50V
R5018 68 2W
D5001 1SS1
D5002 1SS1
C5007 0.01 16V Y
C5032 10 50V
C5033 0.047 F
C5035 1000 16V
C5043 0.047 F
D5005 1SS83
RL5001 G4PE-1117P DC12V
D5006 1SS83
Q5008 HA17808P
R5020 5.6 1W
Q5009 2SD882Q
VR5001 1K
BIAS
C5027 0.1 25V
C5028 10 50V
R5019 50
C5029 0.047 F
D5003 1SS1
D5004 1SS1
C5030 0.047 F
C5031 10 50V
J5003 R0100970
GND
J5004 R0100970
13.5V
To WIRE
Q5007 2SA1283D
R5022 10K
R5021 1K 1/4W
R5023 12K
R5025 10K
C5047 0.047 F
R5024 10K
R5027 10K
R5026 2.7K
R5028 470K
R5029 4.7K
R5030 1K
C5040 0.047 F
Q5010-1 M5218AL
Q5011 2SC2001L
C5041 0.047
C5048 1 50V
R5031 18 1/2W
C5042 0.01 16V Y
C5049 33 50V
Q5010-2 M5218AL
Q5006 BA1A4M
C5038 1 50V
C5039 1 50V
C5036 0.01 16V Y
TH5001 112103-2
JP5001 T9206322
13.5V
TX9
GND
P-SW
GND
To Loca
(See Pa
J5007 P0090621
FAN1
FAN2
To FUN ASSY
13.44(13.35)
13.44(13.36)
13.43(13.36)
13.42(12.53)
13.43
13.43(0)
0(4.75)
5.29(4.86)
0.24
2.88
1.35
0(0.66)
0(0.67)
0(0.61)
0(1.40)
0(13.02)
NOTE:
RESISTER VALUES ARE IN Ω,1/6W :
CAPACITOR VALUES ARE IN μF, 50V :
INDUCTOR VALUES ARE IN H
UNLESS OTHERWISE NOTED.
RX(TX)

Parts Layout

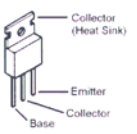




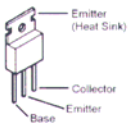
Solder Side



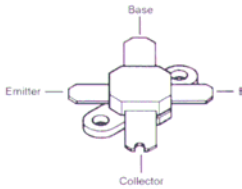
Component Side



2SC2166
(Q5001)



2SC3133
(Q5002,5003)



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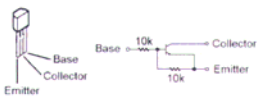

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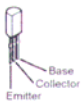

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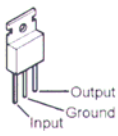
(Q5007)
2SC2001
(Q5011)



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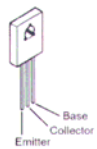

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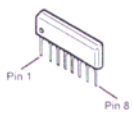

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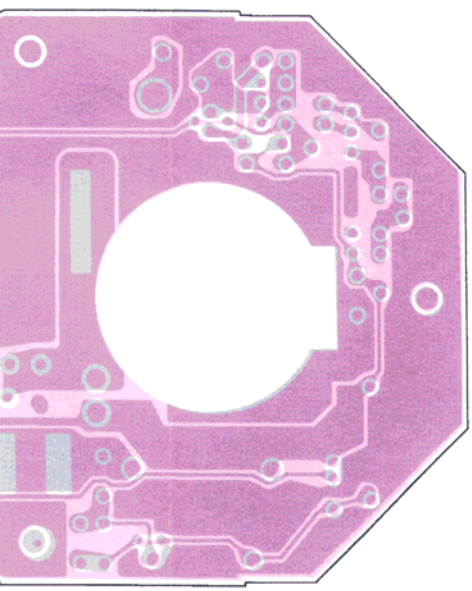

Circuit Diagram