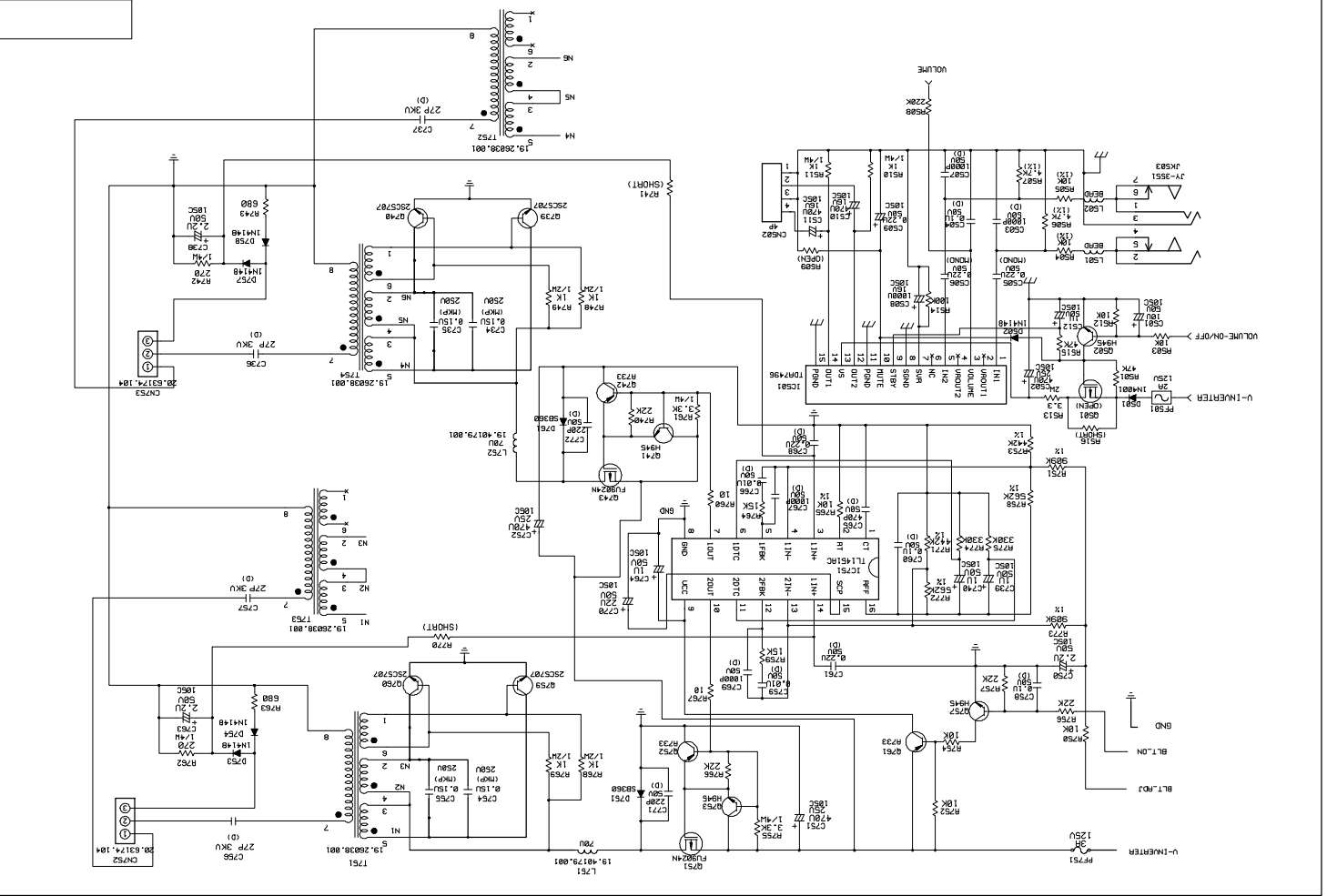
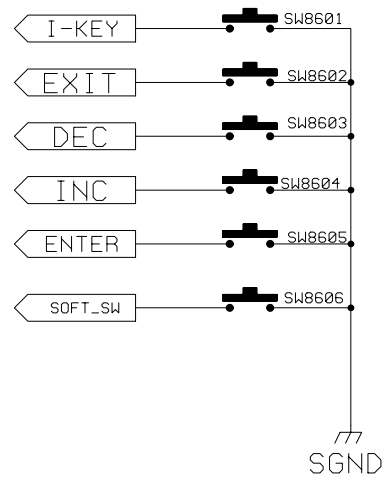
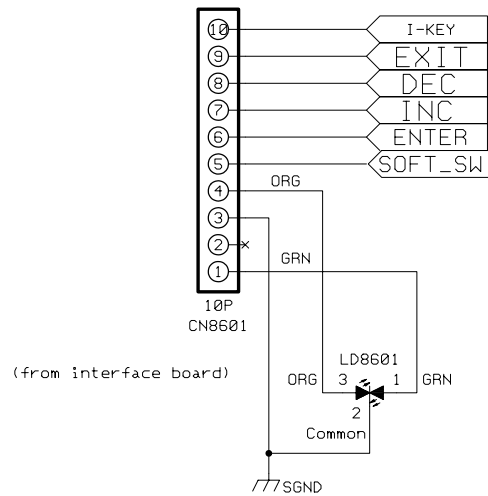


NOTES: Resistor values are in ohm, K=1,000 ohm, M=1,000,000 ohm
2. All resistors are 1/8 watt, 5% except where otherwise indicated
3. // ∇ ∇ Represents PCB common ground.

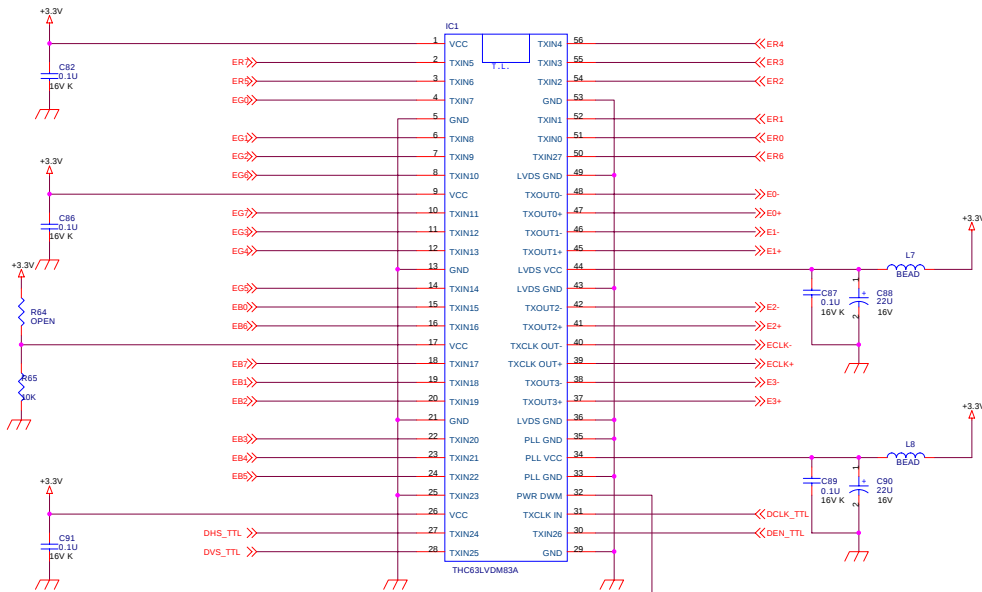
Power Board
DUI72



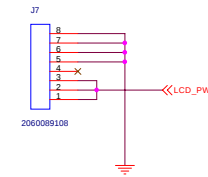
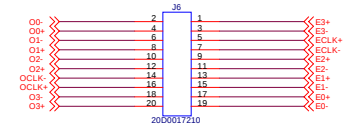
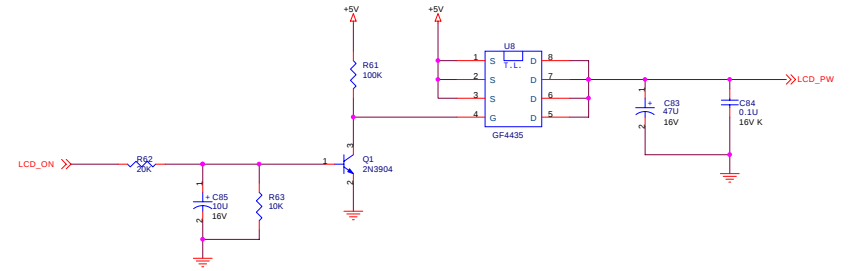
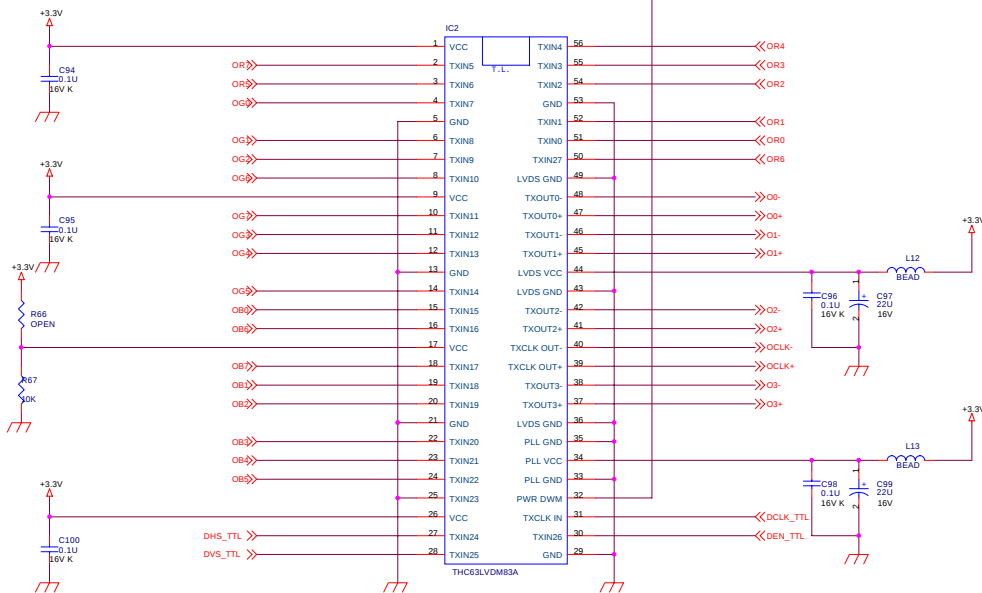


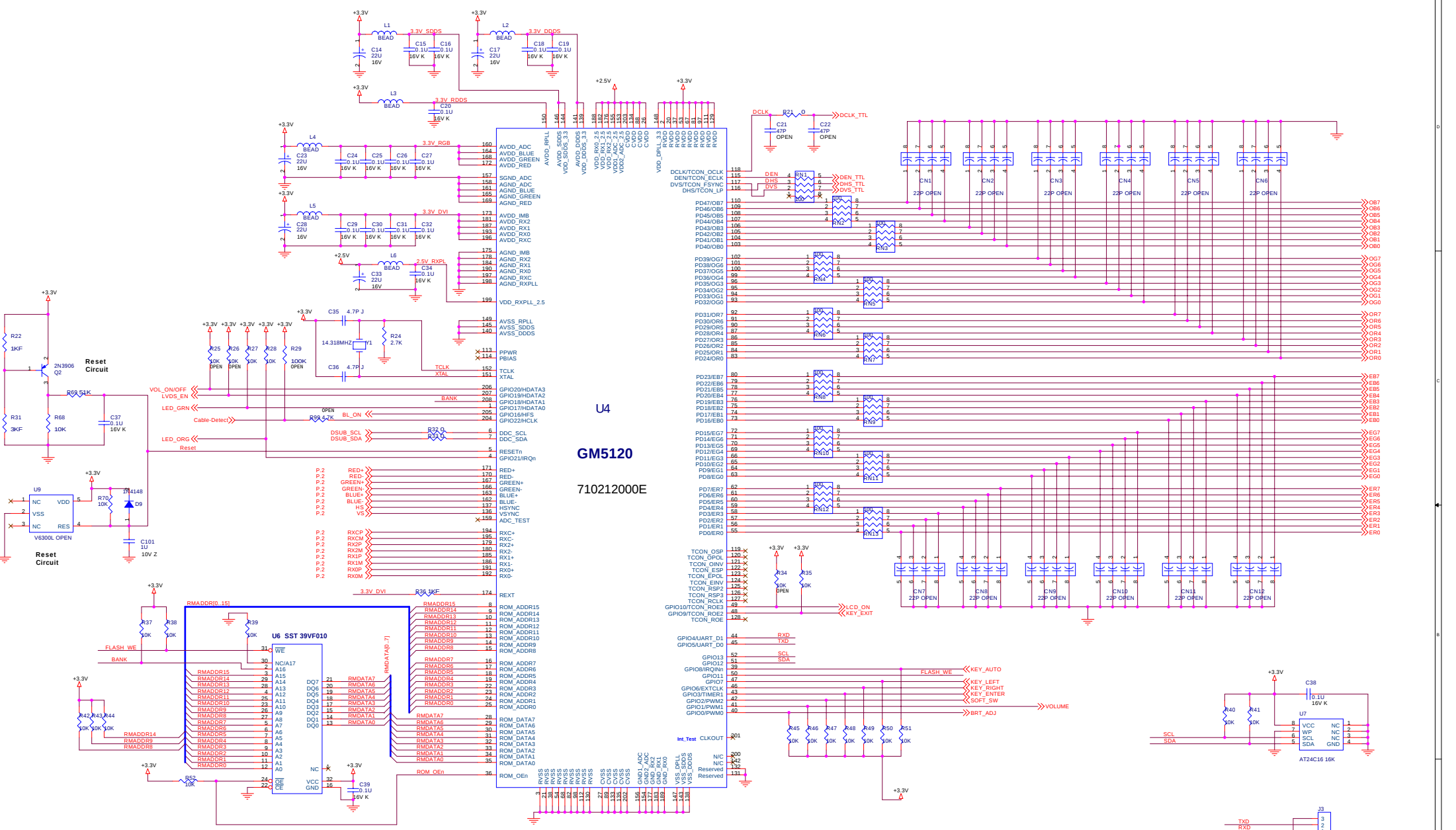
- NOTES:
1. Resistor values are in ohm, K=1,000 ohm, M=1,000,000 ohm
 2. All resistors are 1/8 watt, 5% except where otherwise indicated
 3. $\text{---} \nabla \text{---}$ Represents PCB common ground.

Dir
JU172
Control Board

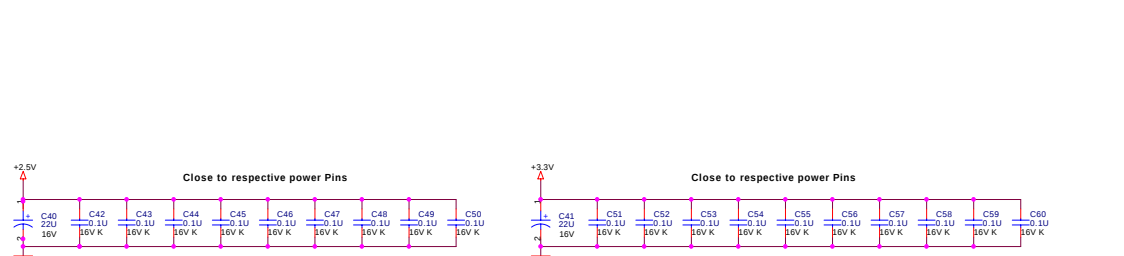


"L" Falling edge trigger
 "H" Rising edge trigger

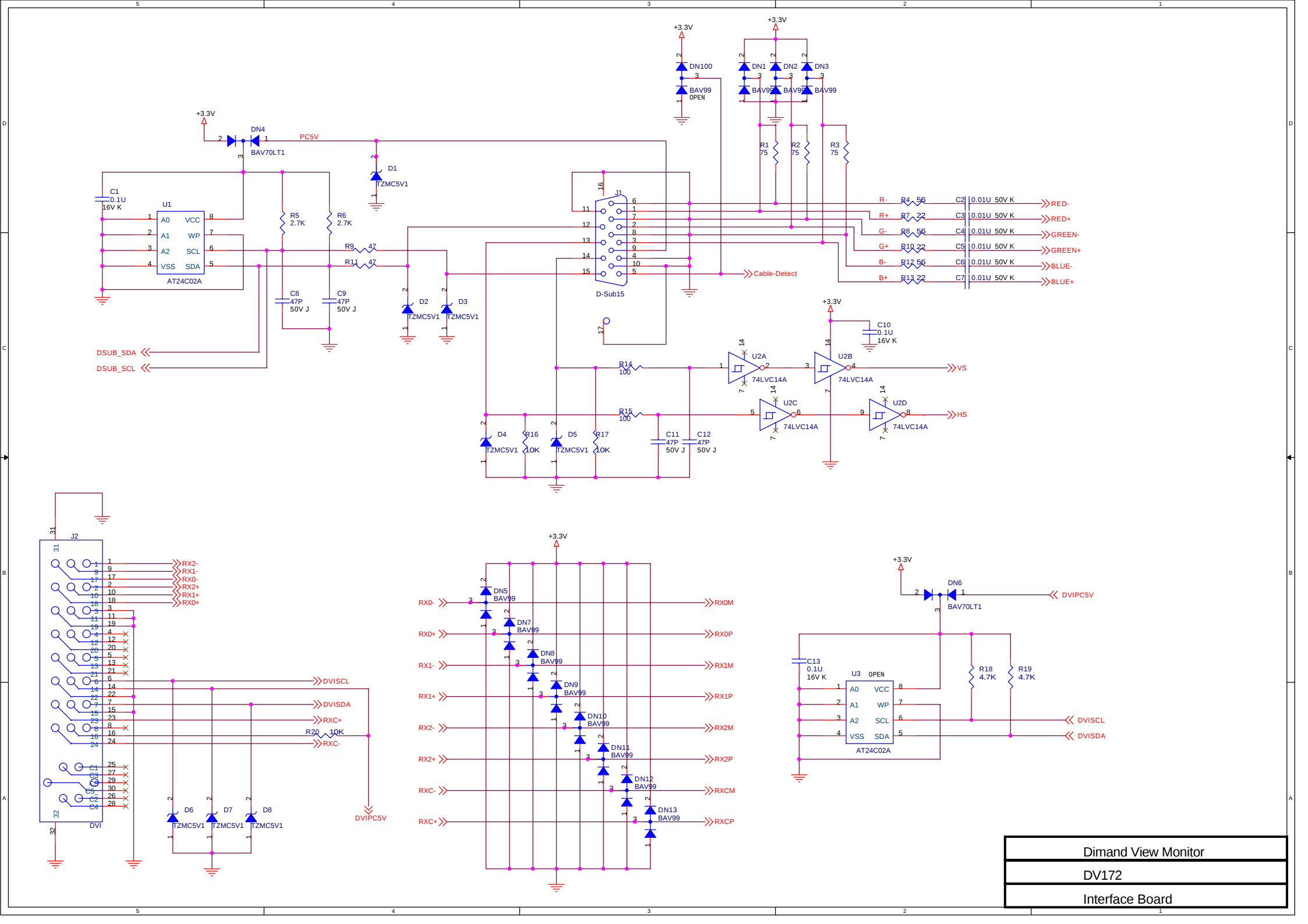




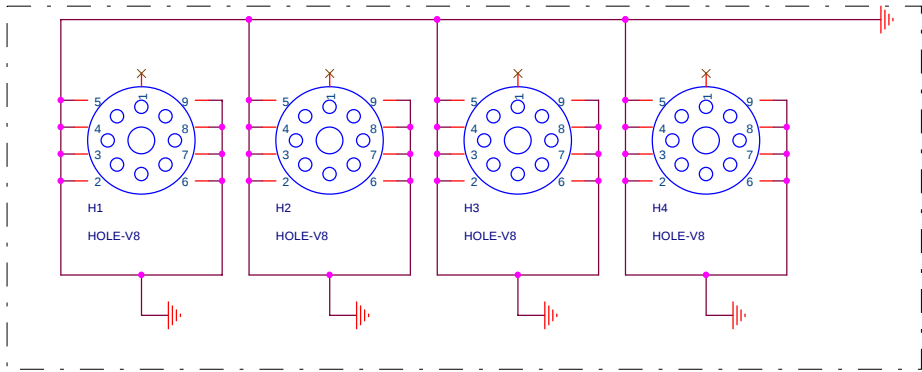
BOOTSTRAP SIGNALS			
ADDRESS	NAME	SET	DESCRIPTION
ROM_ADDR(4:0)	USER_BITS(4:0)	x	Available for reading from a status register
ROM_ADDR5	Reserved	x	If using 6-wire host protocol, program this bit to 0
ROM_ADDR6	SCLPOL	0	Determines polarity of HCLK signal
ROM_ADDR7	HOST_PROTOCOL	0	If using 6-wire host protocol, program this bit to 1
ROM_ADDR8	HOST_PORT_EN	1	GPIO(22:16) is on "Host Port" pins
ROM_ADDR9	OCM_START	1	1 = OCM becomes active after OCM_CLK is stable
ROM_ADDR(12:10)	USER_BITS(7:5)	x	Available for reading from a status register
ROM_ADDR13	OSC_SEL	0	0 = XTAL and TCLK pins are connected
ROM_ADDR14	OCM_ROM_CFG(1)	1	1 = All 48K of ROM is in external ROM



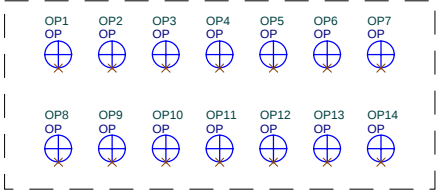
Dimand View Monitor
DV172
Interface Board



Screw Holes



Optical Points



Dimand View Monitor

DV172

Interface Board