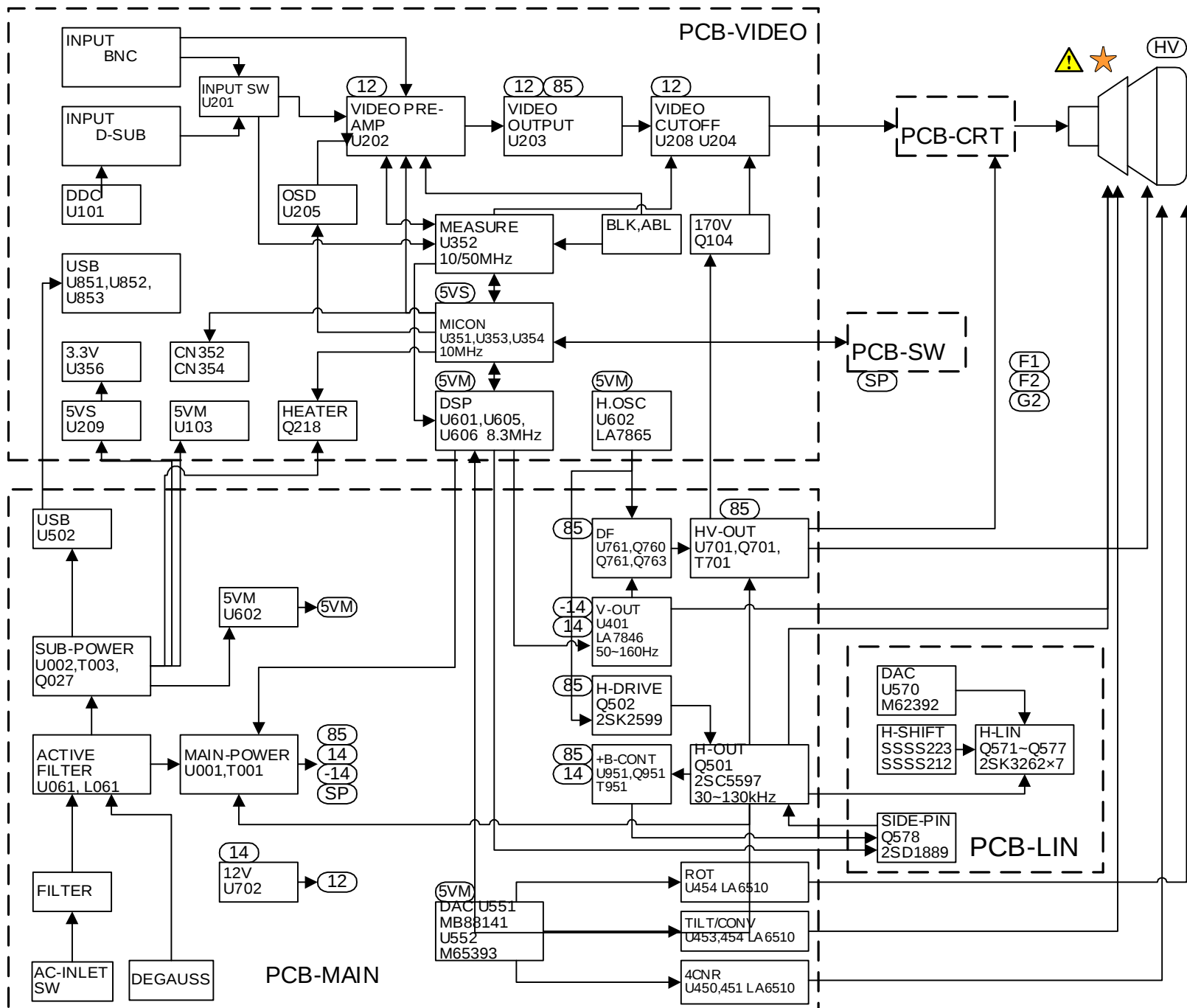
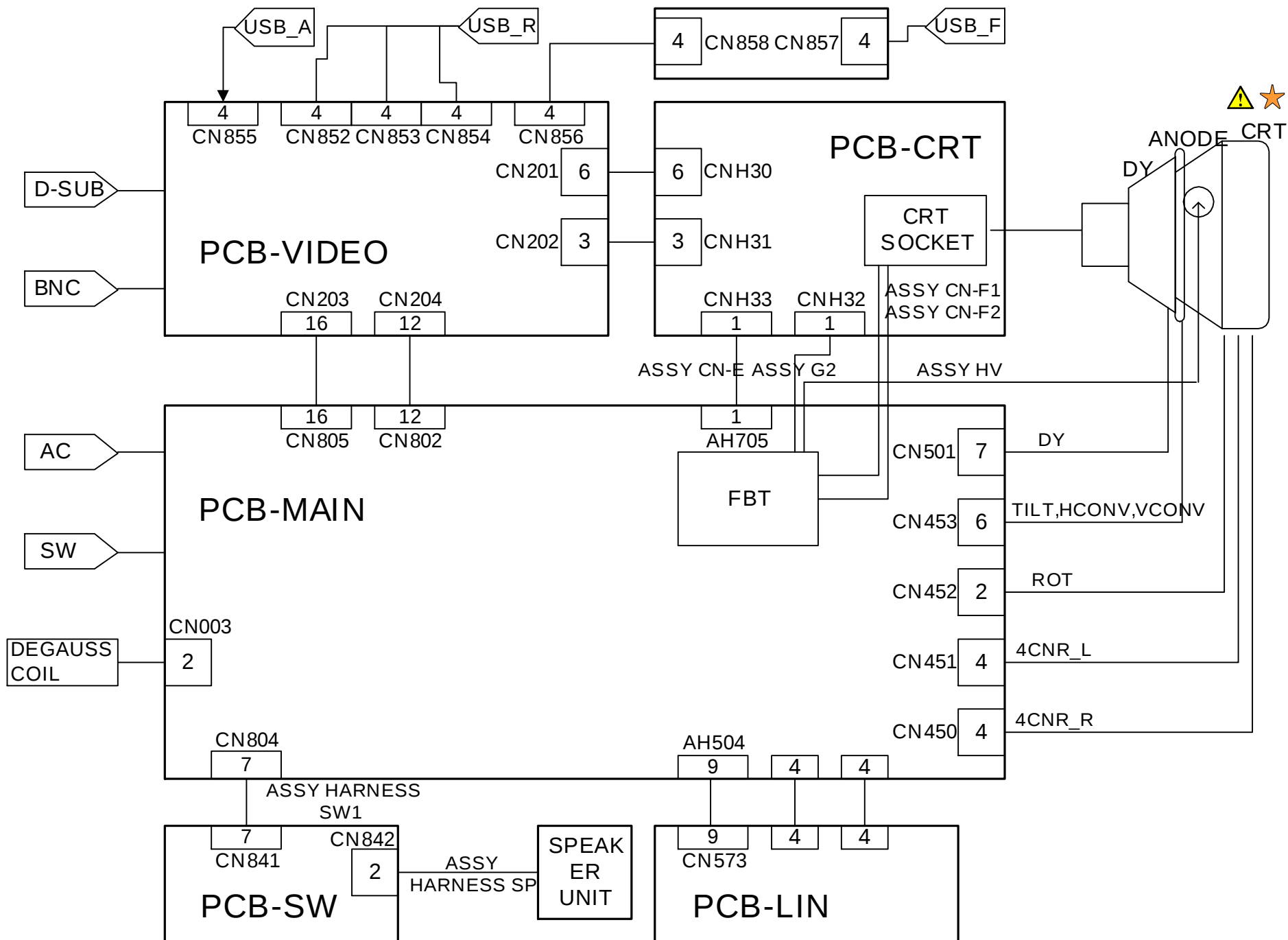
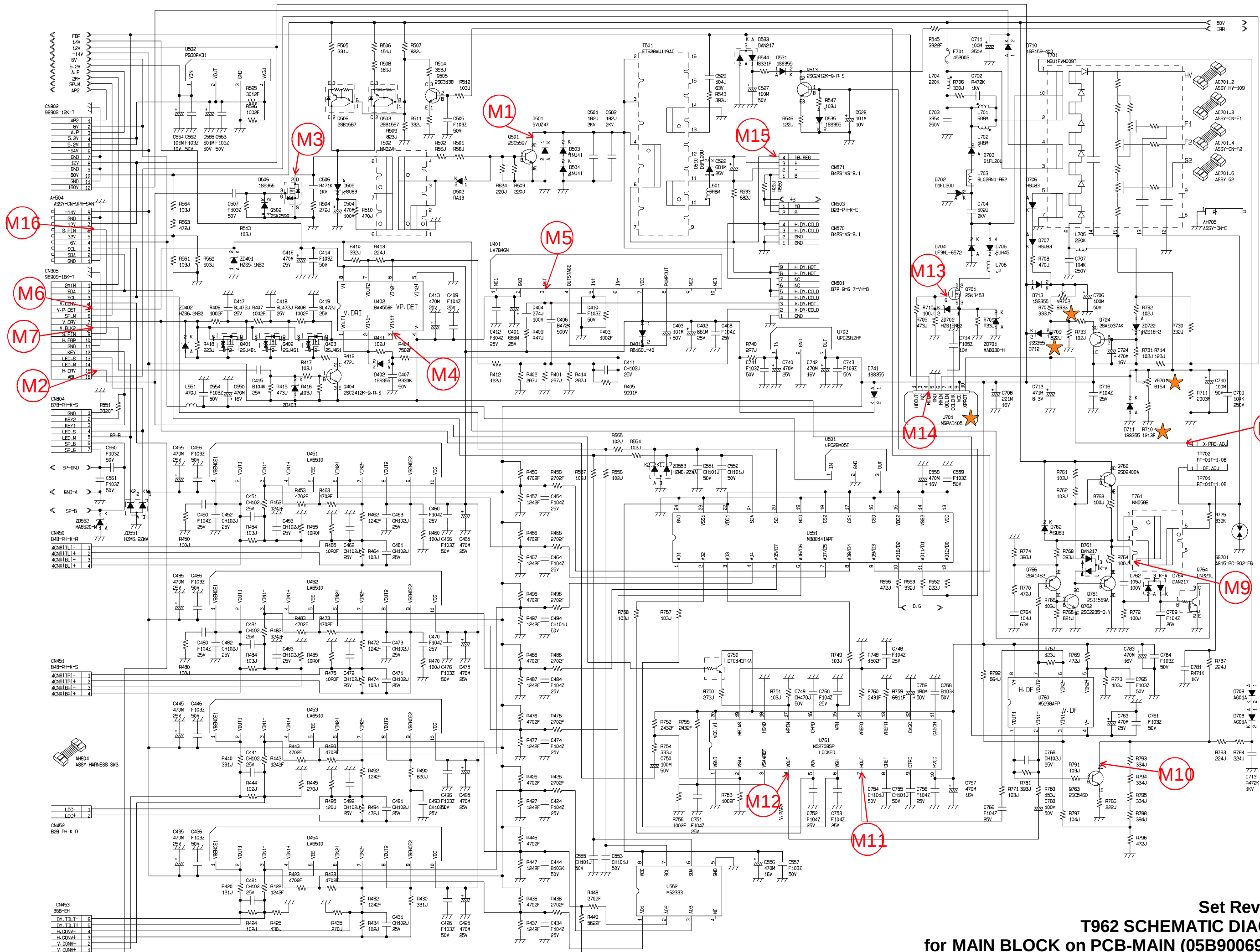


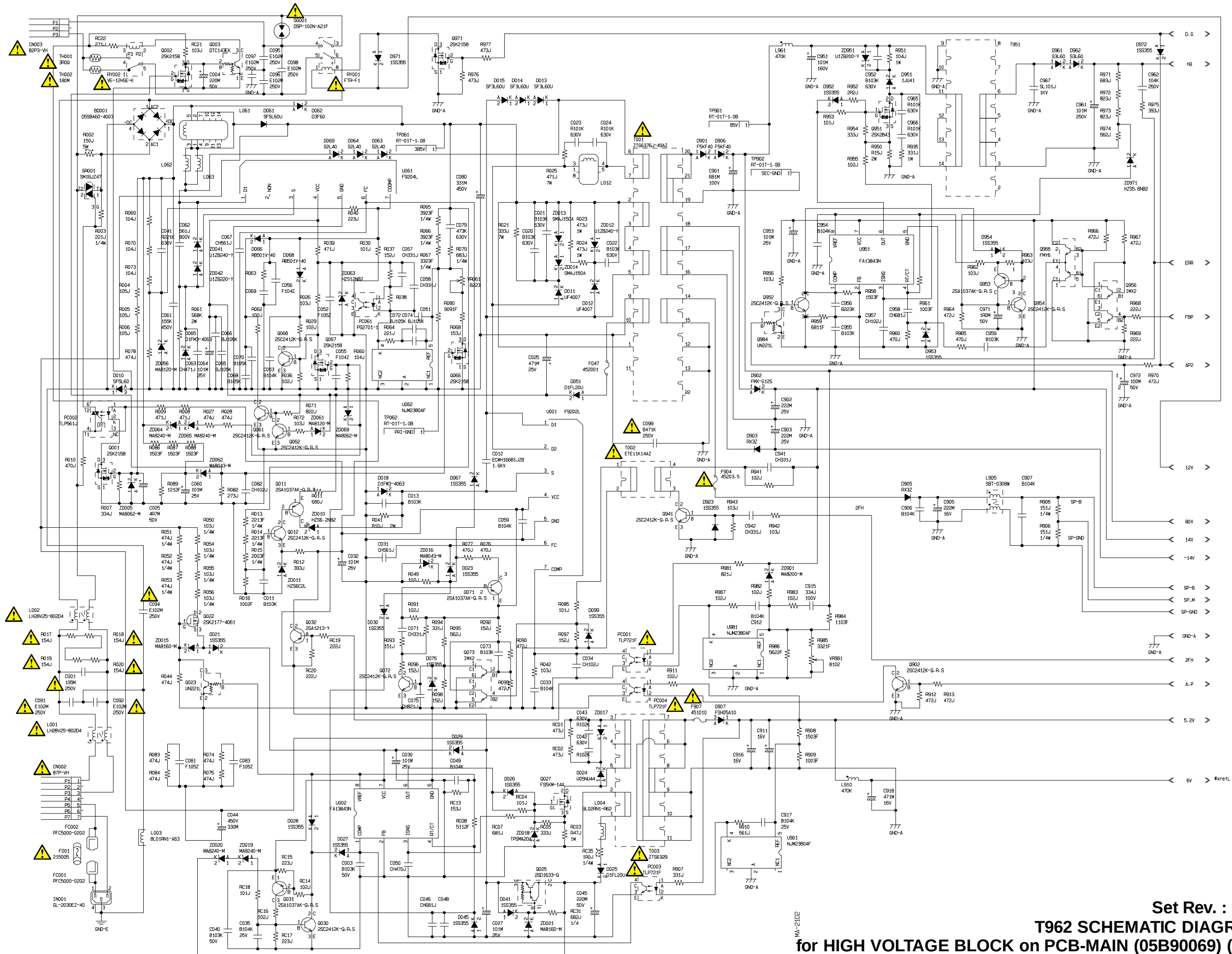




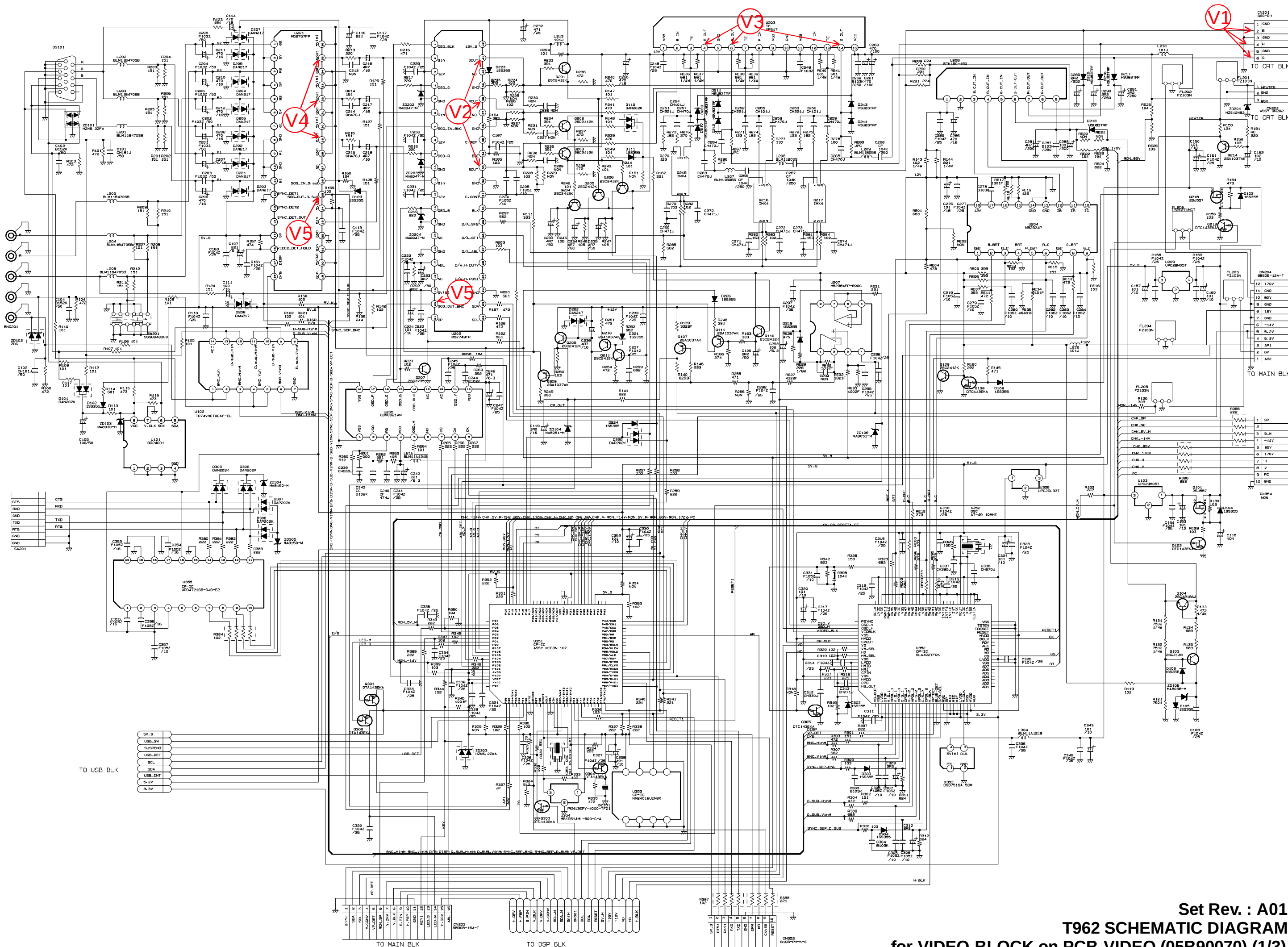
SET REV. : A01
T962 CONNECTION DIAGRAM

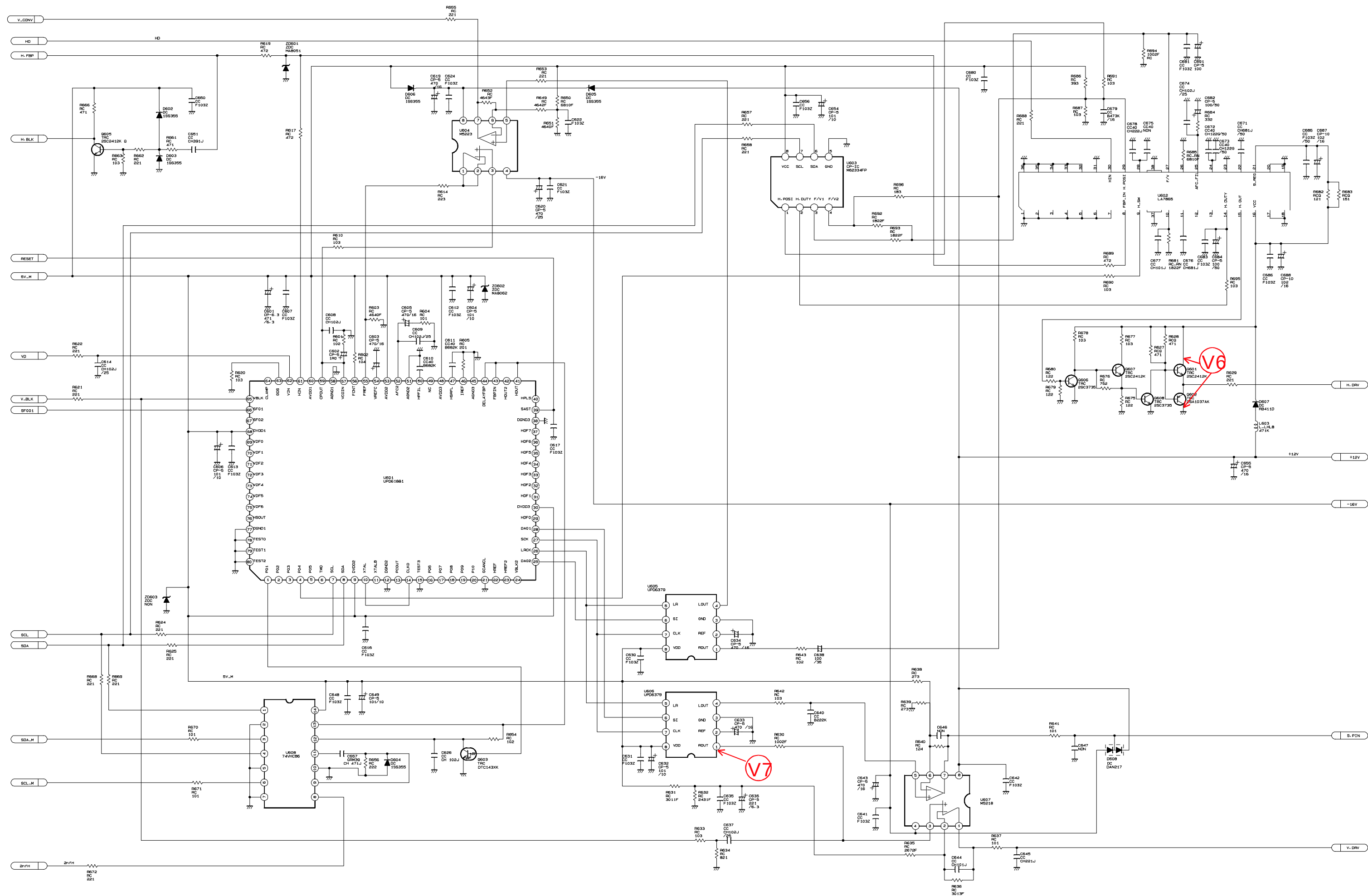


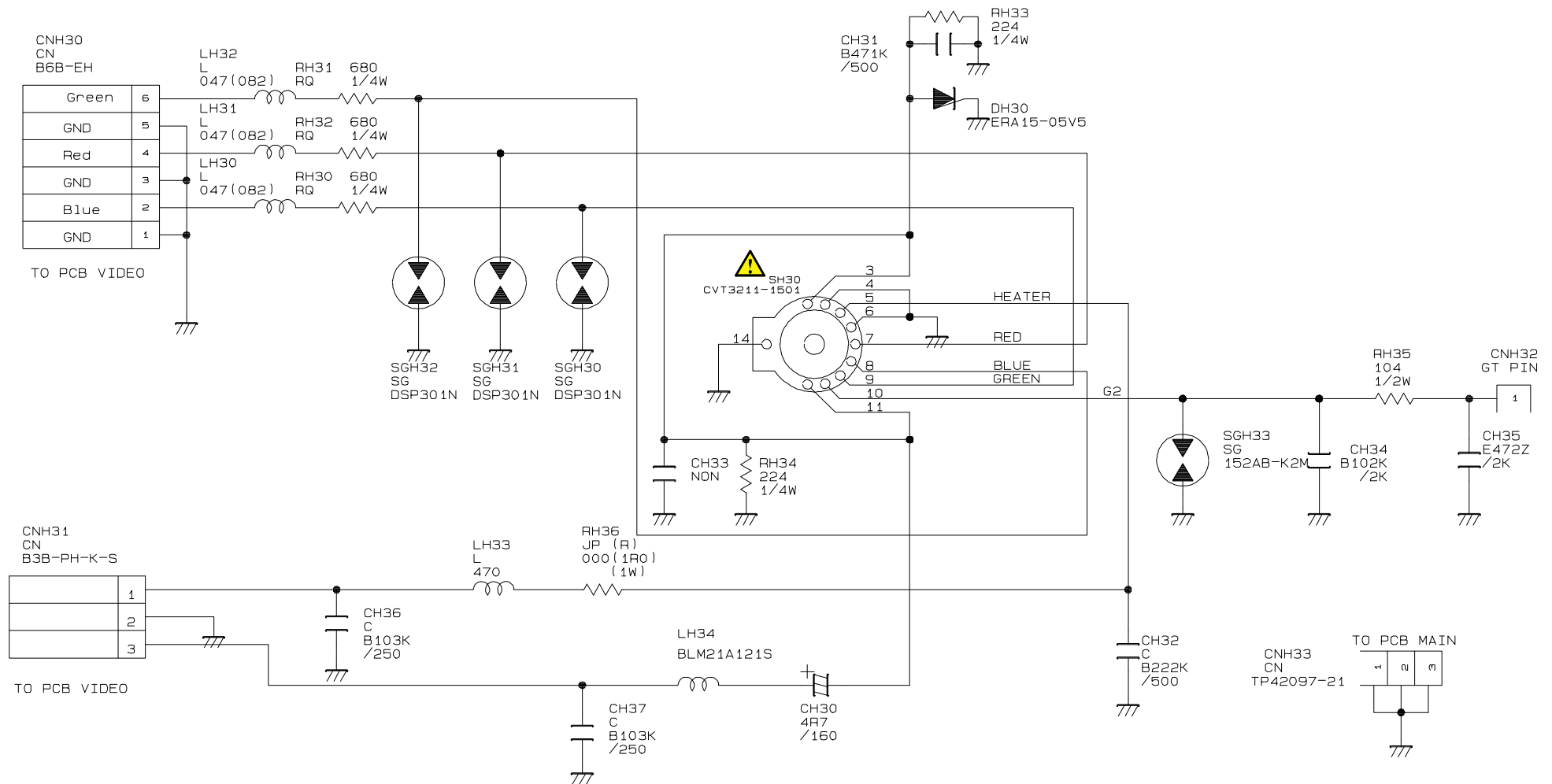
Set Rev. : A01
T962 SCHEMATIC DIAGRAM
for MAIN BLOCK on PCB-MAIN (05B90069) (1/2)



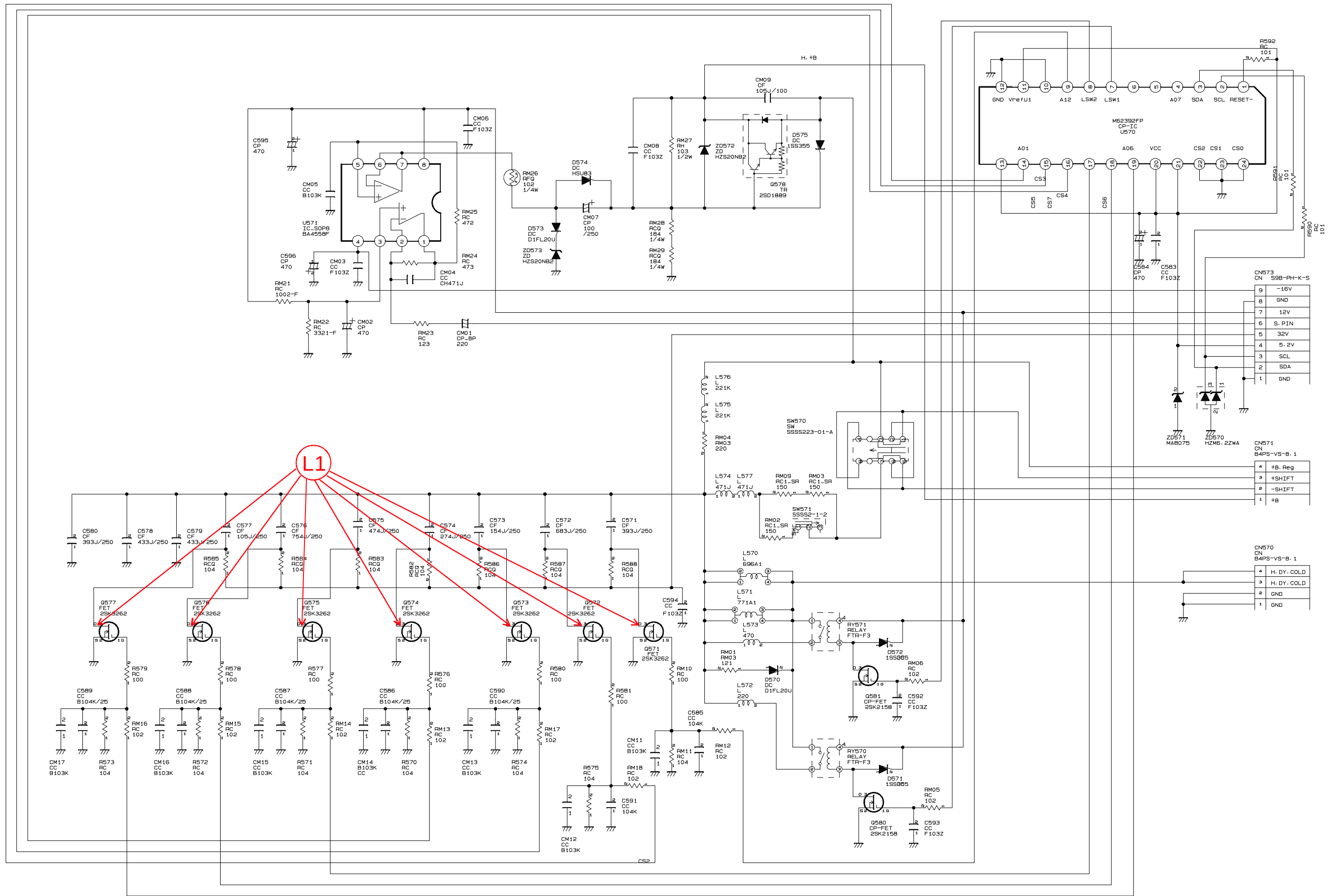
Set Rev. : A01
T962 SCHEMATIC DIAGRAM
for HIGH VOLTAGE BLOCK on PCB-MAIN (05B90069) (2/2)







SCHEMATIC DIAGRAM for PCB-CRT (5D90044)



MA-21D2 CS Capacitor switching timing

Frequency	L2	L1	CS7	CS6	CS5	CS4	CS3	CS2	CS1
130~123k	1	1	0	0	0	0	0	0	0
123~115k	1	1	0	0	0	0	0	0	1
115~103k	1	1	0	0	0	0	0	0	1
103~95k	1	1	0	0	0	0	0	0	1
95~90k	1	1	0	0	0	0	0	0	1
90~83k	1	1	0	0	0	0	0	0	1
83~78k	0	1	0	0	0	0	0	1	1
78~72k	0	1	0	0	0	0	0	1	1
72~67k	0	1	0	0	0	0	1	1	0
67~63k	1	0	0	0	0	1	1	0	0
63~57k	1	0	0	0	0	1	1	0	0
57~52k	1	0	0	0	1	1	0	0	0
52~47.5k	1	0	0	0	1	1	1	0	0
47.5~40k	0	0	1	0	1	1	0	0	1
40~34.5k	0	0	1	0	1	1	0	0	1
34.5~	0	0	1	1	1	1	1	1	1

T962 SCHEMATIC DIAGRAM for PCB-LIN (5C90131)

