



DDC/Power saving/Auto Picture Adjustment

(LG PANEL)

Service Manual

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SAFETY NOTICE

ANY PERSON ATTEMPTING TO SERVICE THIS CHASSIS MUST FAMILIARIZE HIMSELF WITH THE CHASSIS AND BE AWARE OF THE NECESSARY SAFETY PRECAUTIONS TO BE USED WHEN SERVICING ELECTRONIC EQUIPMENT CONTAINING HIGH VOLTAGES.

CAUTION: USE A SEPARATE ISOLATION TRANSFORMER FOR THIS UNIT WHEN SERVICING.

REFER TO BACK COVER FOR IMPORTANT SAFETY GUIDELINES

Important Safety Notice

◀◀ Go to cover page

Proper service and repair is important to the safe, reliable operation of all Maxdata Company** Equipment. The service procedures recommended by Maxdata and described in this service manual are effective methods of performing service operations. Some of these service operations require the use of tools specially designed for the purpose. The special tools should be used when and as recommended.

It is important to note that this manual contains various CAUTIONS and NOTICES which should be carefully read in order to minimize the risk of personal injury to service personnel. The possibility exists that improper service methods may damage the equipment. It is also important to understand that these CAUTIONS and NOTICES ARE NOT EXHAUSTIVE. Maxdata could not possibly know, evaluate and advise the service trade of all conceivable ways in which service might be done or of the possible hazardous consequences of each way. Consequently, Maxdata has not undertaken any such broad evaluation. Accordingly, a servicer who uses a service procedure or tool which is not recommended by Maxdata must first satisfy himself thoroughly that neither his safety nor the safe operation of the equipment will be jeopardized by the service method selected.

* * Hereafter throughout this manual, Maxdata Company will be referred to as Maxdata.

WARNING

Critical components having special safety characteristics are identified with a  by the Ref. No. in the parts list and enclosed within a broken line* (where several critical components are grouped in one area) along with the safety symbol  on the schematics or exploded views.

Use of substitute replacement parts which do not have the same specified safety characteristics may create shock, fire, or other hazards.

Under no circumstances should the original design be modified or altered without written permission from Maxdata. Maxdata assumes no liability, express or implied, arising out of any unauthorized modification of design. Servicer assumes all liability.

* Broken Line



FOR PRODUCTS CONTAINING LASER :

- DANGER-** Invisible laser radiation when open.
AVOID DIRECT EXPOSURE TO BEAM.
- CAUTION-** Use of controls or adjustments or performance of procedures other than those specified herein may result in hazardous radiation exposure.
- CAUTION-** The use of optical instruments with this product will increase eye hazard.

TO ENSURE THE CONTINUED RELIABILITY OF THIS PRODUCT, USE ONLY ORIGINAL MANUFACTURER'S REPLACEMENT PARTS, WHICH ARE LISTED WITH THEIR PART NUMBERS IN THE PARTS LIST SECTION OF THIS SERVICE MANUAL.

Take care during handling the LCD module with backlight unit

- Must mount the module using mounting holes arranged in four corners.
- Do not press on the panel, edge of the frame strongly or electric shock as this will result in damage to the screen.
- Do not scratch or press on the panel with any sharp objects, such as pencil or pen as this may result in damage to the panel.
- Protect the module from the ESD as it may damage the electronic circuit (C-MOS).
- Make certain that treatment person's body are grounded through wrist band.
- Do not leave the module in high temperature and in areas of high humidity for a long time.
- Avoid contact with water as it may cause a short circuit within the module.
- If the surface of panel become dirty, please wipe it off with a soft material. (Cleaning with a dirty or rough cloth may damage the panel.)

Technical Specifications

1.1 LCD

Type NR.	: LM181E05-C3 (LG-Philips)
Outside dimensions	: 389(w)317.2(h)27(d) (Typ) mm
Pitch (mm)	: 0.2805 (per one triad) x 0.2805mm
Color pixel arrangement	: RGB vertical stripes
Display surface	: low reflection, antiglare with hard coating
Number of color	: 256 gray levels(8 bits)
Backlight	: CCFL edge light system
Active area(WxH)	: 359x287.2mm (18" diagonal)
View angle	: Horizontal & Vertical 160 degree (CR>=10)
Contrast ratio	: 350:1
White luminance	: 250 nits (Typ)

1.2 Scanning frequencies

Hor.	: 30 ~ 82 K Hz
Ver.	: 56 ~ 76 Hz

1.3 Video dot rate

	: < 140 MHz
--	-------------

1.4 Power input

	: 90-264 V AC, 50/60 2 Hz
--	---------------------------

1.5 Power consumption

	: < 60 W maximum (exclude USB option)
--	---

1.6 Dimensions

	: 426 * 454 * 180 mm
--	----------------------

1.7 Weight

	: 8.4 KGS
--	-----------

1.8 Functions :

D-shell analog R/G/B separate inputs, H/V sync separated,

Composite (H+V) TTL level,

DVI digital Panel Link TMDS input

1.9 Ambient temperature

	: 5 C - 35 C
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2 Electrical characteristics

2.1 Interface signals

The input signals can be applied in three different modes :

1). D-shell Analog

Input signal : Video, Hsync., Vsync

Video : 0.7 Vp-p, input impedance, 75 ohm @DC

Sync. : Separate sync TTL level , input impedance 2k2 ohm

terminate

Hor. sync Positive/Negative

Ver. sync Positive/Negative

Composite sync TTL level, input impedance 2k2 ohm terminate

(Positive/Negative)

Sync on green video 0.3 Vp-p Negative (Video 0.7 Vp-p Positive)

2). Intel DVI Digital

Input signal : Single channel TMDS signal

2.1.1 Audio

Input signal level : 500mVrms

Headphone output signal level : 32ohm 0.7+0.7 Vrms

Input signal connector : 3.5 mm mini jack

Loud speaker : 2+2 W stereo firing

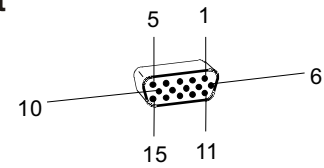
2.1.2 Microphone

Sensitivity: -65 5 dB re 1V/ubar at 1khz

Output impedance : 600 ohm typical

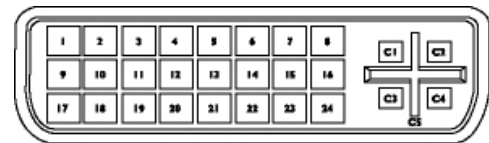
Pin Assignment

D-Sub connector



Pin No.	Assignment
1	Red video input
2	Green video input
3	Blue video input
4	Ground
5	Not connected
6	Red return
7	Green return
8	Blue return
9	+ 5v DDC
10	Ground
11	Ground
12	SDA (DDC data)
13	Hor. Sync
14	Vert. Sync
15	SCL (DDC Clock)

The digital/analog DVI-I onnector contains 29 signal contacts organized in three rows of eight contacts. Signal pin assignments are listed in the following table:



Pin No.	Description
1	TMDS data2-
2	TMDS data2+
3	TMDS data2 shield
4	NC
5	NC
6	DDC clock
7	DDC data
8	Not connected
9	TMDS data1-
10	TMDS data1+
11	TMDS data1 shield
12	NC
13	NC
14	+5V
15	Ground(røturn for +5V and H/Vsync)
16	Hot plug detect
17	TMDS data0-
18	TMDS data0+
19	TMDS data0 shield
20	NC
21	NC
22	TMDS clock shield
23	TMDS clock+
24	TMDS clock-
C1	Analog R
C2	Analog G
C3	Analog B
C4	Analog Hsync
C5	Analog Vert. Sync.

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Input signal mode

Factory preset 16 modes (F)

PRESET VIDEO RESOLUTION

#	Resolution	Frequency	Pixel rate	Sync	Comment
1	640X350	31.5K/70HZ	25.175	(+/-)	IBM VGA 10h
2	720X400	31.5K/70HZ	28.322	(-/+)	IBM VGA 3h
3	640X480	37.5K/75HZ	31.501	(-/-)	
4	640X480	35.0K/67HZ	30.24	(-/-)	
5	640X480	31.5K/60HZ	25.175	(-/-)	
6	800X600	35.2K/56HZ	36	(+/-)	
7	800X600	46.9K/75HZ	49.498	(+/-)	
8	800X600	37.9K/60HZ	40	(+/-)	
9	832X624	49.7K/75HZ	57.28	(+/-)	MAC
10	1024X768	60.0K/75HZ	78.75	(+/-)	
11	1024X768	48.4K/60HZ	65	(-/-)	
12	1152X870	68.7K/75HZ	100	(-/-)	MAC
13	1152X900	71.8K/76HZ	108	(+/-)	SUN Mode II
14	1280X1024	64.0K/60HZ	108	(+/-)	
15	1280X1024	80.0K/75HZ	135	(+/-)	
16	688X556	31.3K/50HZ	27	(-/+)	TV-PAL

Pre-set 32 modes (P)

PRESET VIDEO RESOLUTION

#	Resolution	Frequency	Pixel rate	Sync	Comment
1	640X350	31.5K/70HZ	25.175	(+/-)	IBM VGA 10h
2	720X400	31.5K/70HZ	28.322	(-/+)	IBM VGA 3h
3	640X480	37.5K/75HZ	31.501	(-/-)	
4	640X480	43.3K/85HZ	36	(-/-)	
5	640X480	37.9K/72HZ	31.5	(-/-)	
6	640X480	35.0K/67HZ	30.24	(-/-)	
7	640X480	31.5K/60HZ	25.175	(-/-)	
8	800X600	35.2K/56HZ	36	(+/-)	
9	800X600	46.9K/75HZ	49.498	(+/-)	
10	800X600	37.9K/60HZ	40	(+/-)	
11	800X600	53.7K/85HZ	56.251	(+/-)	
12	832X624	49.7K/75HZ	57.28	(+/-)	MAC
13	800X600	48.1K/72HZ	50	(+/-)	
14	1024X768	60.0K/75HZ	78.75	(+/-)	
15	1024X768	48.4K/60HZ	65	(-/-)	
16	1024X768	56.5K/70HZ	75	(-/-)	
17	1024X768	61.1K/76HZ	83.096	(+/-)	IBM XGA-2
18	1024X768	68.7K/85HZ	94.5	(+/-)	
19	1152X864	67.5K/75HZ	108	(+/-)	
20	1152X864	63.9K/70HZ	94.5	(+/-)	non-VESA
21	1152X864	54.0K/60HZ	79.9	(+/-)	non-VESA
22	1152X870	68.7K/75HZ	100	(-/-)	MAC
23	1152X900	61.8K/66HZ	92.94	serr-	SUN Mode IV
24	1152X900	71.8K/76HZ	108	(+/-)	SUN Mode II
25	1280X960	60.0K/60HZ	108	(+/-)	
26	1280X960	75.0K/75HZ	129.895	(+/-)	non-VESA
27	1280X1024	76.0K/72HZ	130.223	(+/-)	DOS/V
28	1280X1024	64.0K/60HZ	108	(+/-)	
29	1280X1024	80.0K/75HZ	135	(+/-)	
30	1280X1024	81.1K/76HZ	135.008	(-/-)	SUN Mode I
31	1280X1024	71.7K/67HZ	117	(+/-)	SUN Mode V
32	688X556	31.3K/50HZ	27	(-/+)	TV-PAL

Allowed 85 Hz overscan signal mode specified

Once the signal input of PC is 85Hz, this monitor is able to display at least for 10 minutes . An attention signal appears and shows

This is 85Hz overscan for 5 seconds, change computer display input to 1024X768 @ 60 Hz

Dot rate (MHz)	H.freq (KHz)	Mode	Resolution	V.freq (Hz)
36.000	43.269	VESA	640 * 480	85.008
56.250	53.674	VESA	800 * 600	85.061
94.500	68.677	VESA	1024 * 768	84.997

Power management

The power consumption and the status indication of the set with power management function are as follows,

Note : Disconnect DVI signal cable and audio module.

STATUS	Horizontal	Vertical	Power Spec	LED
On	Pulse	Pulse	as normal on	Green
Stand-by	No Pulse	Pulse	< 2 W	Amber
Suspend	Pulse	No Pulse	< 2 W	Amber
off	No Pulse	No Pulse	< 2 W	Amber

According to VESA power saving signaling.
TCO99 power saving requirement
EPA energy star requirement

(Power Switch Off)

for Digital input power consumption is less 2W

(In non-DMPM recoverable off mode)

Connecting cables to the monitor

The monitor ports are protected by a cover.

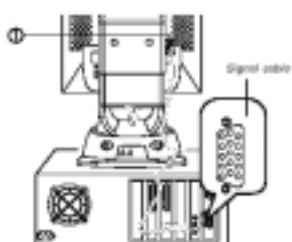


Figure 2a. Connection via the VGA interface

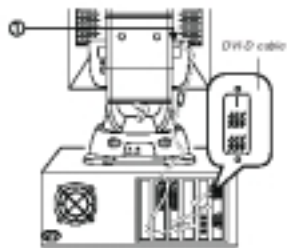
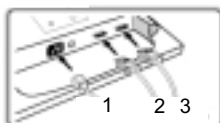


Figure 2b. Connection via the DVI-D interface



Figure 3. Connecting power cable and audio connector



- 1 = Power connector
- 2 = DVI-I connector
- 3 = D-SUB connector

The data cable supplied has two 15-pin D-SUB connectors for the D-SUB connector of the monitor and of the computer.

Connect one of the D-SUB connectors of the data cable to the D-SUB connector on the monitor (3) and secure the plug-in connection by tightening the safety screws.

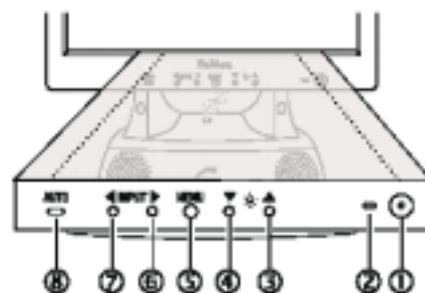
If instead of the data cable supplied you want to use a data cable with DVI-I connector or if you want to connect the monitor to a second computer, proceed as follows:

Connect the DVI-I connector of the data cable to the DVI-I connector on the monitor (2) and secure the plug-in connection by tightening the safety screws.

Data cables with DVI-I connectors are available from specialised dealers.

After switching on the monitor you must select the desired input with the OSD menu .

Switching the monitor on/off



3~8 = Buttons for the OSD menu(On-Screen-Display)

2 = Power indicator

1 = Power button

The ON/OFF switch (1) is used for switching the monitor on and off.

The power indicator (2) glows green when the monitor and computer are switched on. The power indicator glows orange when the monitor does not receive a video signal or is in the energy-saving mode. The power indicator goes off when the monitor is switched off.

Monitor settings with the buttons of the control panel

With the buttons of the control panel, you can make the following settings directly: auto-adjustment of the monitor, adjusting the brightness and adjusting the contrast.

Other settings are carried out via an integrated OSD menu (On-Screen Display).

Performing auto-adjustment of the monitor

- Press the AUTO button.

The picture quality (*PHASE*, *CLOCK*) and picture position will be optimally adjusted for your system.

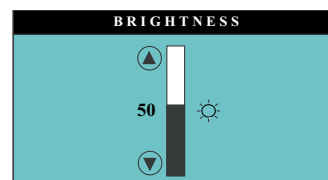
- Press the MENU button to save the settings.

Adjusting the brightness

The brightness of the monitor is adjusted directly with the arrow buttons.

Press the button (▲) or (▼).

The window *BRIGHTNESS* with a setting scale is visualised.



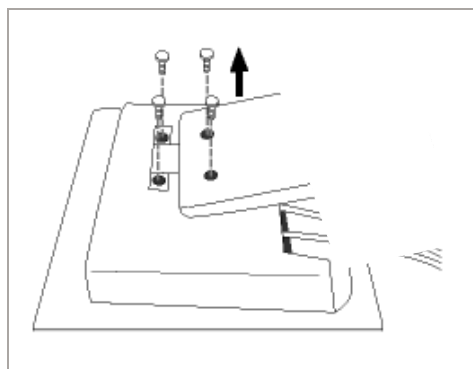
Press the (▲) or (▼) button to adjust the brightness.

The setting window disappears after approximately 60 seconds if no further setting is made.

All changes are stored automatically.

Removing the monitor base

The monitor surface is susceptible to scratching!



- Lay the monitor on its face on a soft surface.
- Remove the four screws on the rear of the monitor.

Settings using the OSD menu

◀◀ Go to cover page

With the buttons on the control panel, call up and use the integrated OSD (On-Screen Display) menu.

The OSD menu is available in different languages. The English menu names are used in the following description (default setting). With the OSD function *LANGUAGE* you can select another language.

The main menu appears on the screen with icons for the setting functions.

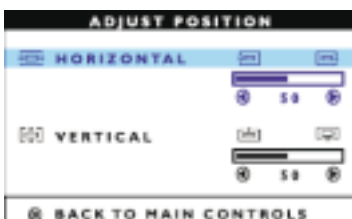


The first symbol (*LANGUAGE*) is highlighted.

If necessary, press the ⬆ or ⬇ button to mark another icon (e.g. *ADJUST POSITION*).

Press the MENU button to select the highlighted icon.

The corresponding setting window (here: *ADJUST POSITION*) is displayed.



Press the ⬆ or ⬇ button to adjust the selected function (here: *HORIZONTAL*).

Press the key ⬆ or ⬇ to mark another function or press the MENU key to return to the main menu.

Select the symbol ⬇ (EXIT MAIN CONTROLS) in the main menu and press the MENU key to exit the OSD menu.

Setting language for the OSD menu

	Setting language for the OSD menu (<i>LANGUAGE</i>) You can choose from English (default setting), German, French, Spanish, Italian, and Japanese.
--	---

Adjusting picture position

	Calling the <i>ADJUST POSITION</i> setting window	
	Horizontal picture position (<i>HORIZONTAL</i>): Shifting the picture to the left or to the right	
	Vertical picture position (<i>VERTICAL</i>): Shifting the picture down or up	

Adjusting the brightness and contrast

	Calling the <i>BRIGHTNESS & CONTRAST</i> setting window	
	Setting the brightness of the display (<i>BRIGHTNESS</i>)	
	Setting the contrast of the display (<i>CONTRAST</i>)	

Setting the picture quality

	Eliminating picture interference (<i>VIDEO NOISE</i>)	
	Ⓐ Eliminating picture noise and horizontal interference (<i>PHASE</i>)	
	Ⓑ Eliminate vertical interference with local fuzziness (<i>CLOCK</i>)	

Setting the colour temperature

The "warmth" of the screen colours is set using the colour temperature. The colour temperature is measured in K (= Kelvin).

	Calling the <i>ADJUST COLOR</i> setting window	
	<i>ORIGINAL PANEL COLOR</i> = Setting for general applications (default setting)	
	<i>9300K FOR CAD/CAM</i> = Setting for CAD/CAM programmes	
	<i>6500K FOR IMAGE MANAGEMENT</i> = Setting, for example, for image processing or playing DVDs	
	<i>USER PRESET</i> = User - defined setting In the user - defined setting you can change the colour ratios of the basic colours (red, green, blue) as required.	

Setting position for the OSD menu

	Calling the <i>OSD SETTINGS</i> setting window	
	You can shift the OSD menu up, down, to the left or to the right.	

Displaying monitor data

	You will find the serial number and the current resolution of this screen in <i>PRODUCT INFORMATION</i>	
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Activating the factory settings

	Activating the factory settings (<i>RESET TO FACTORY SETTINGS</i>)	
	<i>NO</i> = retain own settings	
	<i>YES</i> = Activating the factory settings	
	With <i>YES</i> all monitor settings are reset to the factory settings.	

Selecting input signal

The monitor can be operated with analog or digital signals. With this function you can (depending on the graphics card you use) switch over between the analog and the digital mode.

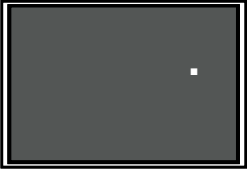
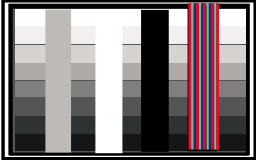
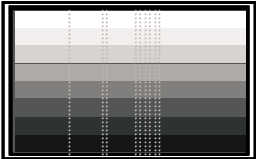
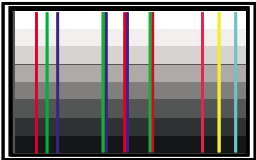
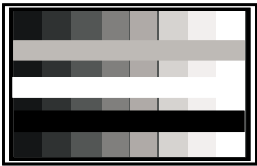
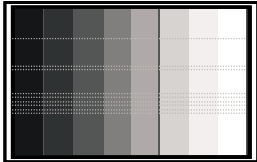
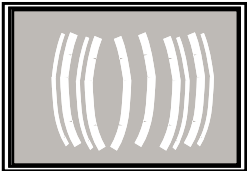
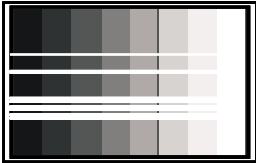
	Calling the <i>INPUT SELECTION</i> setting window	
	Switch on the analog mode (<i>ANALOG DSUB</i>). The monitor processes the signals of the analog port (VGA/DSUB).	
	Switch on the digital mode (<i>DIGITAL DVI</i>). The monitor processes the signals of the digital port (DVI-I).	
	Switch on analog mode via digital port (<i>ANALOG DVI</i>). The monitor processes the analog signals of the digital port (DVI-I).	

Locking the OSD menu (OSD Lock)

The OSD menu can be locked to prevent accidental or unauthorised changes to the monitor settings. Press the MENU button and hold it for approx. 10 seconds. Please proceed in the same manner to release the locked OSD menu again.

Quick reference for failure mode of LCD panel

This page presents problems that could be made by LCD panel. It is not necessary to repair circuit board. Simply follow the Mechanical instruction on this manual to eliminate failure by replace LCD panel or backlight tubes.

Failure description	Phenomenon		
		Polarizer has bubbles	
Vertical block defect		Polarizer has bubbles	
Vertical dim lines		Foreign material inside polarizer. It shows linear or dot shape.	
Vertical lines defect (Always bright or dark)		Concentric circle formed	
Horizontal block defect		Bottom back light of LCD is brighter than normal	
Horizontal dim lines		Backlight un-uniformity	
Horizontal lines defect (Always bright or dark)		Backlight has foreign material. Black or white color, linear or circular type	
Has bright or dark pixel			

Trouble shooting

Should an error occur, first check the following points. If the distortion is still not eliminated, the monitor should, if possible, be checked on another computer and/or with another data cable.

Having this problem?	Check the following points:
No display (power indicator does not light)	•Check whether the monitor is switched on. •Check whether the power cable on the monitor is connected correctly. •Check whether the computer is switched on.

Having this problem?	Check the following points:
No display (power indicator lights)	• Check whether the computer is switched on. • Check whether the data cable for the monitor is correctly attached to the monitor port on the computer. • Press any key on the computer keyboard the computer may be in energy saving mode. • Alter the brightness and/or contrast until you get a picture.



	• Check whether the data cable for the monitor is correctly attached to the monitor port on the computer. • Check whether the computer is switched on. The input signal (horizontal frequency and refresh rate) at the displayed input does not correspond to the technical monitor data. • Adjust the video frequency range using the computer software (safe Mode) or Using another CRT monitor.
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Picture position is not correct	The monitor recognises an undefined mode (see Technical data'). • Press the AUTO button to conduct the auto-adjustment of the screen. • Set the picture position with the OSD menu .
---------------------------------	---

Picture is shaking	• Check whether the data cable for the monitor is correctly attached to the monitor port on the computer.
--------------------	---

Having this problem?	Check the following points:
Picture disturbances (vertical lines) 	• Press the AUTO button to conduct the auto-adjustment of the screen. • Set the picture quality in VIDEO NOISE (CLOCK).
Picture disturbances (horizontal lines, picture noise) 	• Press the AUTO button to conduct the auto -adjustment of the screen. • Set the picture quality in VIDEO NOISE (PHASE).

The screen becomes darker	The background lighting has a limited lifetime.If your monitor display should become too dark,the background lighting will have to be exchanged.
Image of last screen display still visible	If you switch from a high contrast display (for example black lines/ characters on a white background) to a picture of medium brightness, you may be able to see a faint image of the previous display for a time. This phenomenon is not due to a monitor defect but to the technology used (IPS display with large viewing angle, IPS = In Plane Switching).

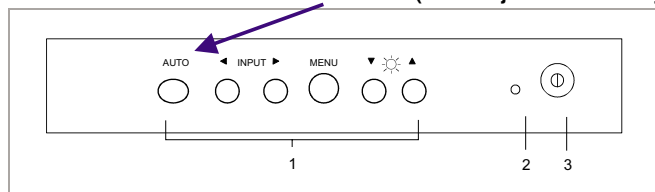
Permanently unlit or lit pixels	The standard of production techniques today cannot guarantee an absolutely fault -free screen display. A few isolated constant lit or unlit pixels may be present. The maximum permitted number of pixels faults is stipulated in the stringent international standard ISO 13406 - 2 (Class II). Example: a 17" or 18" flat screen monitor with a resolution of 1280 x 1024 has 1280 x 1024 = 1310720 pixels. Each pixel consists of three subpixels (red, green and blue), so there are almost 4 million dots in total. According to ISO 13406-2 (Class II), a maximum of 6 pixels and 7 subpixels may be defective, i. e. a total of 25 faulted dots. This corresponds to approx. 0,0006 % of the entire screen surface!
---------------------------------	--

The monitor has build-in a auto adjustment hotkey on the front panel, you may obtained a optimal video display by simply press the **AUTO** button and save the settings. CLOCK, PHASE, Vertical position, and Horizontal position are adjusted automatically.

Due to the different quality of video signal generated from graphics cards. By press AUTO button for CLOCK and PHASE adjustment automatically. Sometimes, the deviation of video signal which generated from graphics cards is out of control, then it is necessary to adjust CLOCK and PHASE functions for the optimal video display of LCD monitor by manual. Following steps will guide you to make correct adjustment of CLOCK and PHASE.

However, CLOCK and PHASE functions are only available while analog video signal is supplied. Operating unit under digital signal state, the video clock information can be obtained from graphics cards directly. Therefore, it is unnecessary to adjust these functions.

Front control panel AUTO button (Auto adjustment hotkey)



- 1 = Buttons for the OSD menu (On-Screen-Display) Fig. 1
2 = Power indicator
3 = Power button

Manual adjustment

If the quality of display still poor or flicker, you may also improve it by manual adjust CLOCK and PHASE functions to eliminate the flicker.

Step 1 : Click on the Start button (Win95, Win98 or Win NT) and choose " Shut Down..." as shown in Fig. 2.

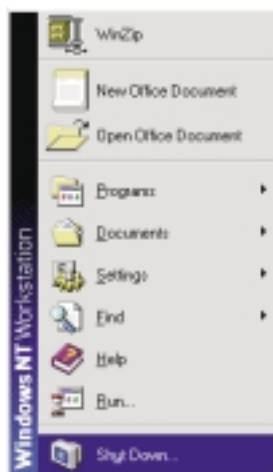


Fig. 2

Step 2 : The menu of " Shut Down Windows " is as shown in Fig. 3

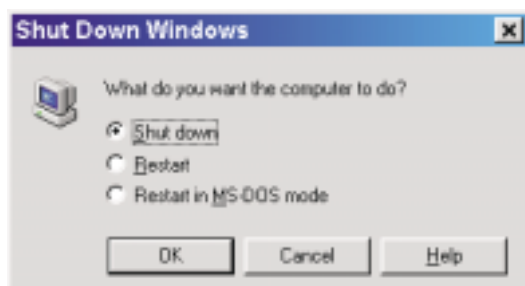
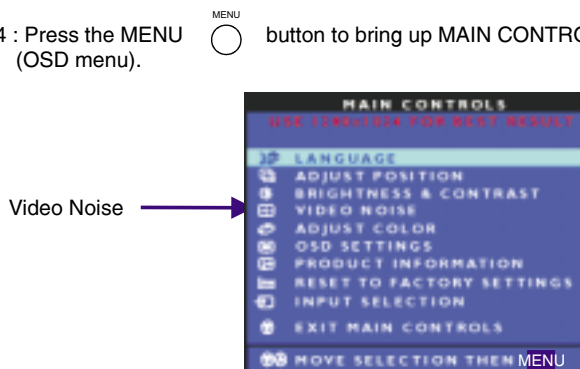


Fig. 3 (Win 98)

Step 3 : Retain Shut Down Window on the screen , follow the CLOCK and PHASE adjustment instructions for the optimal video display.

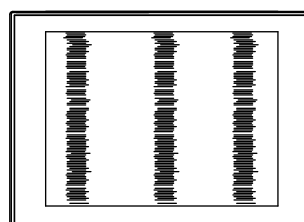
Step 4 : Press the MENU button to bring up MAIN CONTROLS (OSD menu).



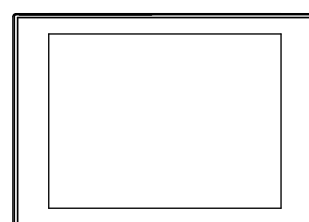
Step 5 : Select Video Noise by press button.
Step 6 : Press button to bring up it s submenu.



Step 7 : Press or button to adjust CLOCK. The picture will be adjusted as following figure, adjust CLOCK to fine-tune the video until optimal display is obtained.

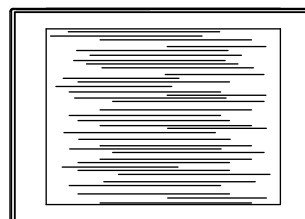


CLOCK Phenomenon (Before)

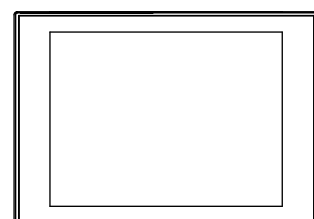


(After)

Step 8 : Press or button to adjust PHASE. The picture will be adjusted as following figure, adjust PHASE and check the picture, stop at the point that without any vertical jitter bar remaining on the screen.



PHASE phenomenon (Before)

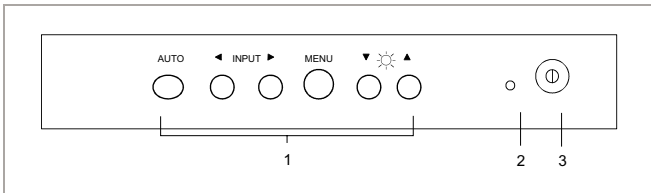


(After)

Step 9 : Quit OSD menu by press button to save the settings.

Go to cover page

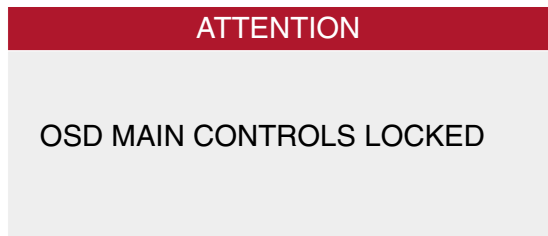
Front control panel



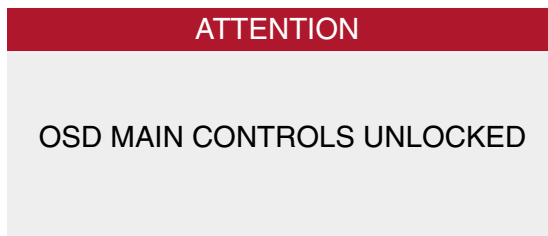
- 1 = Buttons for the OSD menu (On-Screen-Display)
- 2 = Power indicator
- 3 = Power button

To Lock/Unlock OSD function (User Mode)

The OSD function can be locked by pressing  button for more than 10 seconds, the screen shows following windows for 3 seconds. Everytime when you press  or  button, this message appears on the screen automatically. The  &  (brightness) ,   (input) hotkey are still functional for brightness and input signal selection while OSD locked.

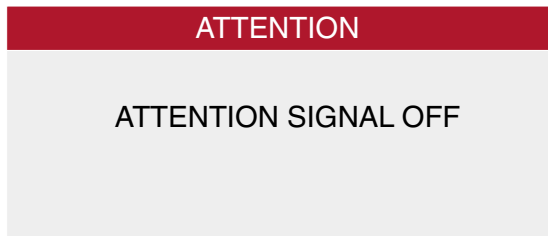


Locked OSD function can be released by pressing  button for more than 10 seconds again. While press "MENU" button for OSD unlocked purpose, the screen will keep showing OSD MAIN CONTROLS LOCKED until OSD function unlocked and screen automatically shows following window for 3 seconds.

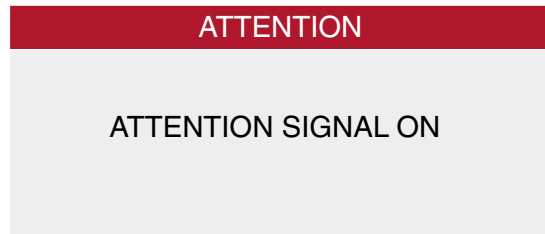


Switch ON/OFF attention signals (User Mode)

All attention signals can be **switched off** by keep pressing  button for more than 10 seconds if there is no video signal supplied. Step 1: Disconnect signal cable (Monitor & PC are still ON.) Step 2: Press "AUTO" button for more than 10 seconds, bring up message as below.



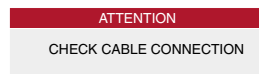
Recover attention signals by pressing  button for more than 10 seconds without video signal input. Step 1: Disconnect signal cable (Monitor & PC are still ON.) Step 2: Press "AUTO" button for more than 10 seconds again, bring up message as below.



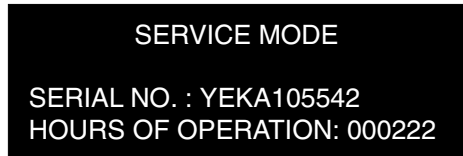
Access Service Mode (in USER MODE)

Method 1:

Step 1: Disconnect signal cable on Rear side of monitor. Don't disconnect signal cable on PC side, because CPU will detect pin-9 of D-SUB (15 pin connector). (Monitor & PC are still ON.) Bring up "CHECK CABLE CONNECTION" screen.



Step 2: Press MENU"  " button for more than 15 seconds. Bring up message as below.



Leave service mode by either re-feed video signal or simply turn off and on the power of monitor.

Access Service Mode (in USER MODE)

Method 2:

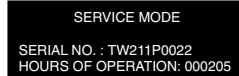
Step 1:

- Turn off monitor.
- Disconnect signal cable on PC side.
- Turn on monitor.

(After 3 seconds) Bring up "NO VIDEO INPUT"



Step 2: Press MENU"  " button for more than 15 seconds. Bring up "SERVICE MODE".



Leave service mode by either re-feed video signal or simply turn off

Access Service Mode (in USER MODE)

Method 3:

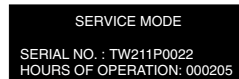
Step 1:

- PC off.
- Signal cable connect to rear side of Monitor only.
- Turn on monitor.

Bring up "NO VIDEO INPUT"

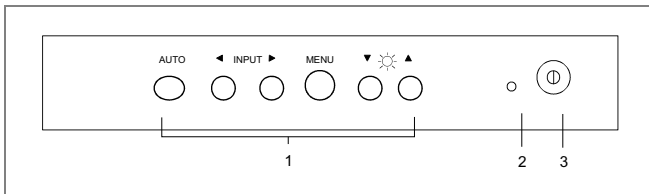


Step 2: Press MENU"  " button for more than 15 seconds. Bring up "SERVICE MODE".



Leave service mode :simply turn off and on the power of monitor.

Front control panel

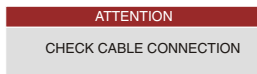


- 1 = Buttons for the OSD menu (On-Screen-Display)
 2 = Power indicator
 3 = Power button

Access Aging.. Mode (Method 1)

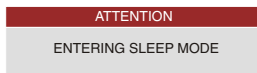
(Monitor & PC are ON for Step 1.)

Step 1: Disconnect signal cable on Rear side of monitor.
 Bring up "CHECK CABLE CONNECTION" screen.



or

Disconnect signal cable on PC side.
 Bring up "ENTERING SLEEP MODE" about 3 seconds,
 then disappear.



Step 2:

Turn off LCD monitor.

Push AUTO "○" & "○" & "Ⓢ" buttons simultaneously
 ,then release Power button only, untill picture comes on the screen.

Bring up "AGING ...".

Bring up :



After 15 seconds, bring up :



After 15 seconds, bring up :



After 15 seconds, bring up :



repeatlly

Connect signal cable again => go back to normal display.

Method 2:

Step 1:

- PC off and Monitor off.
- Signal cable connect to rear side of Monitor only.
- Connect Power Cord to rear side of monitor, then insert the plug into a properly grounded electrical outlet.
 (Power LED of monitor is off now.)

Step 2:

Push AUTO "○" & "○" & "Ⓢ" buttons simultaneously
 ,then release Power button only, untill picture comes on the screen.

Bring up "AGING ...".



repeatlly

Leave AGING mode :connect signal cable to PC,
 then turn on PC.

◀◀ Go to cover page

Access Factory Mode

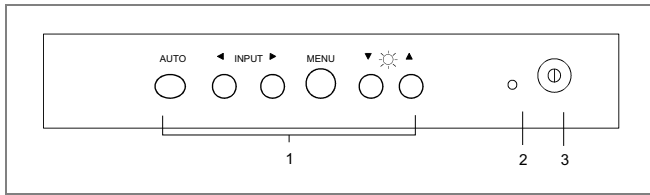


Fig. 1

- 1 = Buttons for the OSD menu (On-Screen-Display)
 2 = Power indicator
 3 = Power button

Factory Mode:

How to Get into Factory Mode Menu

Turn off LCD monitor.

Push AUTO "○" & "○" & "Ⓢ" buttons simultaneously, then release Power button only, until picture comes on the screen. Press MENU "○" button, bring up Factory mode indication as shown in Fig 2.



Fig. 2

Use "○" button to select factory adjustment indication (for example: MAXDATA 180P V1.88 02-04-17), which is the entrance of the factory adjustment menu, press MENU "○" button to access it. The window shows as below.

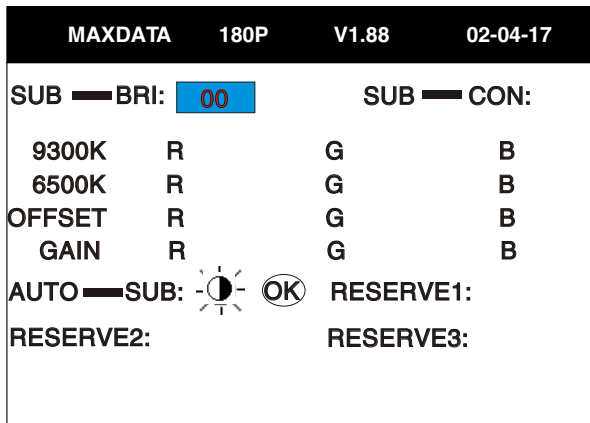


Fig. 3

All the default values are invisible. Press "○" => appear.

Use "○" or "○" buttons to select SUB-CON, 9300K R G B,...etc.

Use "○" or "○" buttons to decrease/increase the value of each item.

Press "MENU" "○" => go back to MAIN CONTROLS.

To press AUTO "○" button on this screen, the values of OFFSET and GAIN will be adjusted by CPU automatically.

Default values (example) are as below.

MAXDATA	180P	V1.88	02-04-17
SUB — BRI:	00	SUB — CON:	50
9300K	R 228	G 251	B 255
6500K	R 252	G 255	B 226
OFFSET	R 86	G 84	B 83
GAIN	R 181	G 188	B 182
AUTO — SUB:	(brightness icon) OK	RESERVE1:	205
RESERVE2:	255	RESERVE3:	255

SUB — BRI:

NO FUNCTIONED, DIGITAL DC-Level RESERVED for LCD MONITOR 150P usage ONLY.

SUB — CON: 255

Contrast adjustment (Sub-Contrast). Use this menu item to adjust the contrast gain of pre-amp ranges from 0 to 255. (For example: 255 of Sub-Contrast in factory mode is preset for 100%(Maximum Value) of Contrast Value in user mode).

9300K R G B
 6500K R G B

Color temperature gain adjustment. Use these menu items to adjust the RGB gains of pre-amp for different color temperatures, ranges from 0 to 255.

OFFSET R G B

Sub-Brightness adjustment. Use this menu item to adjust the brightness level (DC-level) of pre-amp range from 0 to 255.(R/G/B ANALOG DC -level).

GAIN R G B

R/G/B GAIN adjustment. Use this menu item to adjust the R/G/B (GAIN) of Amp. range from 0 to 255.(R/G/B ANALOG GAIN Values).

AUTO — SUB: (brightness icon) OK

AUTO Colour to Adjust OFFSET R/G/B & GAIN R/G/B automatically by LCD MONITOR CPU.

RESERVE1: NO FUNCTIONED, RESERVED ONLY.
 RESERVE2: NO FUNCTIONED, RESERVED ONLY.
 RESERVE3: NO FUNCTIONED, RESERVED ONLY.

(PS: The Offset R G B function can be used on reduce or eliminate snowy noise on the background when the resolution of video signal is 1280 X 1024 vertical 60Hz. Slightly increase or decrease the value until snowy noise completely disappear.)

Leave Factory Mode

Turn off monitor, then turn on monitor again => go back to User mode directly.

Warnings

1. Safety regulations require that the unit should be returned in its original condition and that components identical to the original components are used. The safety components are indicated by the symbol .
2. In order to prevent damage to ICs and transistors, all high-voltage flash-overs must be avoided. In order to prevent damage to the picture tube, the method shown in Fig. 1 should be used to discharge the picture tube. Use a high-voltage probe and a multimeter (position DC-V). Discharge until the meter reading is **0 V** (after approximately 30 seconds).
3. **ESD** 
All ICs and many other semiconductors are sensitive to electrostatic discharges (ESD). Careless handling during repair can drastically shorten their life. Make sure that during repair you are connected by a pulse band with resistance to the same potential as the ground of the unit. Keep components and tools also at this same potential.
4. When repairing a unit, always connect it to the AC Power voltage via an isolating transformer.
5. Be careful when taking measurements in the high-voltage section and on the picture tube panel.
6. It is recommended that safety goggles be worn when replacing the picture tube.
7. When making adjustments, use plastic rather than metal tools. This will prevent any short-circuit or the danger of a circuit becoming unstable.
8. Never replace modules or other components while the unit is switched on.
9. Together with the deflection unit, the picture tube is used as an integrated unit. Adjustment of this unit during repair is not recommended.
10. After repair, the wiring should be fastened in place with the cable clamps.
11. All units that are returned for service or repair must pass the original manufacturer's safety tests.

Notes

1. The direct voltages and waveforms are average voltages. They have been measured using the Service test software and under the following conditions :
 - Mode : 640 * 480 (31.5kHz / 60Hz)
 - Signal pattern : grey scale
 - Adjust brightness and contrast control for the mechanical mid-position (click position)
2. The picture tube panel has printed spark gaps. Each spark gap is connected between an electrode of the picture tube and the Aquadag coating.
3. The semiconductors indicated in the circuit diagram(s) and in the parts lists are completely interchangeable per position with the semiconductors in the unit, irrespective of the type indication on these semiconductors.

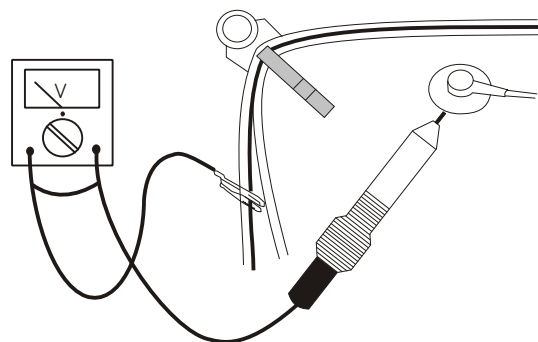


Fig.1 (CRT ONLY)

◀◀ Go to cover page

General

DDC Data Re-programming

In case the DDC data memory IC or main EEPROM which storage all factory settings were replaced due to a defect, the serial numbers have to be re-programmed.

It is advised to re-soldered DDC IC and main EEPROM from the old board onto the new board if circuit board have been replaced, in this case the DDC data does not need to be re-programmed.

Additional information

Additional information about DDC (Display Data Channel) may be obtained from Video Electronics Standards Association (VESA). Extended Display Identification Data(EDID) information may be also obtained from VESA.

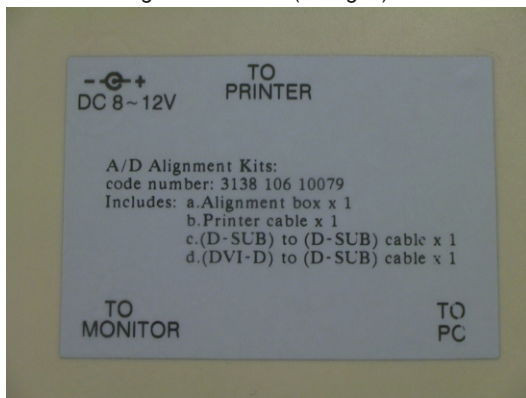
DDC EDID structure

For Analog interface: Standard Version 3.0
Structure Version 1.2
For Digital interface: Standard Version 3.0
Structure Version 1.3

System and equipment requirements

1. An i486 (or above) personal computer or compatible.
2. Microsoft operation system Windows 95/98.
3. EDID301.EXE program (3138 106 10103) as shown in Fig. 1
4. A/D Alignment kits (3138 106 10079):
inclusion : a. Alignment box x1 (as Fig. 2)

Fig. 2



- b. Printer cable x1
c. (D-Sub) to (D-Sub) cable x1
d. (DVI-D) to (D-Sub) cable x1 (as Fig. 3)

Note: The EDID301.EXE (Release Version 1.58 20000818) is a windows-based program, which cannot be run in MS-DOS.



Fig. 1

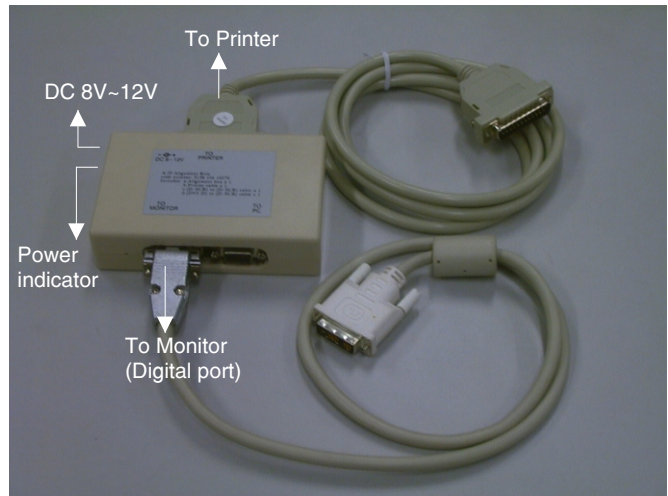
Diskette with EDID301.EXE

Fig. 3



(DVI-D) to (D-Sub) cable

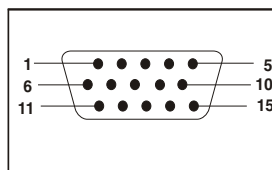
Note: The alignment box has already build-in a batteries socket for using **batteries (9V)** as power source. Pull out the socket by remove four screws at the rear of box. Please do not forget that remove batteries after programming. The energy of batteries can only drive circuits for a short period of time.



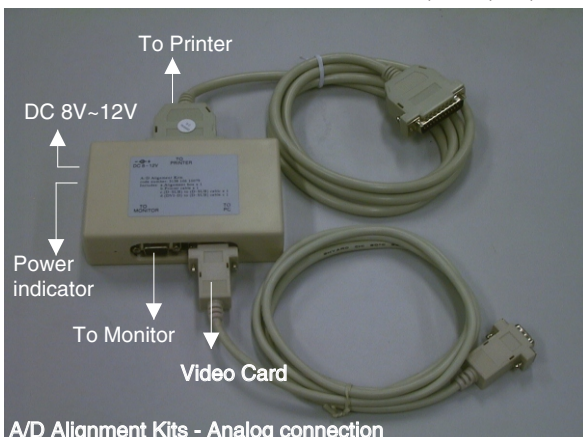
A/D Alignment Kits - Digital connection

Pin assignment

A. 15-pin D-Sub Connector



Pin No.	Assignment	Pin No.	Assignment
1	Red video input	9	+5V
2	Green video input	10	Ground
3	Blue video input	11	Ground
4	Ground	12	Serial data line(SDA)
5	No Connected	13	H.Sync
6	Red video ground	14	V.Sync(VCLK for DDC)
7	Green video ground	15	Data clock line(SCL)
8	Blue video ground		



A/D Alignment Kits - Analog connection

Configuration and procedure

There are 2 chips contained OSD string, serial number..etc on the circuit board, main EEPROM which storage all factory settings, OSD string. DDC IC which storage 128byte EDID data(serial number ..etc.). Following descriptions are the connection and procedure for Analog and Digital DDC application, the main EEPROM can be re-programmed along with Analog/Digital IC by enable factory memory data write function on the DDC program (EDID301.EXE).

Initialize alignment box

In order to avoid that monitor entering power saving mode due to sync will cut off by alignment box, it is necessary to initialize alignment box before running programming software (EDID301.EXE). Following steps show you the procedures and connection.

- Step 1: Supply 8~12V DC power source to the Alignment box by plugging a DC power cord or using batteries.
Step 2: Connecting printer cable and video cable of monitor as Fig. 3

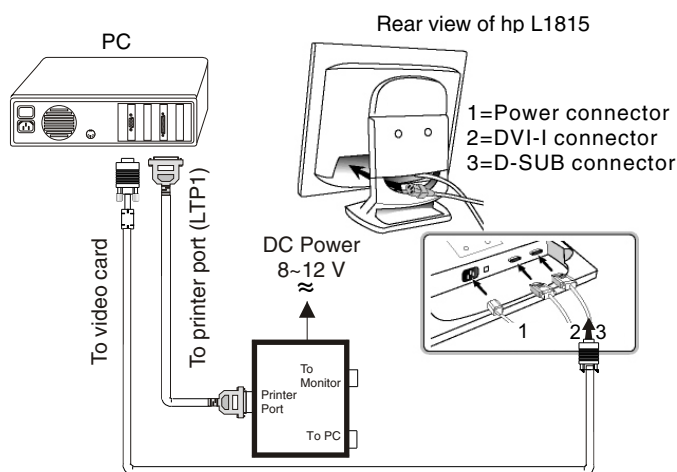


Fig. 3

Step 3: Installation of EDID301.EXE

Method 1: Start on DDC program

Start Microsoft Windows.

1. Insert the disk containing EDID301.EXE program into floppy disk drive.
2. Click Start, choose Run at start menu of Windows 95/98 as shown in Fig. 4.

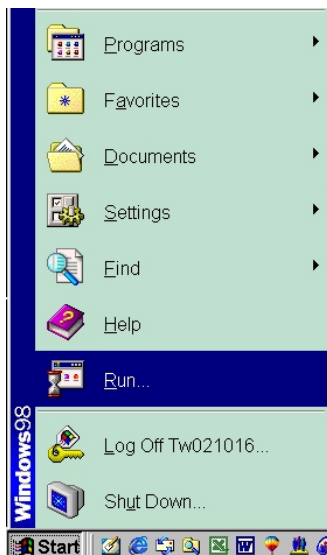


Fig. 4

3. At the submenu, type the letter of your computer's floppy disk drive followed by :EDID301 (for example, A:\EDID301, as shown in Fig. 5).

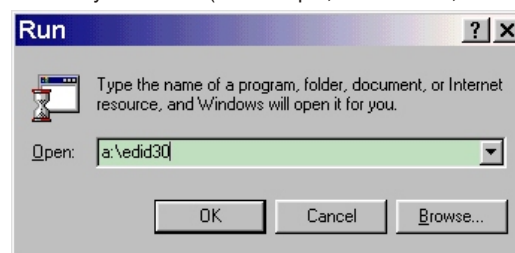


Fig. 5

4. Click OK button. The main menu appears (as shown in Fig. 6).
This is for initialize alignment box.

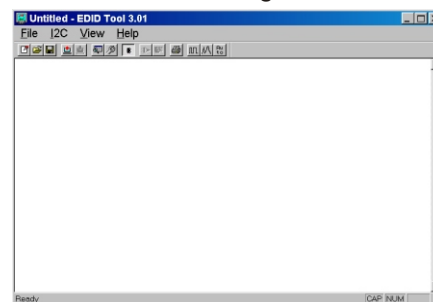


Fig. 6

Note 1: If the connection is improper, you will see the following error message (as shown in Fig. 7) before entering the main menu. Meanwhile, the (read EDID) function will be disable. At this time, please make sure all cables are connected correctly and fixedly, and the procedure has been performed properly.



Fig. 7

Method 2: After create a shortcut of EDID301.EXE

- : Double click EDID301 icon (as shown in Fig. 8) which is on the screen of Windows Wallpaper. Bring up main menu of EDID301 as shown in Fig. 9.
- This is for initialize alignment box.**

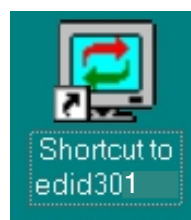


Fig. 8

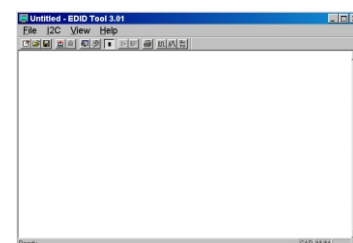
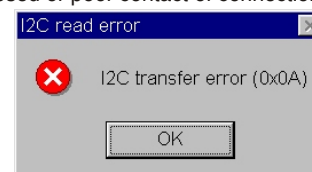


Fig. 9

Note 2: During the loading, EDID301 will verify the EDID data which just loaded from monitor before proceed any further function, once the data structure of EDID can not be recognized, the following error message will appear on the screen as below. Please confirm following steps to avoid this message.

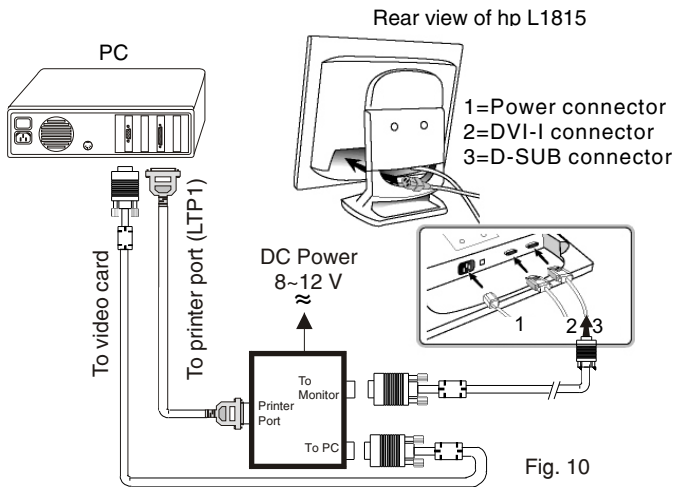
1. The data structure of EDID was incorrect.
2. DDC IC that you are trying to load data is empty.
3. Wrong communication channel has set at configuration setup windows.
4. Cables loosed or poor contact of connection.



Go to cover page

Re-programming Analog DDC IC

Step 1: After initialize alignment box, connecting all cables and box as shown in Fig. 10



Step 2: Read DDC data from monitor

- Click  icon as shown in Fig. 11 from the tool bar to bring up the Channels "Configuration Setup" windows as shown in Fig. 12.

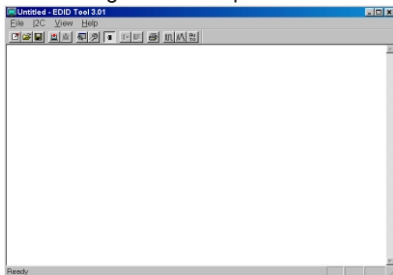


Fig. 11

- Select the DDC2B as the communication channel. Select "Enable" & fill out "F0" for Mapped EDID page address as shown in Fig. 12.

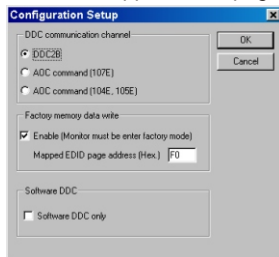
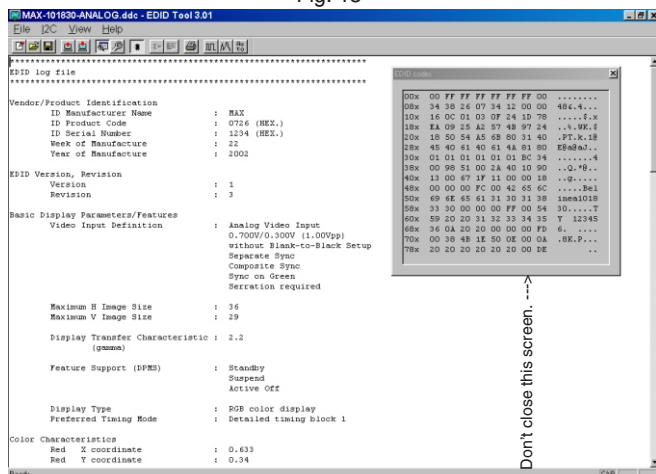


Fig. 12

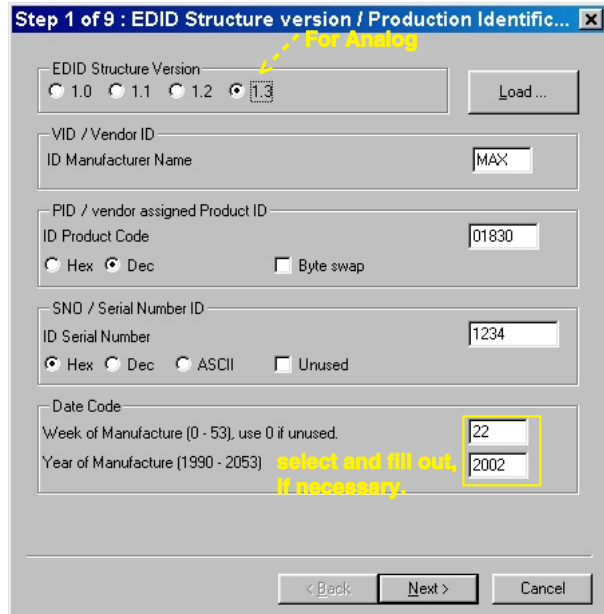
- Click OK button to confirm your selection.
- Click  icon (Read EDID function) to read DDC EDID data from monitor. The EDID codes will display on screen as shown in Fig. 13.

Fig. 13



Step 3: Modify DDC data (verify EDID version, week, year)

- Click  (new function) icon from the tool bar, bring up Step 1 of 9 as shown in Fig. 14. EDID301 DDC application provides the function selection and text change (select & fill out) from Step 1 to Step 9. Fig. 14



Step 4: Modify DDC data (Monitor Serial No.)

- Click Next, bring up Fig. 15.

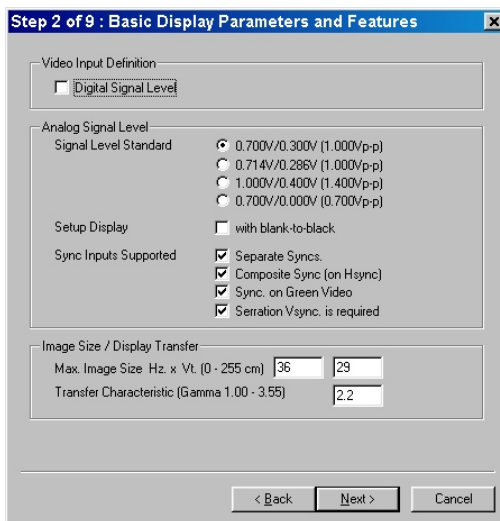


Fig. 15

- Click Next, bring up Fig. 16.

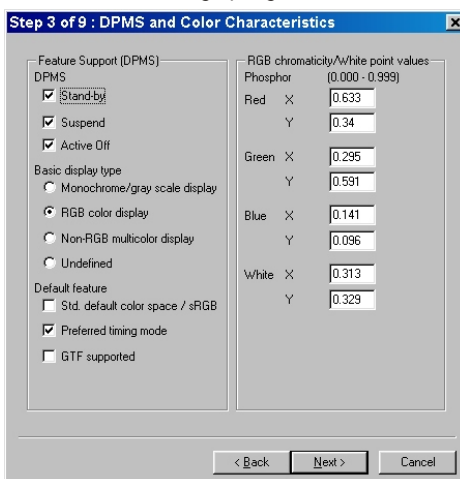


Fig. 16

3. Click **Next**, bring up Fig. 17.

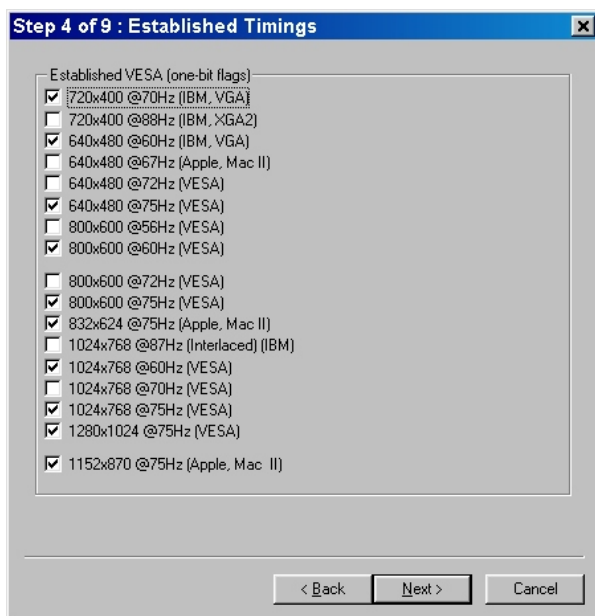


Fig. 17

6. Click **Next**, bring up Fig. 20.

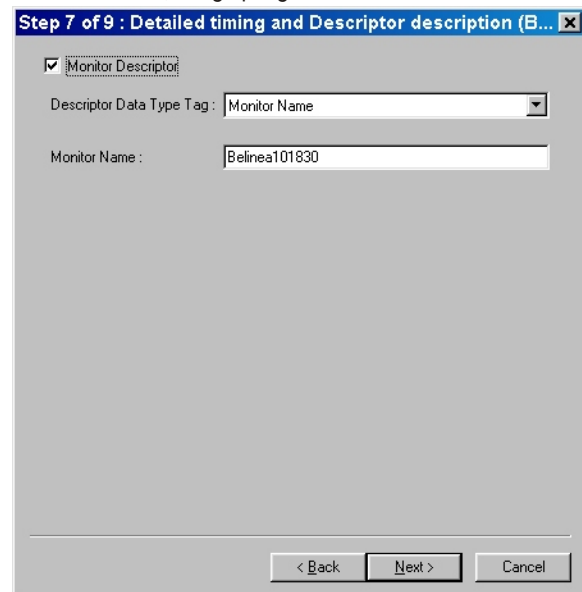


Fig. 20

4. Click **Next**, bring up Fig. 18.

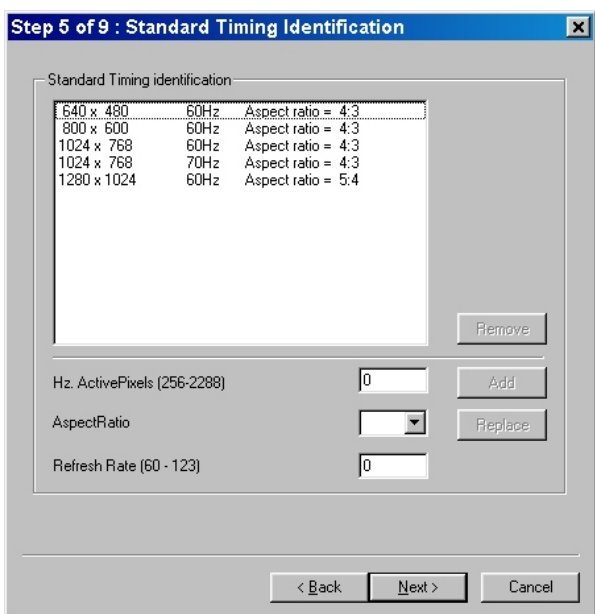


Fig. 18

7. Click **Next**, bring up Fig. 21.

(Serial number can be filled up or be changed at this moment.)

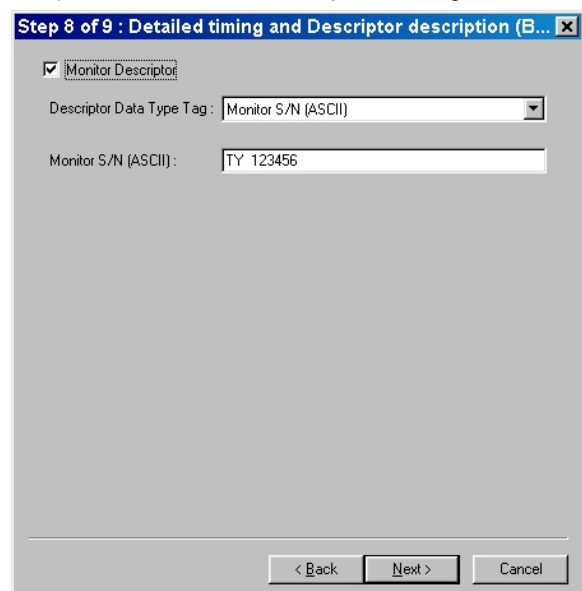


Fig. 21

5. Click **Next**, bring up Fig. 19.

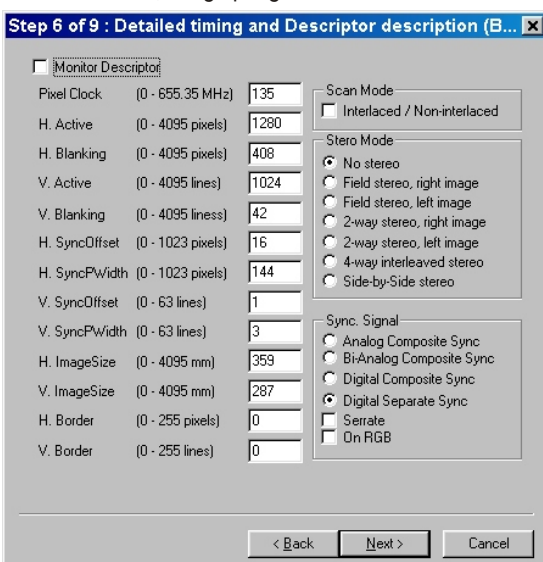


Fig. 19

8. Click **Next**, bring up Fig. 22.

- Click **Finish** to exit the Step window.

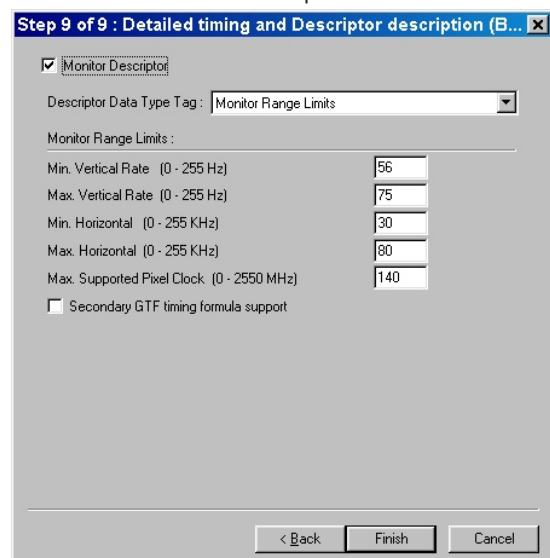
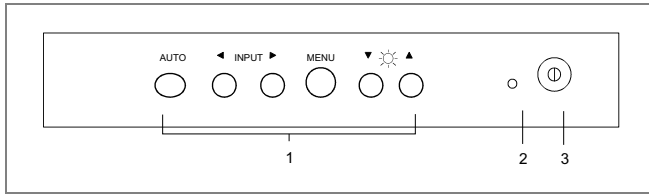


Fig. 22

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Step 5: Access Factory Mode for DDC data writing



Factory Mode:

How to Get into Factory Mode Menu

Turn off LCD monitor.

Push AUTO "○" & "▲" & "Ⓢ" buttons simultaneously, then release Power button only, until picture comes on the screen. Press MENU "○" button, bring up Factory mode indication as shown in Fig 23.



Factory Mode indication==>

Fig. 23

Step 6: Write DDC data

1. Click (Write EDID) icon from the tool bar to write DDC data. (0% ~ 100%, -> READY)
2. Click (Read EDID) to re-confirm (check contents) it.

Step 7: Reconfirm Monitor Serial Number in User Mode

1. Turn off monitor, then turn on monitor again => leave factory mode and return to User Mode directly.
2. Press "MENU" button -> select "PRODUCT INFORMATION" -> press "MENU" button -> bring up Fig. 24.

PRODUCT INFORMATION	
SERIAL NO. :	TY 123456
RESOLUTION :	1024x768@60HZ
VIDEO INPUT :	ANALOG (D-SUB)
MENU BACK TO MAIN CONTROLS	

Fig. 24

Step 8: Save DDC data

Sometimes, you may need to save DDC data as a text file for using in other IC chip. To save DDC data, follow the steps below:

1. Click (Save) icon (or click "file"-> "save as") from the tool bar and give a file name as shown in Fig. 25. The file type is EDID301 file (*.ddc) which can be open in WordPad. By using WordPad, the texts of DDC data & table (128 bytes, hex code) can be modified. If DDC TEXTS & HEX Table are completely correct, it can be saved as .ddc file to re-load it into DDC IC & EEPROM for DDC Data application.

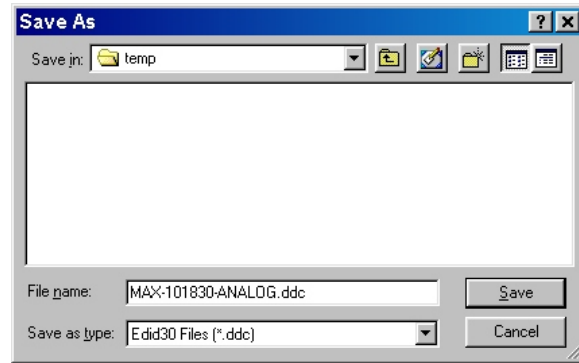


Fig. 25

2. Click **Save**.

Step 9: Load DDC data

1. Click from the tool bar.
2. Select the file you want to open as shown in Fig. 26.
3. Click **Open**.

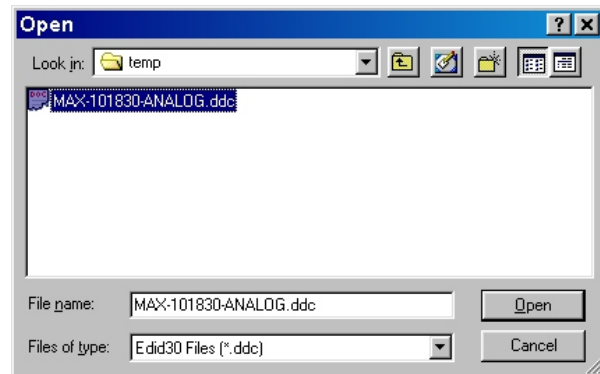


Fig. 26

Step 9: Exit DDC program

Pull down the File menu and select Exit as shown in Fig. 27.

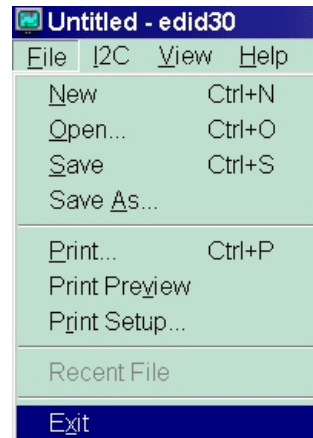


Fig. 27

Re-programming Digital DDC IC (User mode)

Step 1: Connecting all cables and alignment box as shown in Fig. 28.

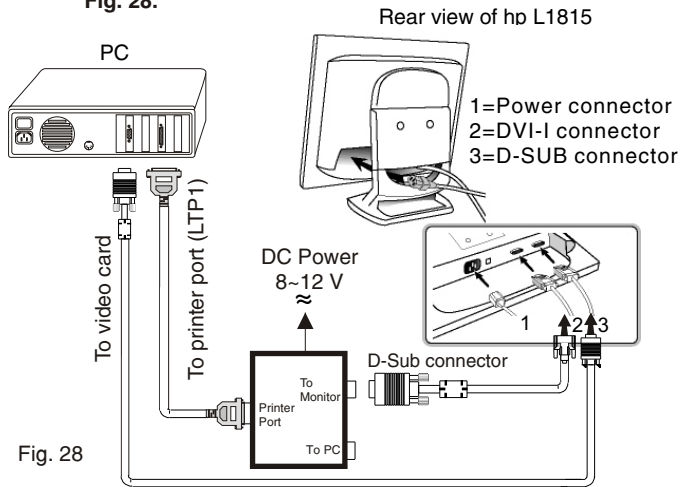


Fig. 28

After connection for Digital DDC application, if it is still in Analog DDC application of EDID301.

Exit EDID301 program before Digital DDC application.

Step 2: Initialize alignment box

(Shortcut of EDID301.EXE on Windows Wallpaper already.)

Double click EDID301 icon (as shown in Fig. 29) which is on the screen of Windows Wallpaper.

Bring up main menu of EDID301 as shown in Fig. 30.



Fig. 29

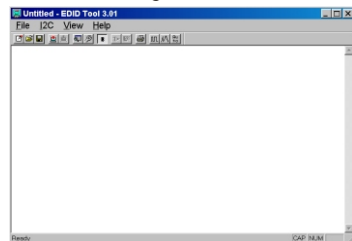
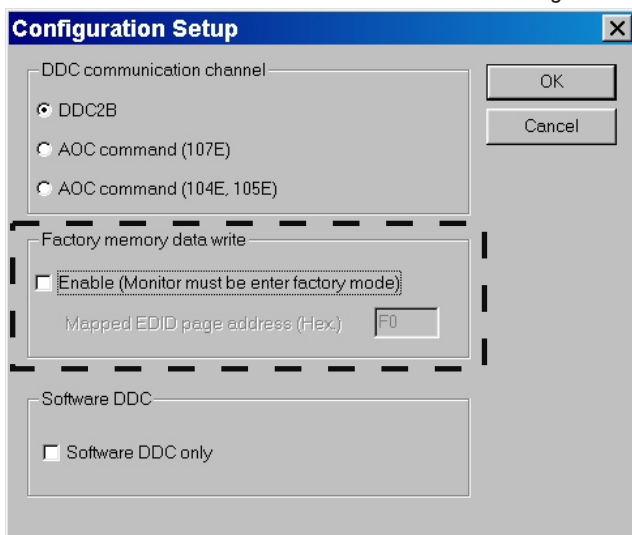


Fig. 30

Step 3: Read DDC data from monitor

- Click icon from the tool bar to bring up the Channels Configuration Setup windows as shown in Fig. 30.
- Select the DDC2B as the communication channel.
Disable "Factory memory data write" for Digital DDC application as shown in Fig. 31.
- Click OK button to confirm your selection.

Fig. 31



- Click icon (Read EDID function) to read DDC EDID data from monitor. The EDID codes will display on screen as shown in Fig. 32.

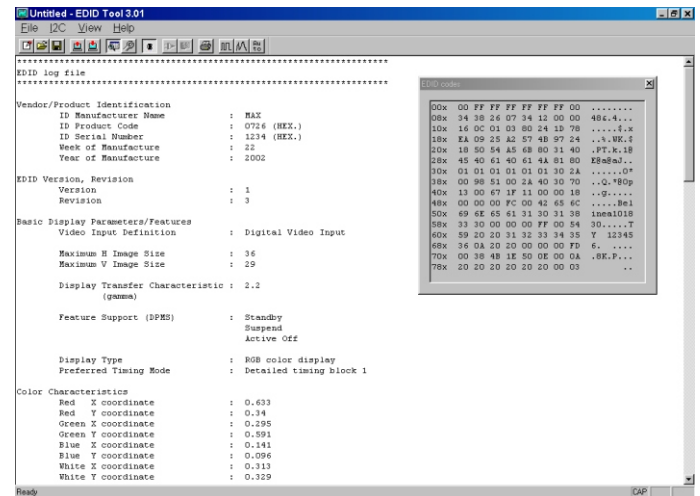


Fig. 32

Step 4: Modify DDC data (verify EDID version, week, year)

- Click (new function) icon from the tool bar, bring up Step 1 of 9 (Digital) as shown in Fig. 33. EDID30 DDC application provides the function selection and text change (select & fill out) from Step 1 to Step 9.

Fig. 33

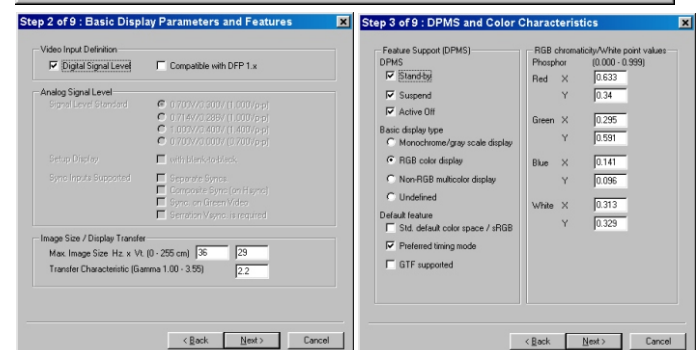
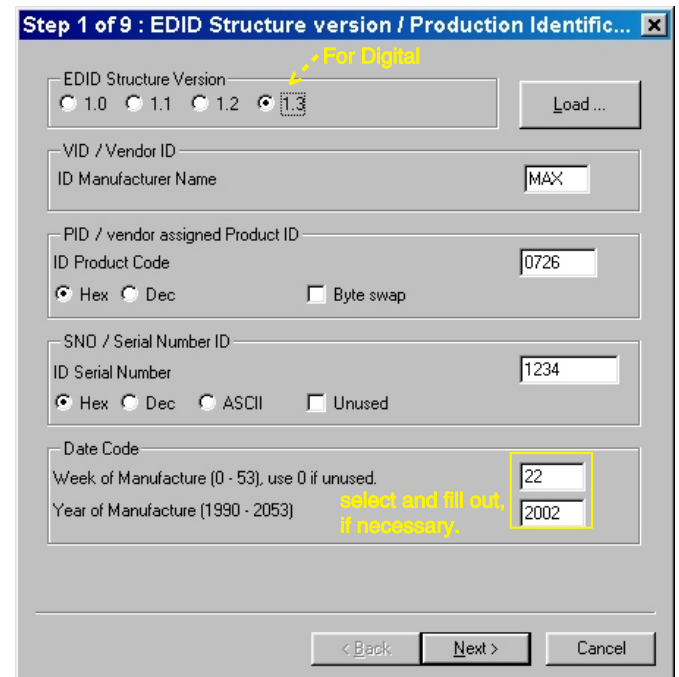


Fig. 34

Fig. 35

DDC Instructions (Continued)

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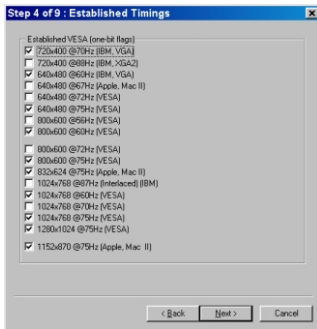


Fig. 36

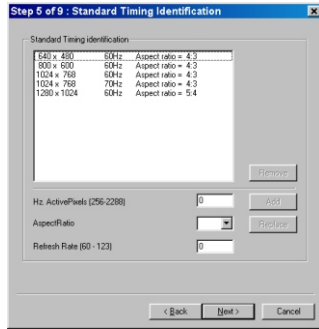


Fig. 37

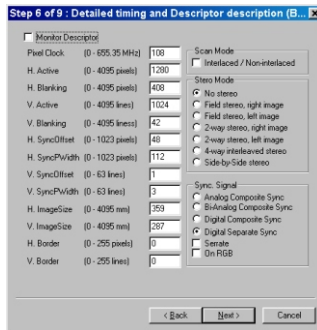


Fig. 38

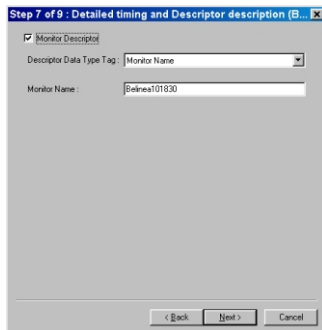


Fig. 39

Step 5: Modify DDC data (Monitor Serial No.)

Monitor Serial No. can be filled up or be changed (for example, TY 123456) as shown in Fig. 40.

Fig. 40

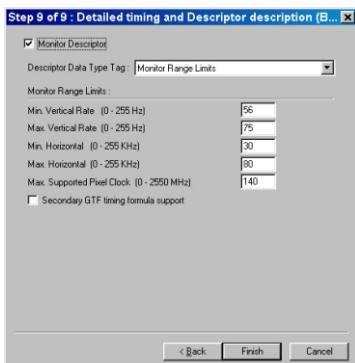
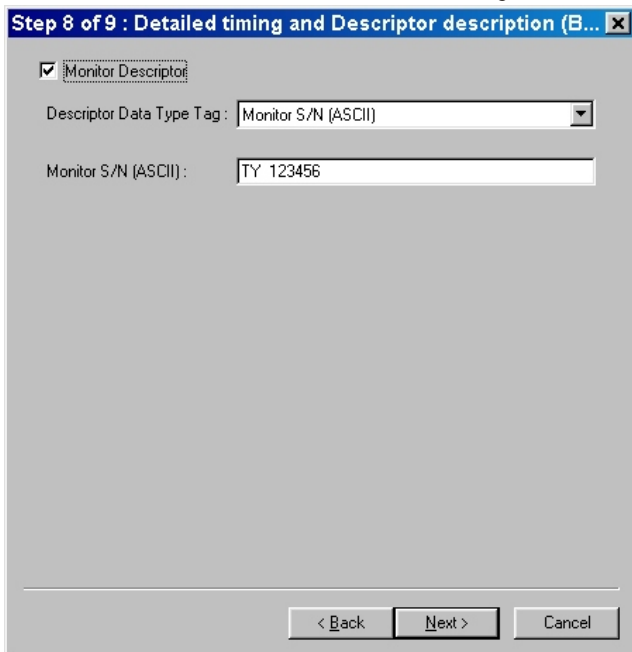


Fig. 41

Click **Finish** to exit the Step window as shown in Fig. 41.

Step 6: Write DDC data

1. Click (Write EDID) icon from the tool bar to write DDC data.
2. Click (Read EDID) to re-confirm (check contents) it.

The 128bytes DDC data which had been written into DDC IC of Digital Mode.

The display of SERIAL NO. by OSD can be change/update by Analog Mode only.

It means "Analog Mode and Digital Mode are using same EEPROM for display of SERIAL NO. by OSD" as shown in Fig. 42.

PRODUCT INFORMATION
SERIAL NO. : TY 123456
RESOLUTION : 1024x768@60HZ
VIDEO INPUT : ANALOG (D-SUB)
MENU BACK TO MAIN CONTROLS

Fig. 42

Step 7: Save DDC data

Sometimes, you may need to save DDC data as a text file for using in other IC chip. To save DDC data, follow the steps below:

1. Click (Save) icon (or click "file"-> "save as") from the tool bar and give a file name as shown in Fig. 43.
2. Click **Save**.

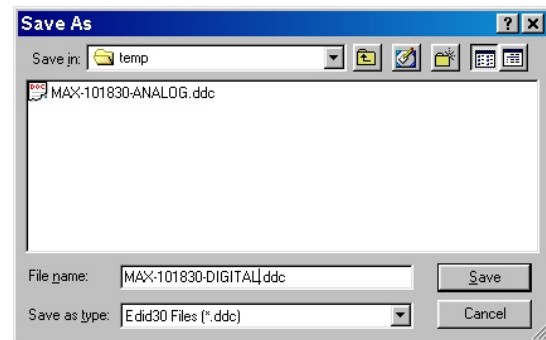


Fig. 43

Step 8: Load DDC data

1. Click from the tool bar.
2. Select the file you want to open as shown in Fig 44.
3. Click **Open**.

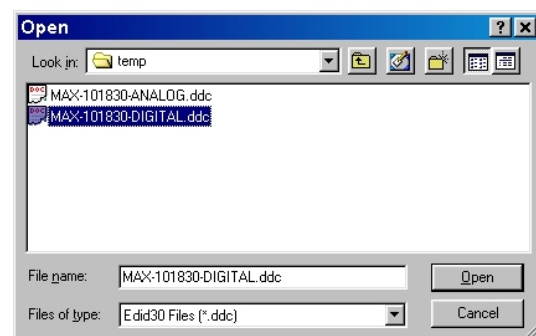


Fig. 44

Step 9: Exit DDC program

Pull down the File menu and select Exit as shown in Fig. 27.

EDID log file

Vendor/Product Identification

ID Manufacturer Name : MAX
 ID Product Code : 0726 (HEX.)
 ID Serial Number : 1234 (HEX.)
 Week of Manufacture : 22
 Year of Manufacture : 2002

EDID Version, Revision

Version : 1
 Revision : 3

Basic Display Parameters/Features

Video Input Definition : Analog Video Input
 0.700V/0.300V (1.00Vpp)
 without Blank_to_Black
 Setup
 Separate Sync
 Composite Sync
 Sync on Green
 Serration required

Maximum H Image Size : 36
 Maximum V Image Size : 29

Display Transfer Characteristic : 2.2
 (gamma)

Feature Support (DPMS) : Standby
 Suspend
 Active Off

Display Type : RGB colordisplay
 Preferred Timing Mode : Detailed timing block 1

Color Characteristics

Red X coordinate : 0.633
 Red Y coordinate : 0.34
 Green X coordinate : 0.295
 Green Y coordinate : 0.591
 Blue X coordinate : 0.141
 Blue Y coordinate : 0.096
 White X coordinate : 0.313
 White Y coordinate : 0.329

Established Timings

Established Timings I : 720 x 400 @ 70Hz (IBM,VGA)
 640 x 480 @ 60Hz (IBM,VGA)
 640 x 480 @ 75Hz (VESA)
 800 x 600 @ 60Hz (VESA)

Established Timings II : 800 x 600 @ 75Hz (VESA)
 832 x 624 @ 75Hz (Apple,Mac II)
 1024 x 768 @ 60Hz (VESA)
 1024 x 768 @ 75Hz (VESA)
 1280 x 1024 @ 75Hz (VESA)

Manufacturer's timings : 1152 x 870 @ 75Hz (Apple,Mac II)

Standard Timing Identification #1

Horizontal active pixels : 640
 Aspect Ratio : 4:3
 Refresh Rate : 60

Standard Timing Identification #2

Horizontal active pixels : 800
 Aspect Ratio : 4:3
 Refresh Rate : 60

Standard Timing Identification #3

Horizontal active pixels : 1024
 Aspect Ratio : 4:3
 Refresh Rate : 60

Standard Timing Identification #4

Horizontal active pixels : 1024
 Aspect Ratio : 4:3
 Refresh Rate : 70

Standard Timing Identification #5

Horizontal active pixels : 1280
 Aspect Ratio : 5:4
 Refresh Rate : 60

Detailed Timing #1

Pixel Clock (MHz) : 135
 H Active (pixels) : 1280
 H Blanking (pixels) : 408
 V Active (lines) : 1024
 V Blanking (lines) : 42
 H Sync Offset (F Porch) (pixels) : 16
 H Sync Pulse Width (pixels) : 144
 V Sync Offset (F Porch) (lines) : 1
 V Sync Pulse Width (lines) : 3
 H Image Size (mm) : 359
 V Image Size (mm) : 287
 H Border (pixels) : 0
 V Border (lines) : 0
 Flags : Non_interlaced
 : Normal Display, No stereo
 : Digital Separate sync.
 : Negative Vertical Sync.
 : Negative Horizontal Sync.

Monitor Descriptor #2

Monitor Name : Belinea101830

Monitor Descriptor #3

Serial Number : TY 123456

Monitor Descriptor #4

Monitor Range Limits
 Min. Vt rate Hz : 56
 Max. Vt rate Hz : 75
 Min. Horiz. rate kHz : 30
 Max. Horiz. rate kHz : 80
 Max. Supported Pixel : 140

No secondary GTF timing formula supported.

Extension Flag : 0

Check sum : DE (HEX.)

EDID data (128 bytes)

0: 00 1: ff 2: ff 3: ff 4: ff 5: ff 6: ff 7: 00
 8: 34 9: 38 10: 26 11: 07 12: 34 13: 12 14: 00 15: 00
 16: 16 17: 0c 18: 01 19: 03 20: 0f 21: 24 22: 1d 23: 78
 24: ea 25: 09 26: 25 27: a2 28: 57 29: 4b 30: 97 31: 24
 32: 18 33: 50 34: 54 35: a5 36: 6b 37: 80 38: 31 39: 40
 40: 45 41: 40 42: 61 43: 40 44: 61 45: 4a 46: 81 47: 80
 48: 01 49: 01 50: 01 51: 01 52: 01 53: 01 54: bc 55: 34
 56: 00 57: 98 58: 51 59: 00 60: 2a 61: 40 62: 10 63: 90
 64: 13 65: 00 66: 67 67: 1f 68: 11 69: 00 70: 00 71: 18
 72: 00 73: 00 74: 00 75: fc 76: 00 77: 42 78: 65 79: 6c
 80: 69 81: 6e 82: 65 83: 61 84: 31 85: 30 86: 31 87: 38
 88: 33 89: 30 90: 00 91: 00 92: 00 93: ff 94: 00 95: 54
 96: 59 97: 20 98: 20 99: 31 100: 32 101: 33 102: 34 103: 35
 104: 36 105: 0a 106: 20 107: 20 108: 00 109: 00 110: 00 111: fd
 112: 00 113: 38 114: 4b 115: 1e 116: 50 117: 0e 118: 00 119: 0a
 120: 20 121: 20 122: 20 123: 20 124: 20 125: 20 126: 00 127: de

DDC Data of Digital

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EDID log file

Vendor/Product Identification

ID Manufacturer Name : MAX
ID Product Code : 0726 (HEX.)
ID Serial Number : 1234 (HEX.)
Week of Manufacture : 22
Year of Manufacture : 2002

EDID Version, Revision

Version : 1
Revision : 3

Basic Display Parameters/Features

Video Input Definition : Digital Video Input

Maximum H Image Size : 36
Maximum V Image Size : 29

Display Transfer Characteristic : 2.2
(gamma)

Feature Support (DPMS) : Standby
Suspend
Active Off

Display Type : RGB color display
Preferred Timing Mode : Detailed timing block 1

Color Characteristics

Red X coordinate : 0.633
Red Y coordinate : 0.34
Green X coordinate : 0.295
Green Y coordinate : 0.591
Blue X coordinate : 0.141
Blue Y coordinate : 0.096
White X coordinate : 0.313
White Y coordinate : 0.329

Established Timings

Established Timings I : 720 x 400 @ 70Hz (IBM,VGA)
640 x 480 @ 60Hz (IBM,VGA)
640 x 480 @ 75Hz (VESA)
800 x 600 @ 60Hz (VESA)

Established Timings II : 800 x 600 @ 75Hz (VESA)
832 x 624 @ 75Hz (Apple, Mac II)
1024 x 768 @ 60Hz (VESA)
1024 x 768 @ 75Hz (VESA)
1280 x 1024 @ 75Hz (VESA)

Manufacturer's timings : 1152 x 870 @ 75Hz (Apple, Mac II)

Standard Timing Identification #1

Horizontal active pixels : 640
Aspect Ratio : 4:3
Refresh Rate : 60

Standard Timing Identification #2

Horizontal active pixels : 800
Aspect Ratio : 4:3
Refresh Rate : 60

Standard Timing Identification #3

Horizontal active pixels : 1024
Aspect Ratio : 4:3
Refresh Rate : 60

Standard Timing Identification #4

Horizontal active pixels : 1024
Aspect Ratio : 4:3
Refresh Rate : 70

Standard Timing Identification #5

Horizontal active pixels : 1280
Aspect Ratio : 5:4
Refresh Rate : 60

Detailed Timing #1

Pixel Clock (MHz) : 108
H Active (pixels) : 1280
H Blanking (pixels) : 408
V Active (lines) : 1024
V Blanking (lines) : 42
H Sync Offset (F Porch) (pixels) : 48
H Sync Pulse Width (pixels) : 112
V Sync Offset (F Porch) (lines) : 1
V Sync Pulse Width (lines) : 3
H Image Size (mm) : 359
V Image Size (mm) : 287
H Border (pixels) : 0
V Border (lines) : 0
Flags : Non_interlaced
Normal Display, No stereo
Digital Separate sync.
Negative Vertical Sync.
Negative Horizontal Sync.

Monitor Descriptor #2

Monitor Name : Belinea101830

Monitor Descriptor #3

Serial Number : TY 123456

Monitor Descriptor #4

Monitor Range Limits
Min. Vt rate Hz : 56
Max. Vt rate Hz : 75
Min. Horiz. rate kHz : 30
Max. Horiz. rate kHz : 80
Max. Supported Pixel : 140

No secondary GTF timing formula supported.

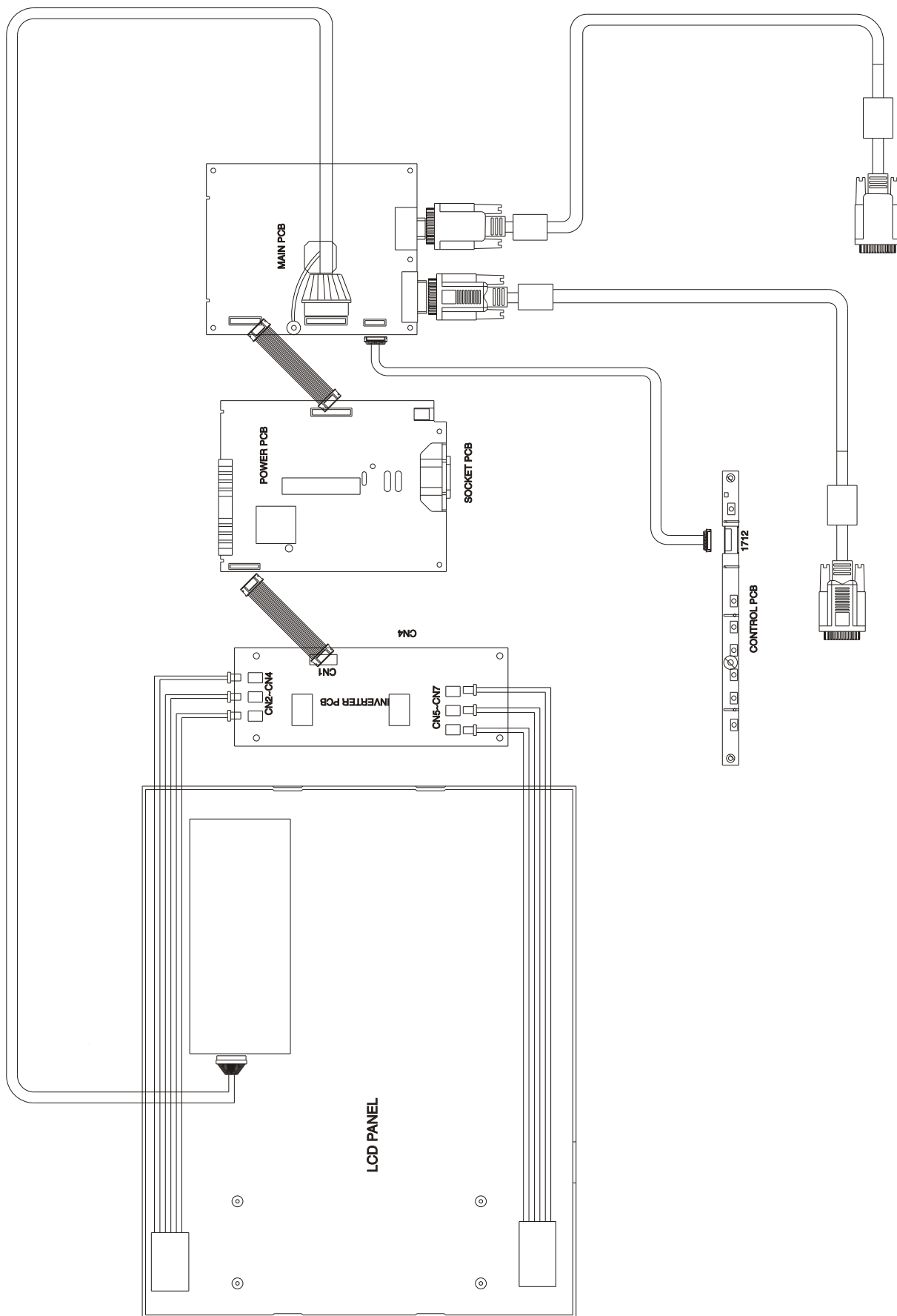
Extension Flag : 0

Check sum : 03 (HEX.)

EDID data (128 bytes)

0: 00 1: ff 2: ff 3: ff 4: ff 5: ff 6: ff 7: 00
8: 34 9: 38 10: 26 11: 07 12: 34 13: 12 14: 00 15: 00
16: 16 17: 0c 18: 01 19: 03 20: 80 21: 24 22: 1d 23: 78
24: ea 25: 09 26: 25 27: a2 28: 57 29: 4b 30: 97 31: 24
32: 18 33: 50 34: 54 35: a5 36: 6b 37: 80 38: 31 39: 40
40: 45 41: 40 42: 61 43: 40 44: 61 45: 4a 46: 81 47: 80
48: 01 49: 01 50: 01 51: 01 52: 01 53: 01 54: 30 55: 2a
56: 00 57: 98 58: 51 59: 00 60: 2a 61: 40 62: 30 63: 70
64: 13 65: 00 66: 67 67: 1f 68: 11 69: 00 70: 00 71: 18
72: 00 73: 00 74: 00 75: fc 76: 00 77: 42 78: 65 79: 6c
80: 69 81: 6e 82: 65 83: 61 84: 31 85: 30 86: 31 87: 38
88: 33 89: 30 90: 00 91: 00 92: 00 93: ff 94: 00 95: 54
96: 59 97: 20 98: 20 99: 31 100: 32 101: 33 102: 34 103: 35
104: 36 105: 0a 106: 20 107: 20 108: 00 109: 00 110: 00 111: fd
112: 00 113: 38 114: 4b 115: 1e 116: 50 117: 0e 118: 00 119: 0a
120: 20 121: 20 122: 20 123: 20 124: 20 125: 20 126: 00 127: 03

Model : DJ9B3L/00Z



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0. General

When carry-out the electrical settings in many cases a video signal must be applied to the monitor.

0.1 AC/DC Measurement:

The measurements for AC waveform and DC figure is based on 640 x 480 31.5 kHz/60 Hz resolution mode with test pattern

"16 gray scale".

Power input: 110V AC

General points

- 1.1 During the test and measuring, supply a distortion free AC mains voltage to the apparatus via an isolated transformer with low internal resistance.
- 1.2 All measurements mentioned hereafter are carried out at a normal mains voltage (90 - 132 VAC for USA version, 195 -264 VAC for EUROPEAN version, or 90 - 264 VAC for the model with full range power supply, unless otherwise stated.)
- 1.3 All voltages are to be measured or applied with respect to ground, unless otherwise stated.
Note: don't use heat-sink as ground.
- 1.4 The test has to be done on a complete set including LCD panel in a room with temperature of 25 +/- 5 degree C.
- 1.5 All values mentioned in these test instruction are only applicable of a well aligned apparatus, with correct signal.
- 1.6 The letters symbols (B) and (S) placed behind the test instruction denotes
(B): carried out 100% inspection at assembly line
(S): carried out test by sampling
- 1.7 The white balance (color temperature), has to be tested in subdued lighted room.
- 1.8 Repetitive power on/off cycle are allowed.

2. Input signal

2.1 Signal type

Video : 0.7 Vp-p linear, positive polarity

Sync. : TTL level, separate, positive or negative polarity

Signal source: pattern generator format as attachment.

(table 1 to 32) Reference generator : QuantumData 801GD

2.2 Input signal mode

Pre-set 32 modes (P)

PRESET VIDEO RESOLUTION

#	Resolution	Frequency	Pixel rate	Sync	Comment
1	640X350	31.5K/70HZ	25.175	(+/-)	IBM VGA 10h
2	720X400	31.5K/70HZ	28.322	(-/+)	IBM VGA 3h
3	640X480	37.5K/75HZ	31.501	(-/-)	
4	640X480	43.3K/85HZ	36	(-/-)	
5	640X480	37.9K/72HZ	31.5	(-/-)	
6	640X480	35.0K/67HZ	30.24	(-/-)	
7	640X480	31.5K/60HZ	25.175	(-/-)	
8	800X600	35.2K/56HZ	36	(+/-)	
9	800X600	46.9K/75HZ	49.498	(+/-)	
10	800X600	37.9K/60HZ	40	(+/-)	
11	800X600	53.7K/85HZ	56.251	(+/-)	
12	832X624	49.7K/75HZ	57.28	(+/-)	MAC
13	800X600	48.1K/72HZ	50	(+/-)	
14	1024X768	60.0K/75HZ	78.75	(+/-)	
15	1024X768	48.4K/60HZ	65	(-/-)	
16	1024X768	56.5K/70HZ	75	(-/-)	
17	1024X768	61.1K/76HZ	83.096	(+/-)	IBM XGA-2
18	1024X768	68.7K/85HZ	94.5	(+/-)	
19	1152X864	67.5K/75HZ	108	(+/-)	
20	1152X864	63.9K/70HZ	94.5	(+/-)	non-VESA
21	1152X864	54.0K/60HZ	79.9	(+/-)	non-VESA
22	1152X870	68.7K/75HZ	100	(-/-)	MAC
23	1152X900	61.8K/66HZ	92.94	serr-	SUN Mode IV
24	1152X900	71.8K/76HZ	108	(+/-)	SUN Mode II
25	1280X960	60.0K/60HZ	108	(+/-)	
26	1280X960	75.0K/75HZ	129.895	(+/-)	non-VESA
27	1280X1024	76.0K/72HZ	130.223	(+/-)	DOS/V
28	1280X1024	64.0K/60HZ	108	(+/-)	
29	1280X1024	80.0K/75HZ	135	(+/-)	
30	1280X1024	81.1K/76HZ	135.008	(-/-)	SUN Mode I
31	1280X1024	71.7K/67HZ	117	(+/-)	SUN Mode V
32	688X556	31.3K/50HZ	27	(-/+)	TV-PAL

2.3 Allowed 85 Hz overscan signal mode specified

Once the signal input of PC is 85Hz, this monitor is able to display at least for 10 minutes . An attention signal appears and shows

This is 85Hz overscan for 5 seconds, change computer display input to 1024X768 @ 60 Hz

Dot rate (MHz)	H.freq (KHz)	Mode	Resolution	V.freq (Hz)
36.000	43.269	VESA	640 * 480	85.008
56.250	53.674	VESA	800 * 600	85.061
94.500	68.677	VESA	1024 * 768	84.997

3. AC, DC Adaptor

3.1 Setup the AC I/P at 90VAC, and Output DC loading at 5Amp,
The DC output voltage is 12.0 1 V DC, Adjusting is no need.

3.2 DC setting

3.01 Detach 8052 to the scaler board before testing the function.

3.02 Apply 12V DC voltage to 1105. (B)

3.03 Connect inverter board to 8051 (B)

3.04 Check voltage at C2067 should be within the range 5V +/- 0.2V

(B)

3.05 Check voltage at C2077 should be within the range 3.3V +/- 0.1V

(B)

4. Display Adjustment

4.1 Input signals check

In factory mode, use 64 gray level and set the R,G,B gain to 100%.

4.2 Display quality test

Use timing mode as describe in 2.2, and use the pixel on / off pattern to adjust the clock until no stripe and adjust the phase until clear picture.

Check all pre-setting 32 modes.

4.2.1 SOG mode test : Use following timing for SOG test

64 KHz/60Hz, 1280 X 1024, pixel=108 MHz

Horizontal		Vertical	
Frame border =	0	Frame border =	0
Total size =	15.620 s	Total size =	16.670 ms
Display size =	11.852 s	Display size =	16.000 ms
Rear porch =	2.000 s	Rear porch =	0.580 ms
Sync width =	1.000 s	Sync width =	0.080 ms
Sync polarity =	-	Sync polarity =	-
		Sync on green	

4.3 Check of WHITE-D (B)

Apply a 1024*768 / 60Hz signal with white pattern, set brightness control at 100%, and contrast control at 70%. Adjust the R,G,B sub_gain, for the screen center, the 1931 CIE chromaticity (X, Y) co-ordinates shall be;

	9300 K		6500 K	
x (center)	0.281	0.020	0.312	0.020
y (center)	0.311	0.020	0.338	0.020

Use Minolta CA-110 for color coordinates and luminance check.

Luminance : 200 Nits in the center of the screen when brightness at 100% and contrast set to 100%.

4.4 Check the digital interface

Check the 64 gray level color poor & noise condition.

5. PIN ASSIGNMENT OF VARIOUS WAFERS

5.1. PIN ASSIGNMENT OF INVERTER & WAFER 8051

PIN No. SIGNAL (INVERTER BOARD)

1	backlight GND
2	backlight GND
3	backlight GND
4	backlight brightness; 3.3 V : 6.2 mA 0.3 mA 0 V : 2.05 mA 0.2 mA
5	backlight ; ON/OFF control : ON > 2V, OFF < 1V
6	backlight power (12V)
7	backlight power (12V)
8	backlight power (12V)

PIN No. SIGNAL (WAFER 8051)

1	backlight power (12V)
2	backlight power (12V)
3	backlight power (12V)
4	backlight brightness; 3.3 V : 6.2 mA 0.3 mA 0 V : 2.0 mA 0.2 mA
5	backlight ; ON/OFF control : ON > 2V, OFF < 1V
6	backlight GND
7	backlight GND
8	backlight GND

5.2 PIN ASSIGNMENT OF WAFER 8052

PIN No. SIGNAL (WAFER 8052)

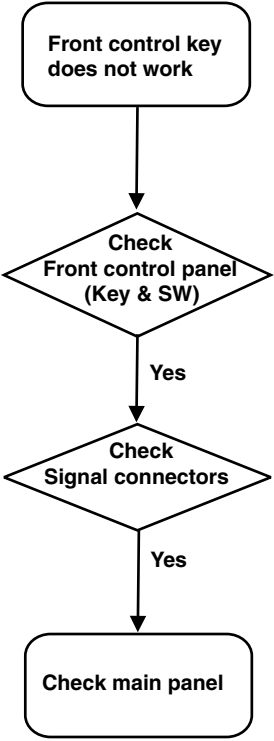
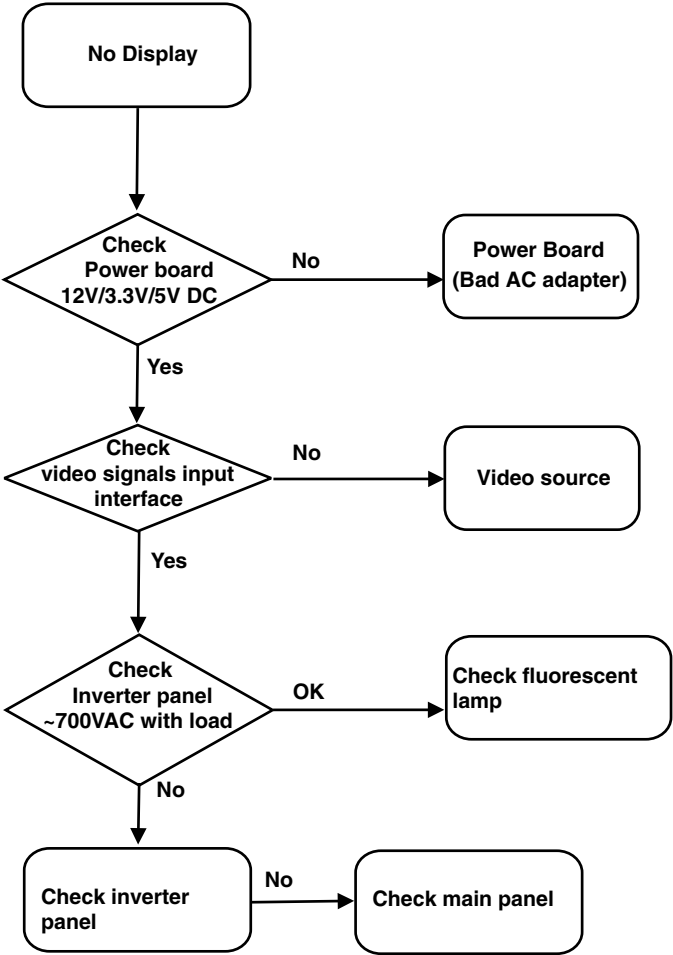
1	LIGHT_BRI_CTL
2	LIGHT_PWR_CTL
3	GND
4	GND
5	+3.3V
6	GND
7	+5V
8	+5V
9	GND
10	GND
11	+12V
12	+12V
13	GND
14	GND

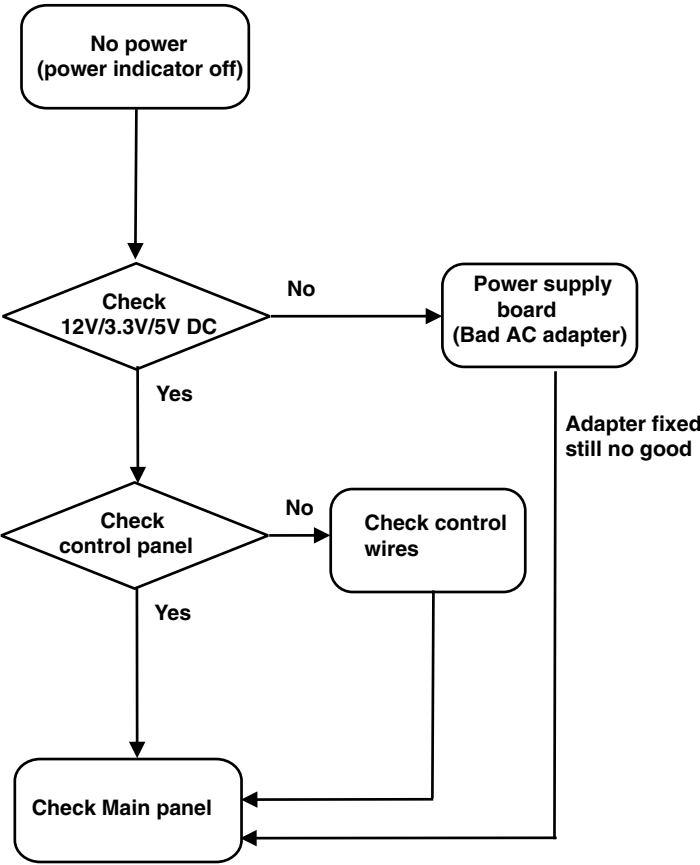
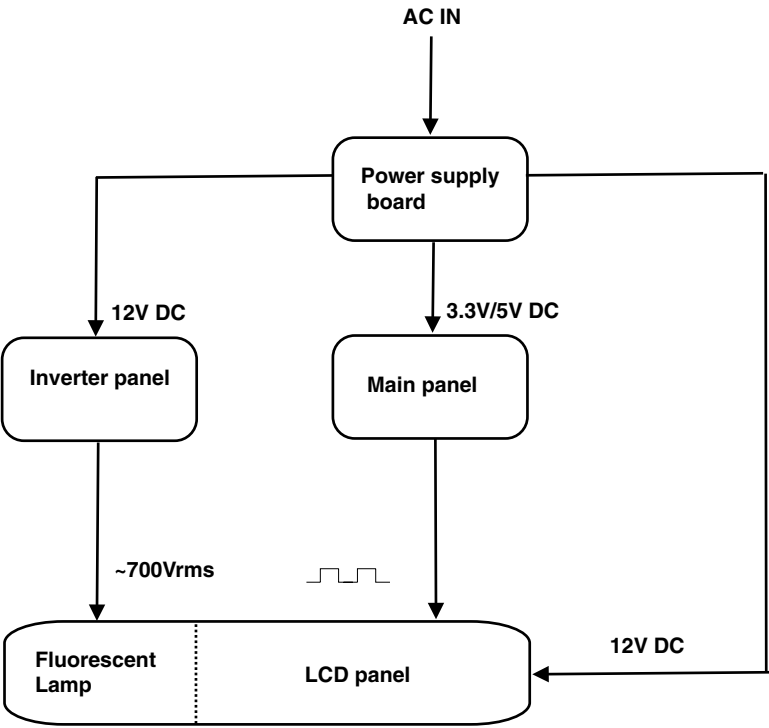
5.3 PIN ASSIGNMENT OF WAFER 1406

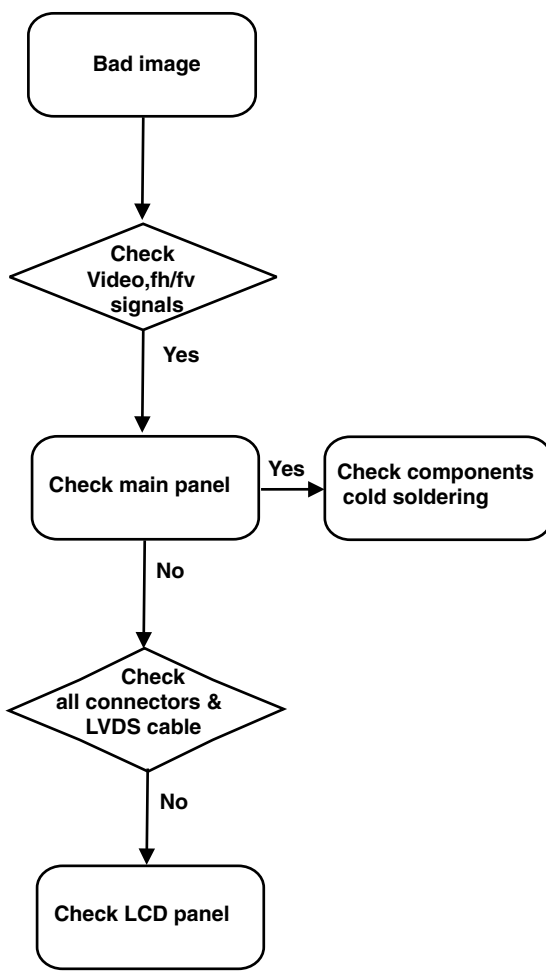
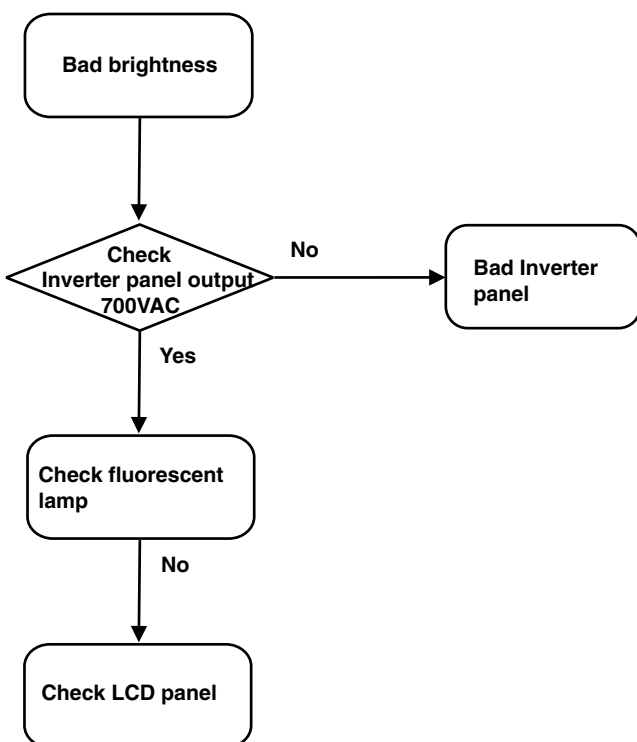
PIN No. SIGNAL (WAFER 1406)

1	DC_SW
2	+3.3V
3	KEY1
4	GND
5	KEY2
6	LEDR
7	LEDG

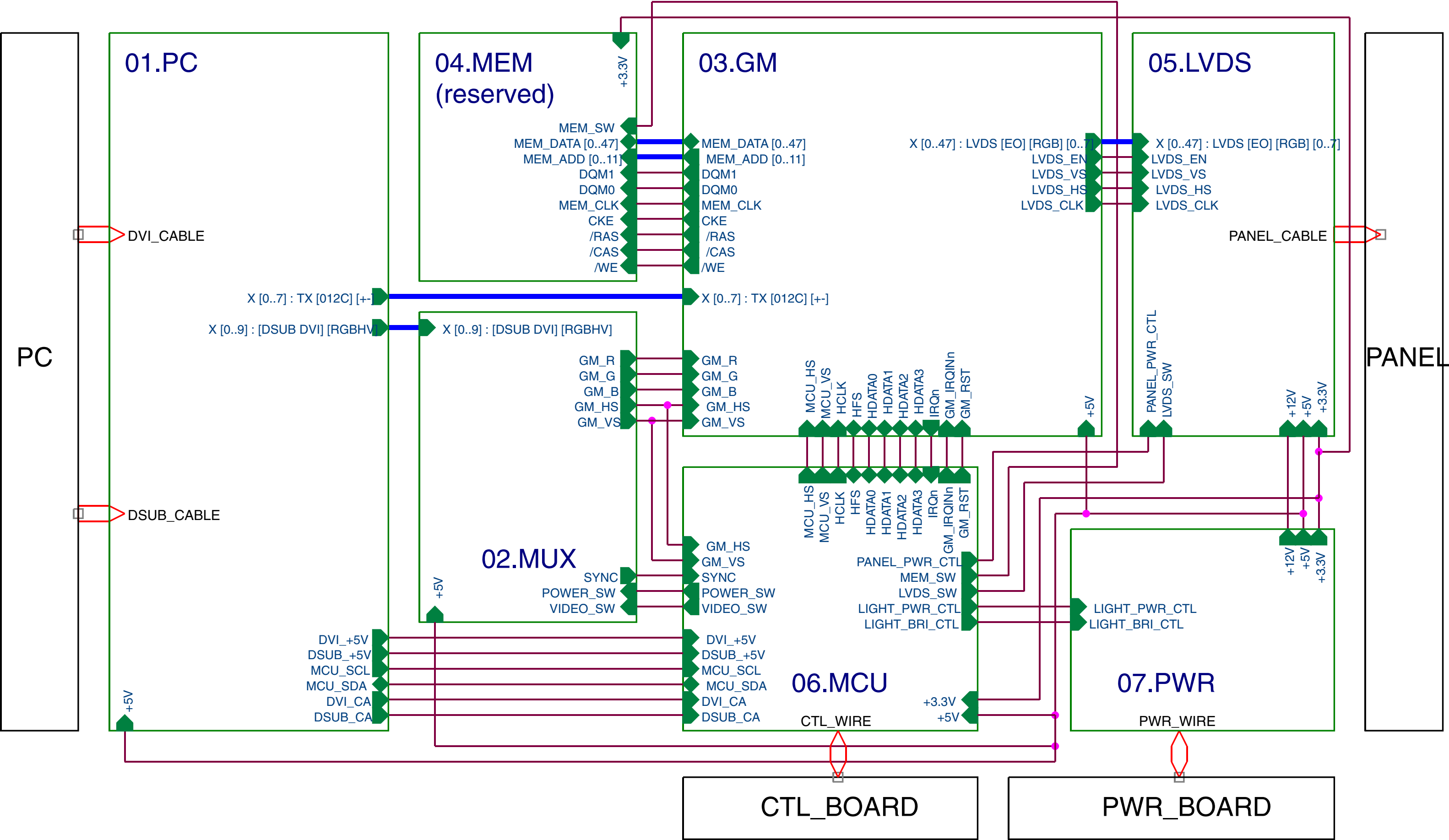
Repair Flow Chart

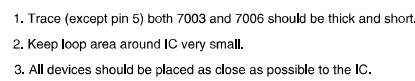


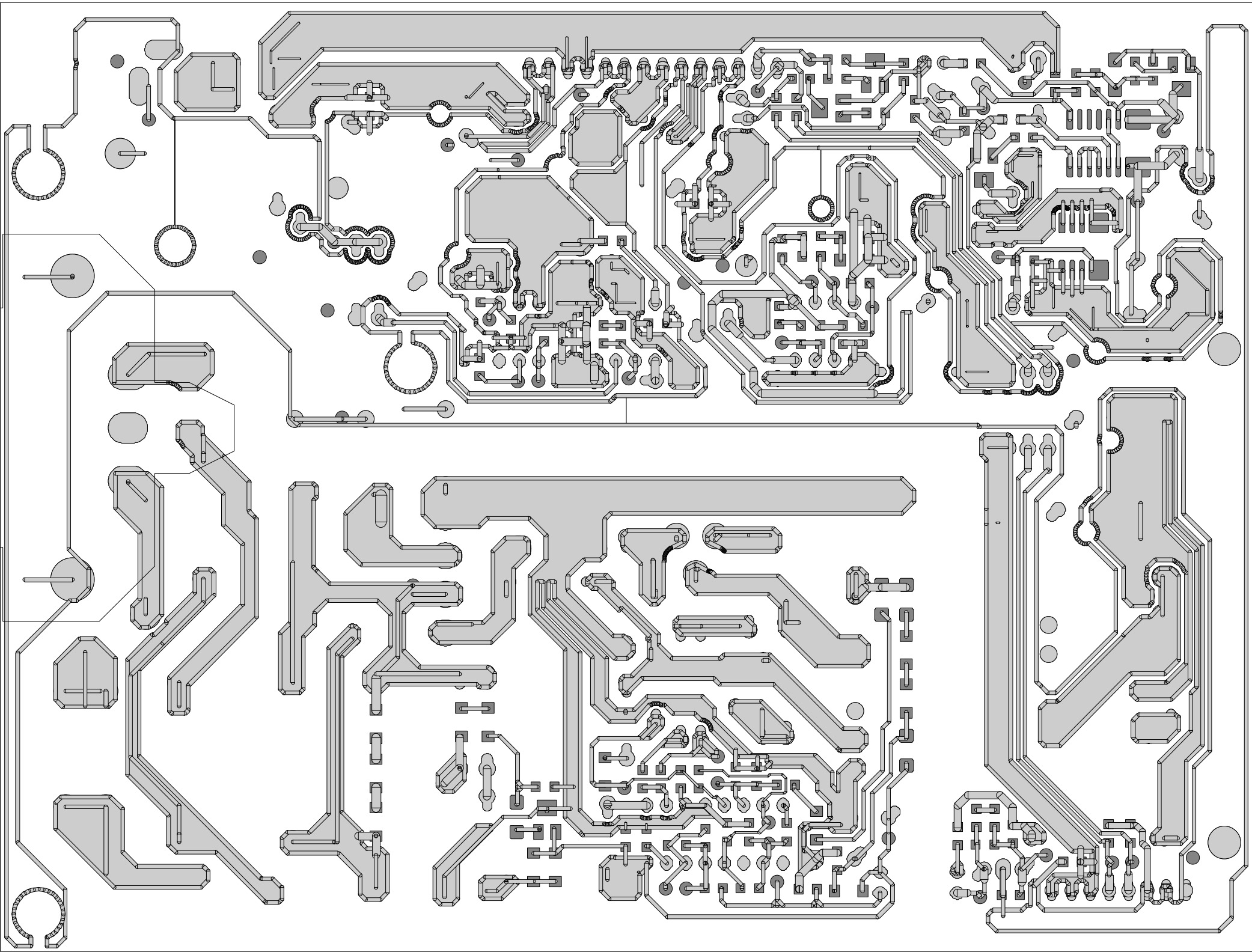




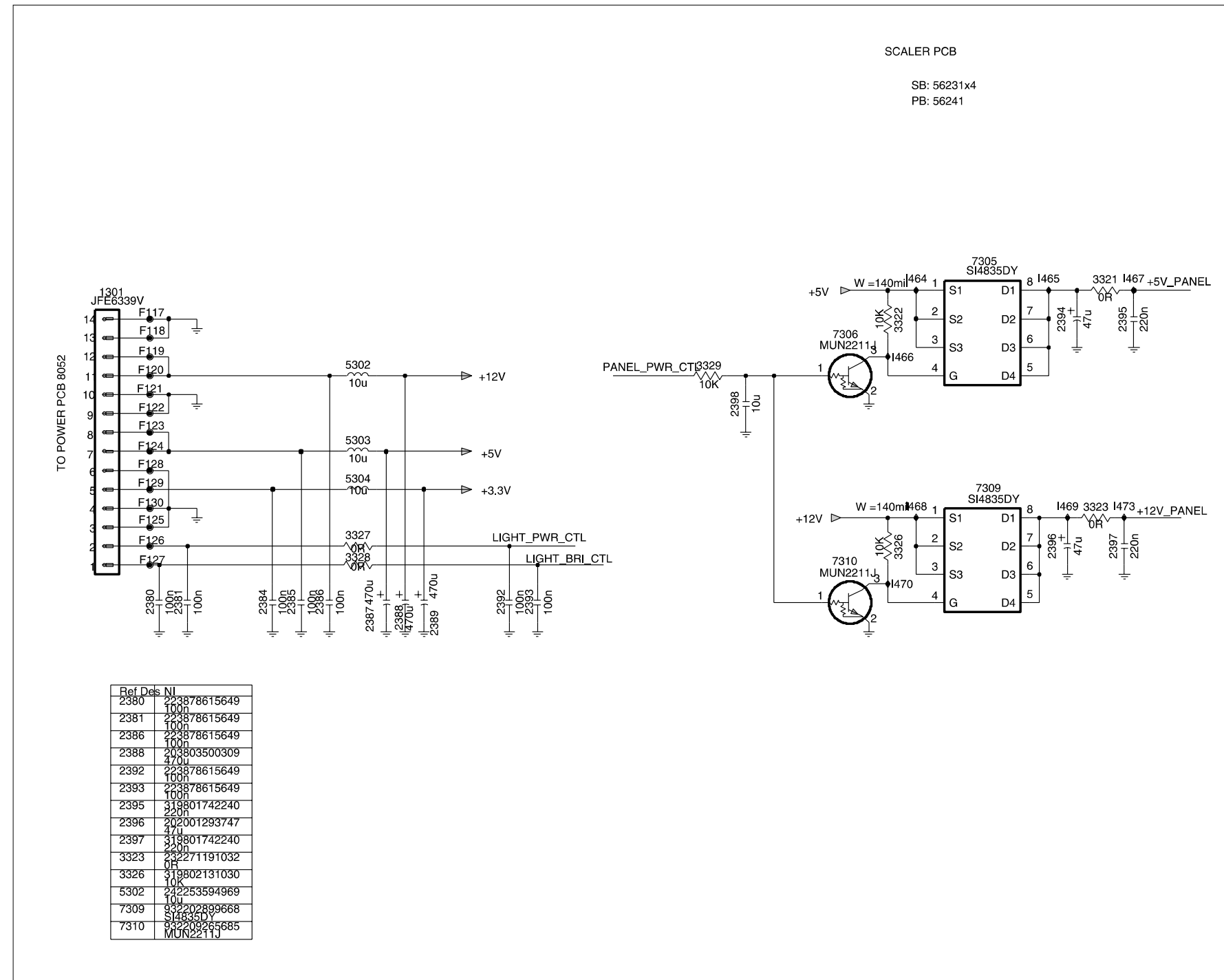
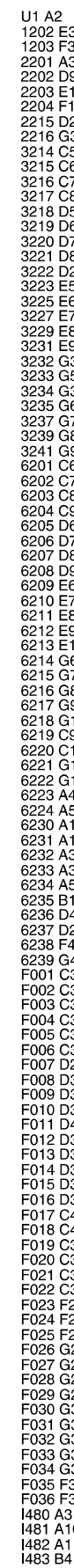
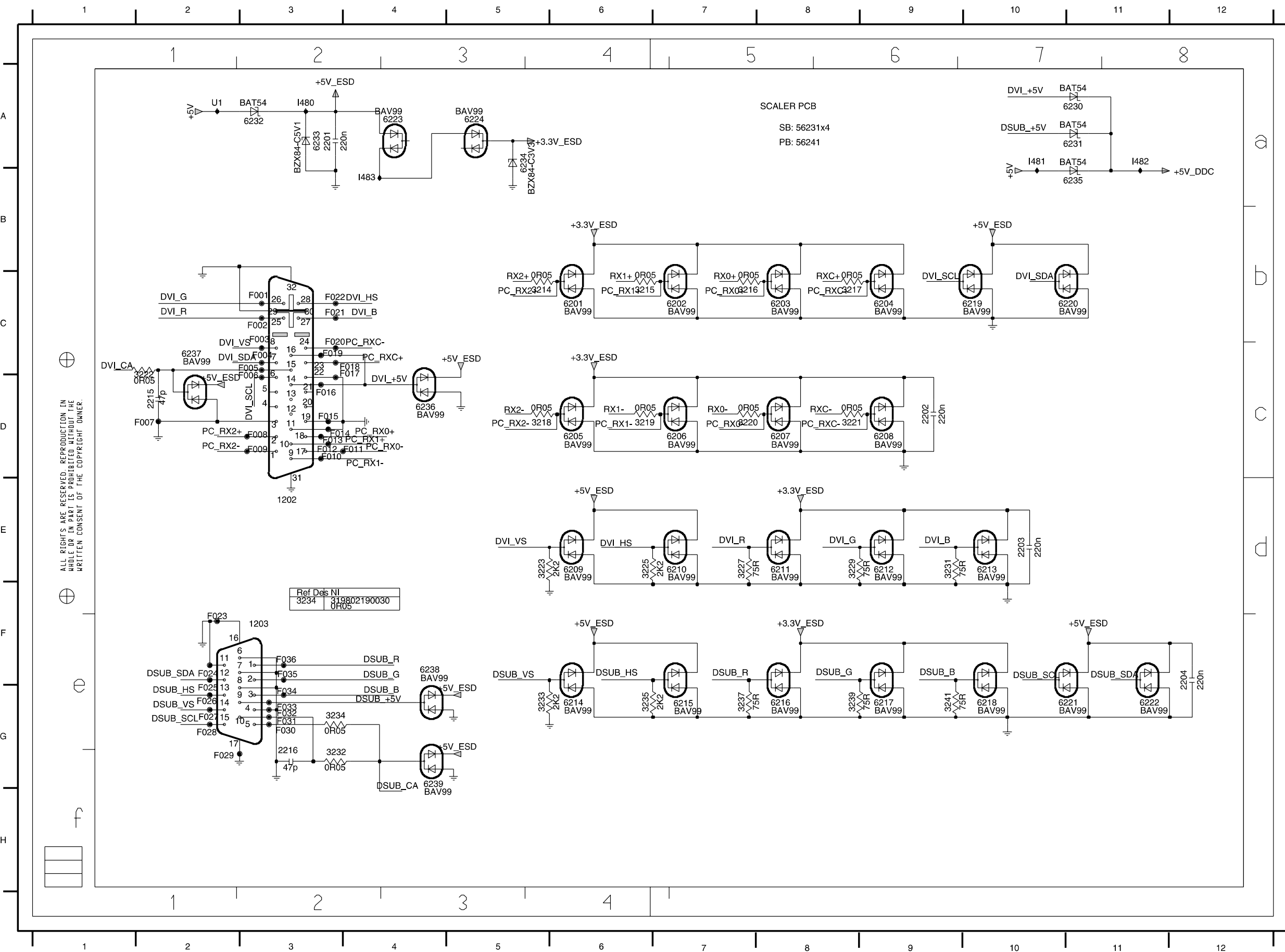
Block Diagram



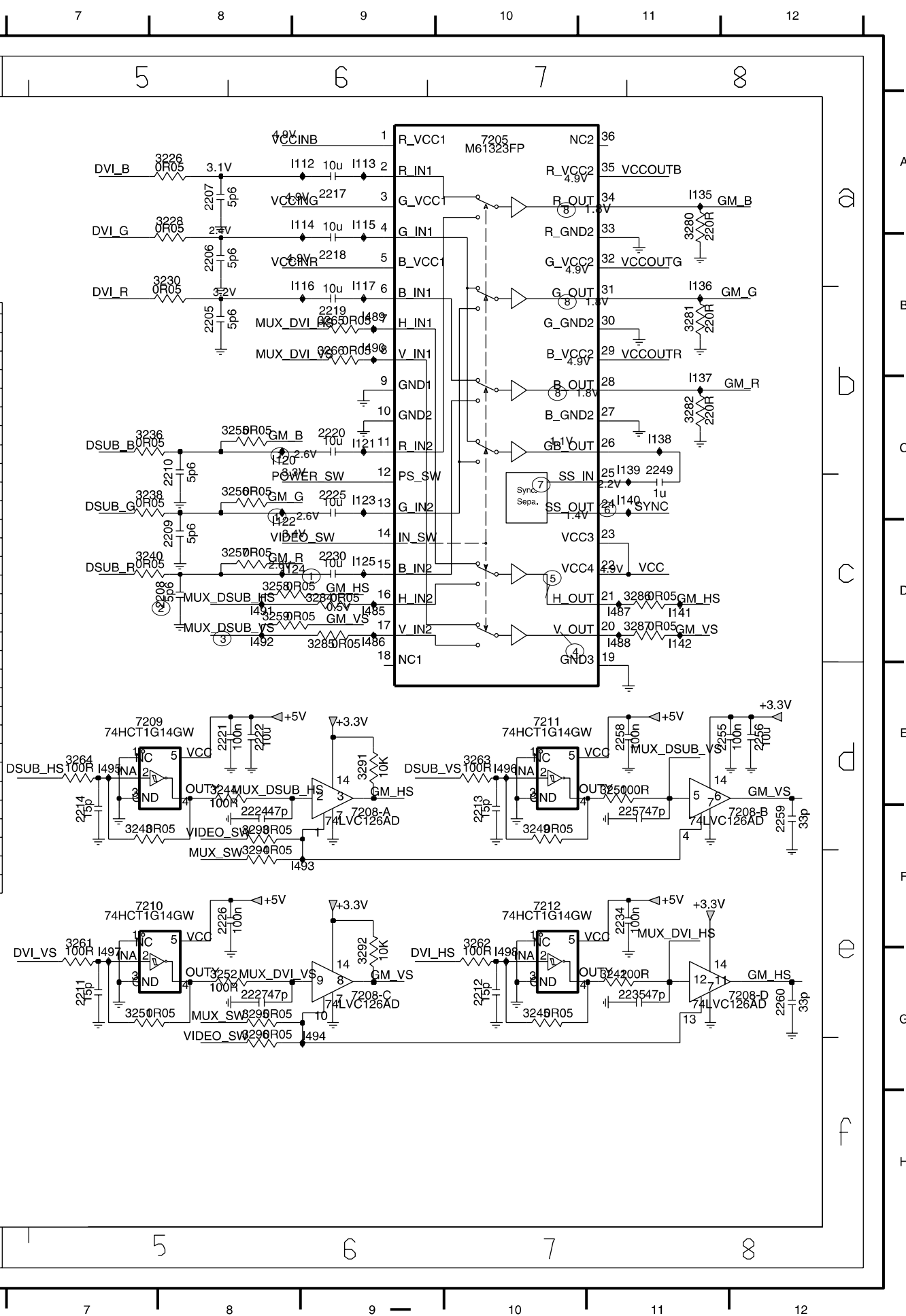
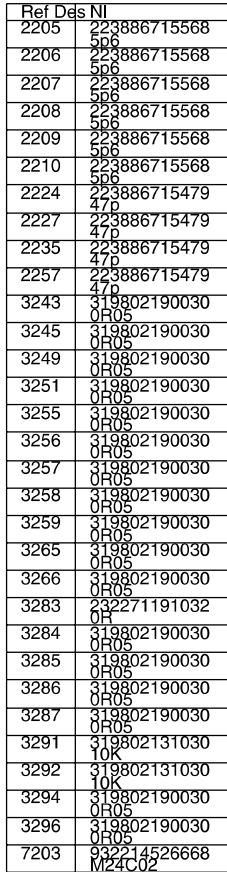
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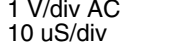
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1101 A1	7071 A2	2055 B3	3111 B2
1102 B1	7085 A3	2056 B3	3116 B2
1105 A1	7101 B2	2062 A1	3117 B2
2061 A1	7102 A2	2063 A1	3118 B2
2067 A1	7104 B3	2064 A2	3119 B2
2068 A1	7106 B3	2065 A2	3120 B1
2077 A2	8051 B3	2066 A2	3122 B3
2080 A3	8052 A2	2069 A1	3123 B3
2081 A3	8102 A1	2070 A2	3124 B2
2082 A3	9005 A1	2072 A2	3125 B3
2084 A3	9007 B2	2073 A2	3126 B3
2101 A1	9008 A2	2074 A2	3128 B2
2102 B1	9012 B3	2075 A2	3129 B2
2103 A1	9013 A3	2076 A2	3131 B1
2104 A1	9014 A3	2078 A2	3132 B1
2105 A1	9015 A3	2079 A1	3133 B1
2111 B2	9016 A3	2083 A3	3134 B2
2112 A1	9017 A3	2085 A2	3136 B2
2113 B3	9018 A3	2106 B2	3191 B2
2115 B3	9019 A3	2107 B2	3192 B2
2116 A3	9021 A3	2108 B2	6081 A3
2117 A3	9022 A2	2109 B2	6107 A3
2118 A3	9023 A1	2110 B2	6108 A2
2121 A3	9024 A2	2114 B2	6112 B1
2123 B1	9025 A2	2119 B3	6113 B3
2124 B2	9026 A1	2120 B3	7051 A2
3051 A2	9027 A2	2122 B2	7052 A2
3053 A2	9028 A1	3052 A2	7081 A3
3063 A1	9030 A3	3054 A2	7082 A3
3064 A1	9031 A3	3056 A2	7083 A3
3085 A3	9033 A3	3057 A2	7084 A3
3101 B1	9034 A3	3059 A2	7107 B1
3104 B1	9035 A1	3061 A1	
3112 B2	9036 A1	3062 A2	
3113 B2	9101 A1	3071 A2	
3114 B2	9111 A1	3072 A2	
3115 A2		3073 A2	
3121 B3		3074 A2	
3127 B2		3075 A3	
3130 A3		3076 A3	
3181 A1		3080 A2	
4101 A2		3081 A3	
4102 A3		3082 A3	
5061 A2		3083 A3	
5071 A2		3084 A3	
5101 B1		3086 A3	
5103 B3		3087 A3	
5105 A1		3088 A3	
5106 B1		3089 A3	
6061 A1		3091 A3	
6062 A1		3092 A2	
6071 A2		3093 A2	
6072 A2		3102 B1	
6101 A1		3103 B1	
6105 B1		3105 B1	
6106 B2		3106 B2	
6109 A3		3107 B2	
6111 B2		3108 B2	
6114 B1		3109 B2	

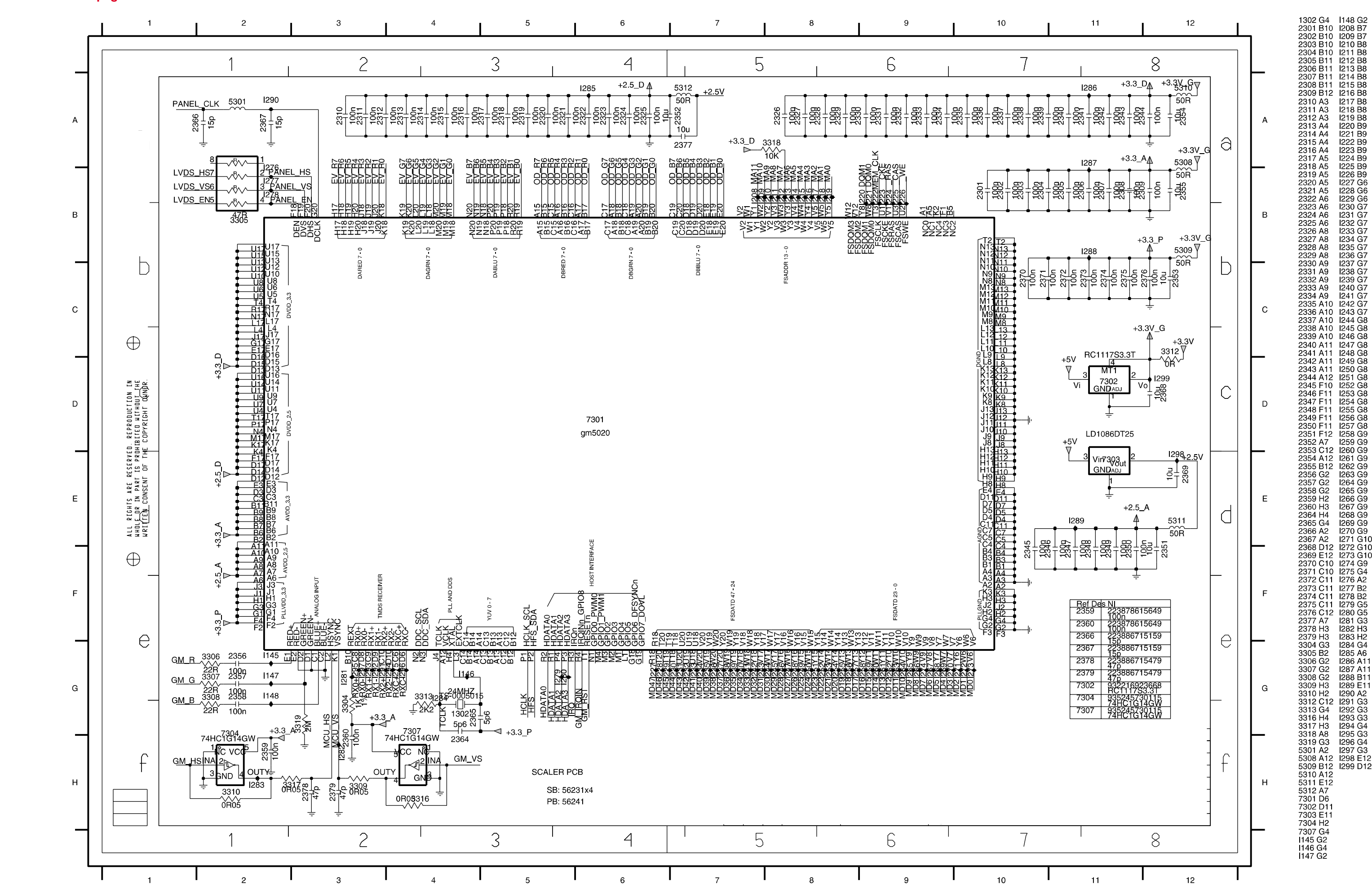


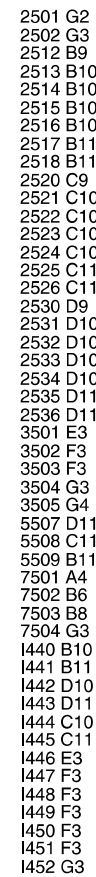
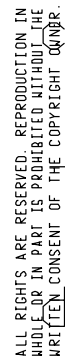
1301 C2
2380 E2
2381 E3
2384 E3
2385 E3
2386 E4
2387 F4
2388 F4
2389 F4
2392 E5
2393 E5
2394 C9
2395 C10
2396 E9
2397 E10
2398 D7
3321 C9
3322 C8
3323 E9
3326 E8
3327 E4
3328 E4
3329 D7
5302 D4
5303 D4
5304 E4
7305 C8
7306 C7
7309 E9
7310 E7
F117 C2
F118 C2
F119 D2
F120 D2
F121 D2
F122 D2
F123 D2
F124 D2
F125 E2
F126 E2
F127 E2
F128 D2
F129 E2
F130 E2
I464 C8
I465 C9
I466 D8
I467 C10
I468 E8
I469 E9
I470 E8
I473 E10

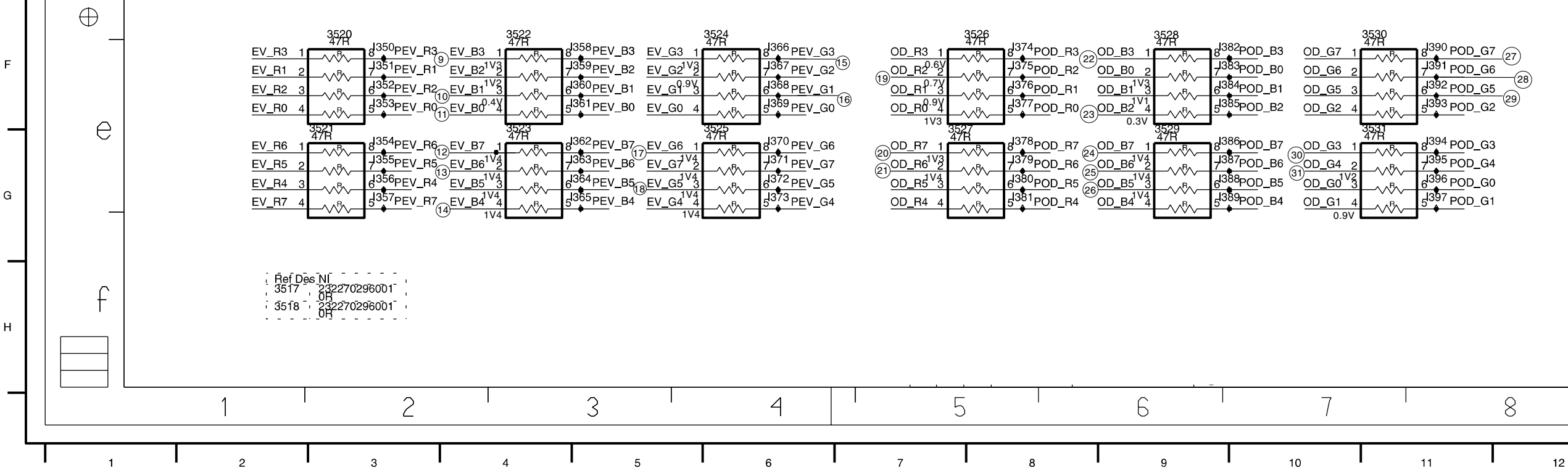


2205 B8	5203 H4
2206 B8	5204 H2
2207 A8	5205 F4
2208 D8	5206 G3
2209 D8	5207 F4
2210 C8	7201 A4
2211 F7	7202 D3
2212 G10	7203 C4
2213 F10	7205 A10
2217 F7	7206 F3
2217 A9	7207 G2
2218 B9	7208-A F9
2219 B9	7208-B F12
2220 C9	7208-C G9
2221 E8	7208-D G12
2222 E8	7209 E7
2223 A5	7210 F7
2224 F8	7211 E10
2225 C9	7212 F10
2226 F8	1102 C4
2227 G8	1103 B2
2228 D4	1106 D4
2230 D9	1108 E4
2231 F5	1109 H2
2232 G3	1110 E3
2233 F5	1111 F5
2234 F11	1112 A9
2235 G1	1113 A9
2236 H3	1114 A9
2237 H2	1115 A9
2238 F5	1116 B9
2239 G5	1117 B9
2240 G3	1120 C8
2241 G5	1121 C9
2242 F5	1122 D8
2243 H5	1123 C9
2244 G5	1124 D9
2245 G5	1125 D9
2246 H5	1126 G4
2247 F3	1127 F2
2248 F2	1130 G5
2249 C1	1131 G3
2251 B3	1132 G5
2252 C4	1133 F5
2253 E3	1134 H5
2254 E4	1135 A11
2255 E12	1136 B11
2256 E12	1137 C11
2257 F11	1138 C11
2258 E11	1139 C11
2259 F12	1140 C11
2260 G12	1141 D11
3226 A8	1142 D11
3228 A8	1485 D9
3230 B8	1486 D9
3236 C7	1487 D11
3238 C7	1488 D11
3240 D7	1489 B9
3242 G11	1490 B9
3243 F7	1491 D8
3244 E8	1492 D8
3245 G10	1493 F9
3246 B2	1494 G9
3247 A3	1495 E7
3248 A4	1496 E10
3249 F10	1497 G7
3250 E11	1498 G10
3251 F7	
3252 G8	
3253 C5	
3254 A4	
3255 C8	
3256 C8	
3257 D8	
3258 D8	
3259 D8	
3261 F7	
3262 F10	
3263 E10	
3264 E7	
3265 B9	
3266 B9	
3267 D3	
3274 D4	
3276 E3	
3279 D5	
3280 A11	
3281 B11	
3282 C11	
3283 B4	
3284 D9	
3285 D9	
3286 D11	
3287 D11	
3288 F3	
3290 F2	
3291 E9	
3292 G9	
3293 F8	
3294 F8	
3295 G8	
3296 G8	
5201 G4	
5202 G4	

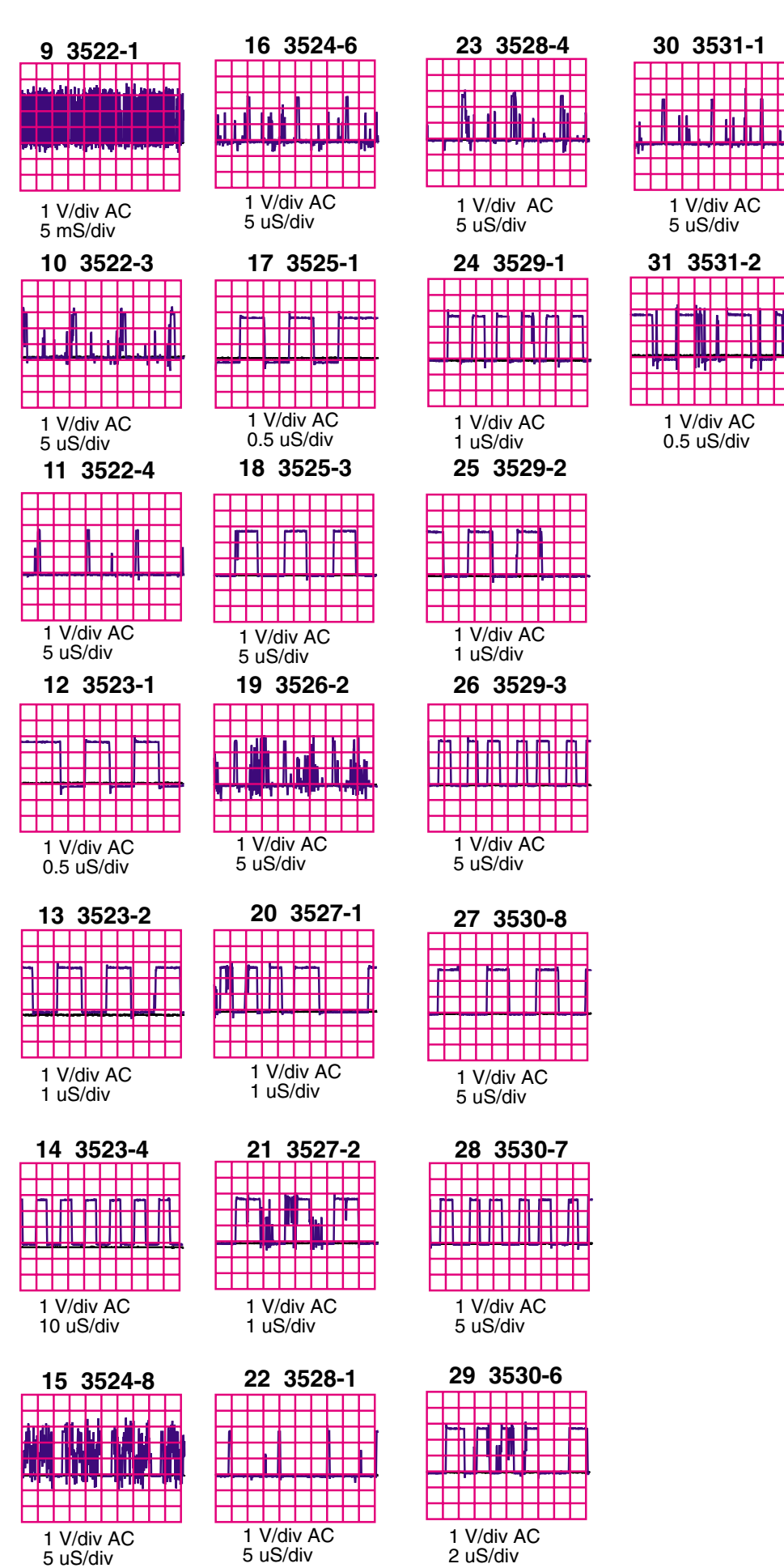




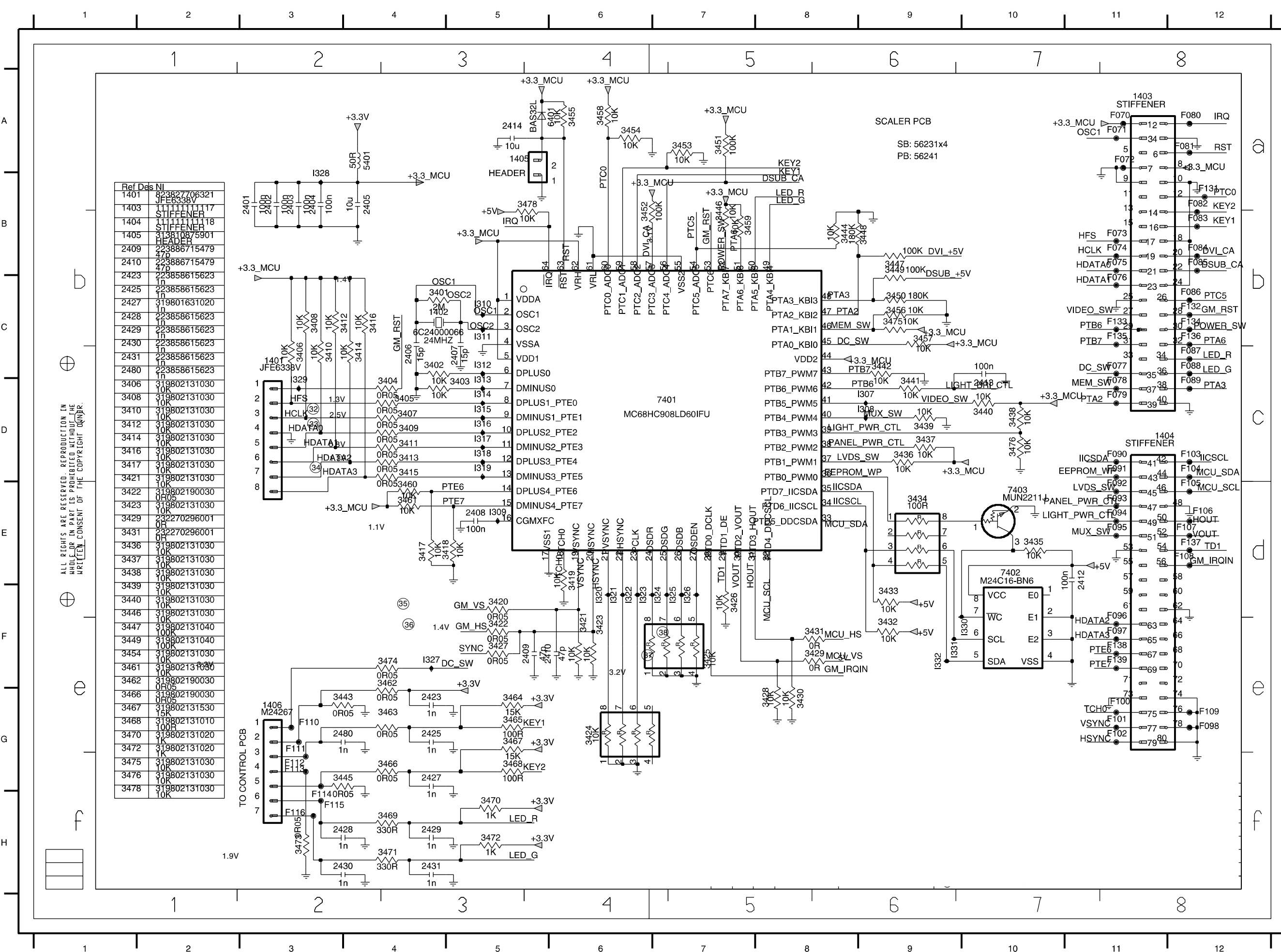




1501 E11	I367 F6
2500 A2	I368 F6
2503 B1	I369 F6
2504 B1	I370 G6
2505 B2	I371 G6
2506 B2	I372 G6
2507 B2	I373 G6
2508 B2	I374 F6
2509 B2	I375 F8
2510 C1	I376 F8
2511 C2	I377 F8
2519 C1	I378 G6
2527 D1	I379 G6
2528 D2	I380 G6
2529 D2	I381 G6
2540 B3	I382 F9
2541 B3	I383 F9
2542 B4	I384 F9
2543 B4	I385 F9
2544 B4	I386 G5
2545 B4	I387 G5
2546 B4	I388 G5
2547 C3	I389 G5
2548 C4	I390 F1
2549 C4	I391 F1
2550 D3	I392 F1
2551 D4	I393 F1
2552 D4	I394 F1
3510 D5	I395 G1
3511 D5	I396 G1
3513 A2	I397 G1
3514 D8	I398 C2
3515 D8	I399 A3
3517 C5	M00 C5
3518 C8	M01 C8
3520 F3	M02 D6
3521 G3	M05 D8
3522 F4	M07 E6
3523 G4	M08 E7
3524 F6	M09 E6
3525 G6	M10 E1
3526 F8	M11 A2
3527 G7	M12 A4
3528 F9	M13 B2
3529 G9	M14 B4
3530 F11	M15 D2
3531 F11	M16 D4
5501 A2	M17 D2
5502 B3	M18 D3
5503 B2	M19 B2
5504 C2	M20 B4
5505 D2	M21 C2
5506 D2	M22 C3
5510 A4	
5511 B5	
5512 B4	
5513 C4	
5514 D4	
5515 D4	
7505 A9	
7506 A6	
F050 C11	
F051 E11	
F052 C11	
F053 D11	
F054 C11	
F055 D11	
F056 C11	
F057 D11	
F058 C11	
F059 D11	
F060 B11	
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F062 B11	
F063 D11	
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F065 D11	
F066 B11	
F067 C11	
F068 B11	
F069 C11	
F151 B12	
F152 C12	
F153 C12	
F154 C12	
F155 D12	
F156 D12	
I350 F3	
I351 F3	
I352 F3	
I353 F3	
I354 G3	
I355 G3	
I356 G3	
I357 G3	
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I366 F6	

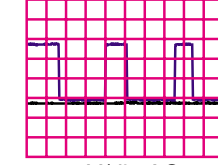


Schematic diagram(MCU)

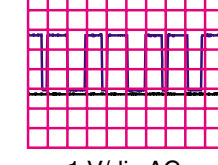


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1403 A11	7401 D7
1404 D12	7402 E10
1405 A5	7403 E10
1406 G3	F070 A11
2401 B3	F071 A11
2402 B3	F072 A11
2403 B3	F073 B11
2404 B3	F074 B11
2405 B4	F075 B11
2406 C4	F076 C11
2407 C5	F077 C11
2408 E5	F078 D11
2409 F5	F079 D11
2410 F6	F080 A12
2412 E11	F081 A12
2413 D10	F082 B12
2414 A5	F083 B12
2423 G4	F084 B12
2425 G4	F085 B12
2427 G4	F086 C12
2428 H3	F087 C12
2429 H4	F088 C12
2430 H3	F089 D12
2431 H4	F090 D11
2480 G3	F091 D11
3401 C4	F092 E11
3402 C4	F093 E11
3403 D5	F094 E11
3404 D4	F095 E11
3405 D4	F096 F11
3406 C3	F097 F11
3407 D4	F098 G12
3408 C3	F100 G11
3409 D4	F101 G11
3410 C3	F102 G11
3411 D4	F103 D12
3412 C3	F104 D12
3413 D4	F105 E12
3414 C4	F106 E12
3415 D4	F107 E12
3416 C4	F108 E12
3417 E4	F109 G12
3418 E5	F110 G3
3419 E6	F111 G3
3420 F5	F112 G3
3421 F6	F113 G3
3422 F5	F114 G3
3423 F6	F115 H3
3424 G6	F116 H3
3425 F7	F131 B12
3426 F7	F132 C12
3427 F5	F133 C11
3428 G8	F134 C12
3429 F8	F135 C11
3430 G8	F136 C12
3431 F8	F137 E12
3432 F9	F138 F11
3433 F9	F139 F11
3434 E9	I307 D9
3435 E10	I308 D9
3436 D9	I309 E5
3437 D9	I310 C5
3438 D10	I311 C5
3439 D9	I312 C5
3440 D10	I313 D5
3441 D9	I314 D5
3442 C9	I315 D5
3443 G3	I316 D5
3444 B8	I317 D5
3445 G3	I318 D5
3446 B7	I319 D5
3447 B9	I320 F6
3448 B9	I321 F6
3449 B9	I322 F6
3450 C9	I323 F6
3451 A7	I324 F7
3452 B6	I325 F7
3453 A7	I326 F7
3454 A6	I327 F4
3455 A6	I328 B3
3456 C9	I329 D3
3457 C9	I330 F10
3458 A6	I331 F9
3459 B7	I332 F9
3460 E4	
3461 E4	
3462 F4	
3463 G4	
3464 G5	
3465 G5	
3466 G4	
3467 G5	
3469 H4	
3470 H5	
3471 H4	
3472 H5	
3473 H3	
3474 F4	
3475 C9	
3476 D10	
3478 B5	

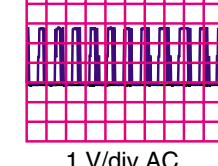
32 7401-8



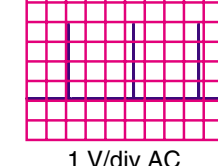
33 7401-9



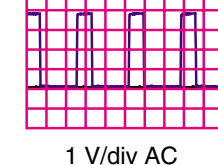
34 7401-13



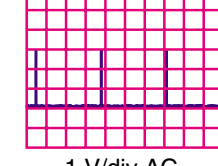
35 7401-19



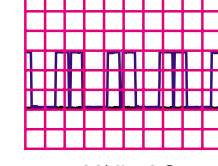
36 7401-20



37 7401-30

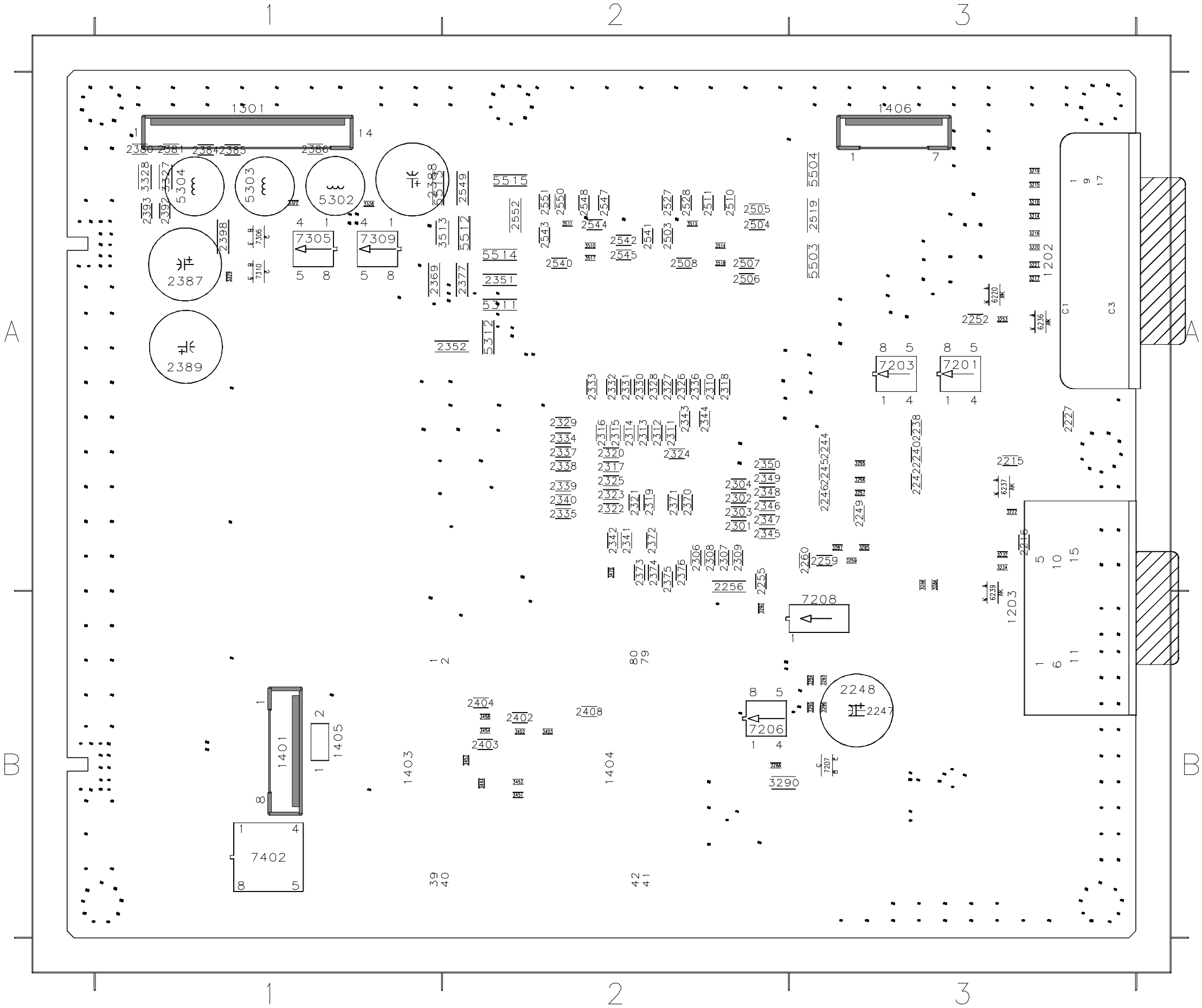


38 7401-31

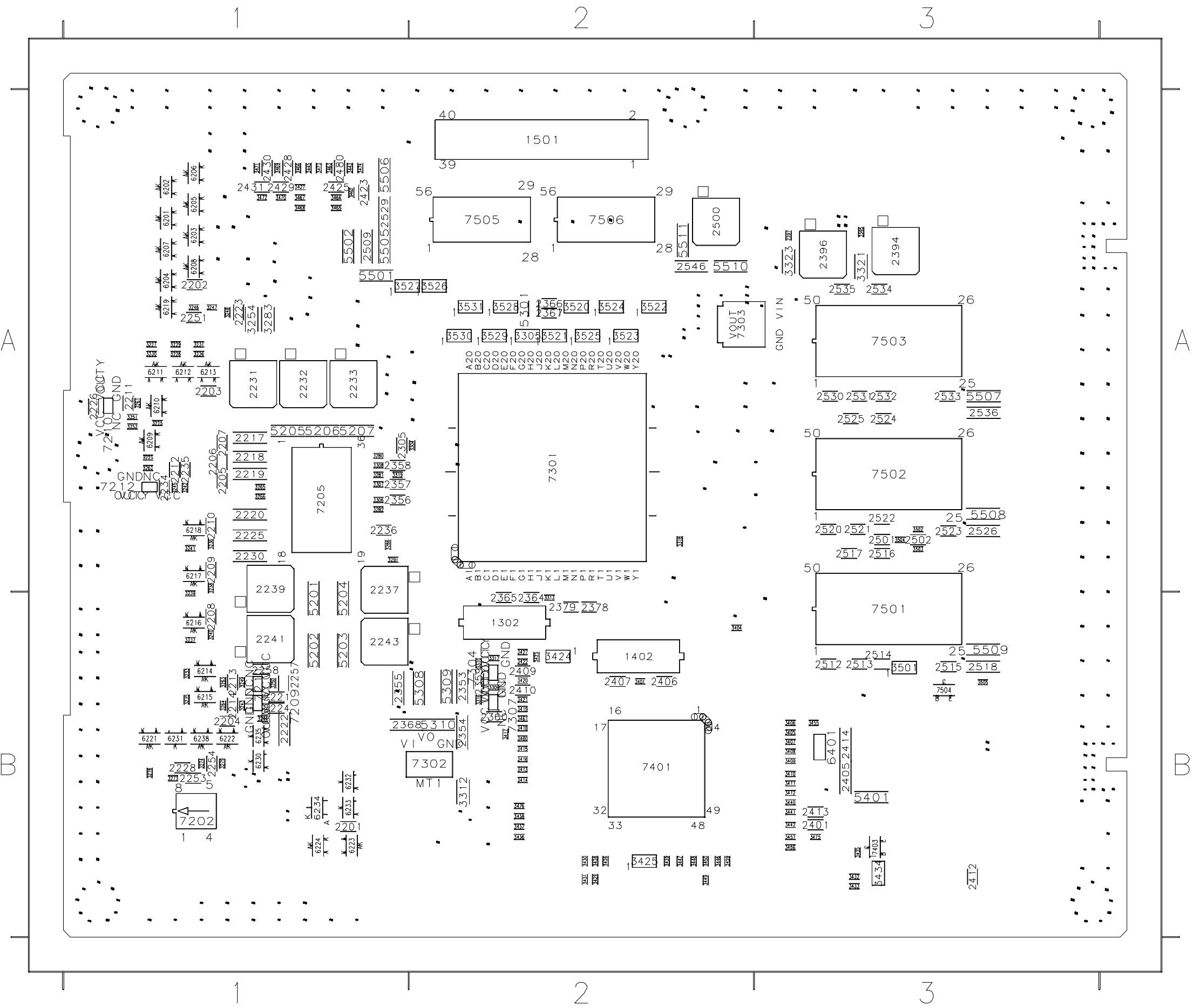


Schematic PWB(Copper track side view)

Go to cover page

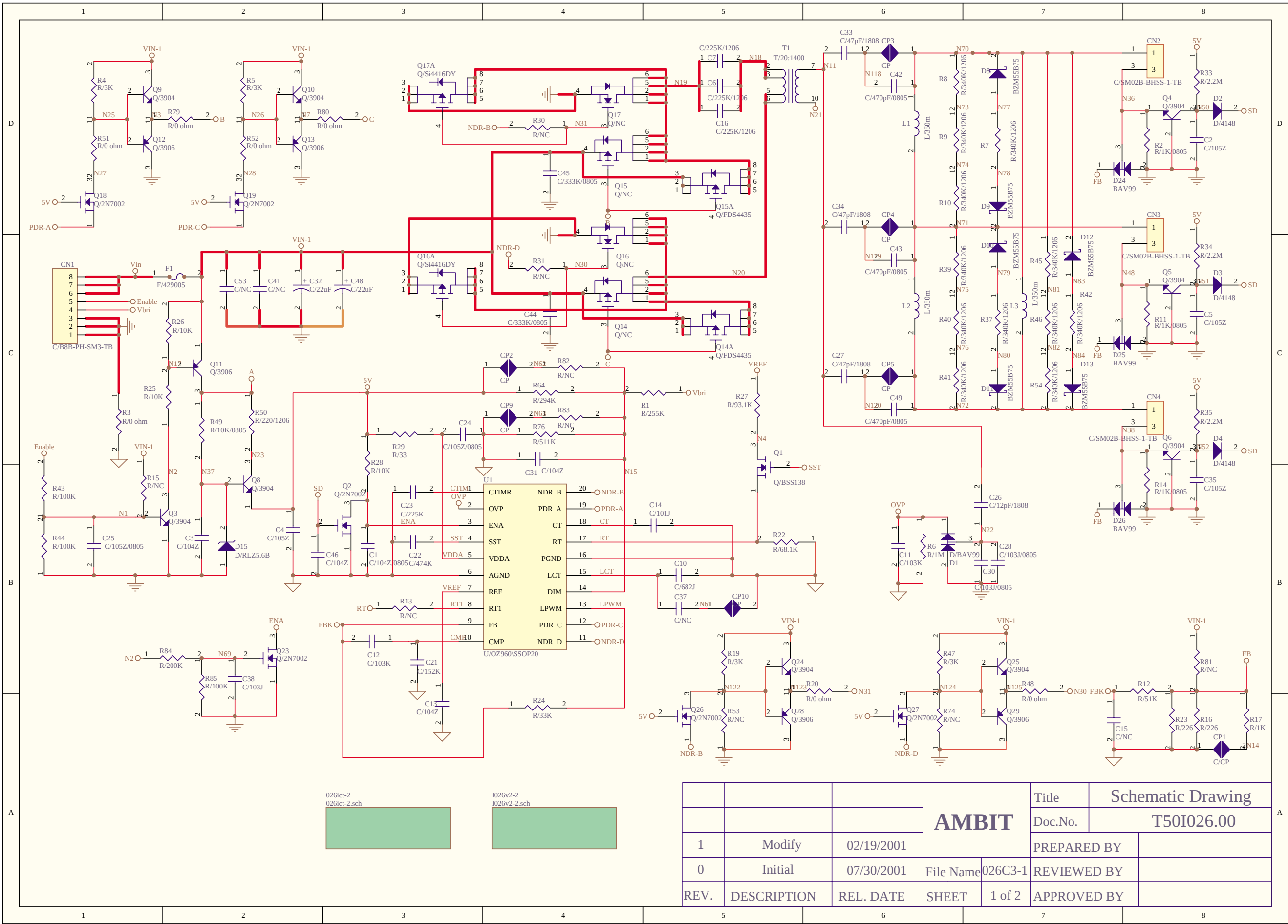


#	GRID	BOARD		#	GRID	BOARD		#	GRID	BOARD		#	GRID	BOARD	
#	REF	LABEL	SIDE	#	REF	LABEL	SIDE	#	REF	LABEL	SIDE	#	REF	LABEL	SIDE
1202	A3	B		2215	A3	A		2374	A2	A		3453	B2	A	
1203	B3	B		2216	A3	A		2375	A2	A		3454	B2	A	
1301	A1	B		2227	A3	A		2376	A2	A		3458	B2	A	
1401	B1	B		2238	A3	A		2377	A2	A		3478	A2	A	
1403	B1	B		2240	A3	A		2380	A1	A		3510	A2	A	
1404	B2	B		2242	A3	A		2381	A1	A		3511	A2	A	
1405	B1	B		2244	A3	A		2384	A1	A		3513	A2	A	
1406	A3	B		2245	A3	A		2385	A1	A		3514	A2	A	
2248	B3	B		2246	A3	A		2386	A1	A		3515	A2	A	
2387	A1	B		2247	B3	A		2392	A1	A		3517	A2	A	
2388	A1	B		2249	A3	A		2393	A1	A		3518	A2	A	
2389	A1	B		2252	A3	A		2398	A1	A		5311	A2	A	
5302	A1	B		2255	A2	A		2402	B2	A		5312	A2	A	
5303	A1	B		2256	A2	A		2403	B2	A		5503	A3	A	
5304	A1	B		2259	A3	A		2404	B2	A		5504	A3	A	
7402	B1	B		2260	A3	A		2408	B2	A		5512	A2	A	
				2301	A2	A		2503	A2	A		5513	A1	A	
				2302	A2	A		2504	A2	A		5514	A2	A	
				2303	A2	A		2505	A2	A		5515	A2	A	
				2304	A2	A		2506	A2	A		6220	A3	A	
				2306	A2	A		2507	A2	A		6236	A3	A	
				2307	A2	A		2508	A2	A		6237	A3	A	
				2308	A2	A		2510	A2	A		6239	B3	A	
				2309	A2	A		2511	A2	A		7201	A3	A	
				2310	A2	A		2519	A3	A		7203	A3	A	
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				2313	A2	A		2540	A2	A		7208	B3	A	
				2314	A2	A		2541	A2	A		7305	A1	A	
				2315	A2	A		2542	A2	A		7306	A1	A	
				2316	A2	A		2543	A2	A		7309	A1	A	
				2317	A2	A		2544	A2	A		7310	A1	A	
				2318	A2	A		2545	A2	A					
				2319	A2	A		2547	A2	A					
				2320	A2	A		2548	A2	A					
				2321	A2	A		2549	A2	A					
				2322	A2	A		2550	A2	A					
				2323	A2	A		2551	A2	A					
				2324	A2	A		2552	A2	A					
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				2326	A2	A		3215	A3	A					
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				2328	A2	A		3217	A3	A					
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				2352	A2	A		3322	A1	A					
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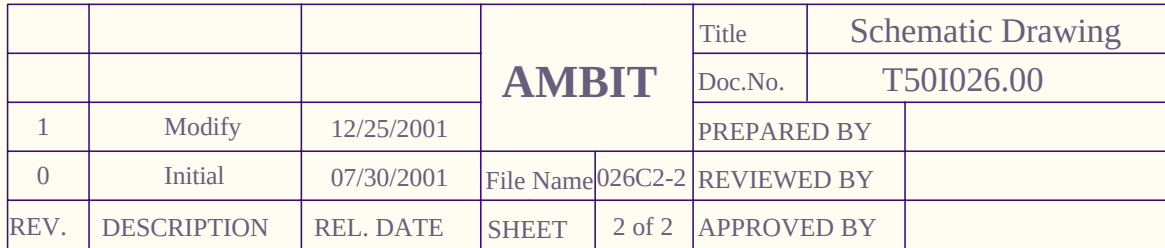
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1501	A2	B		2425	A1	B		3457	B3	B	
2201	B1	B		2427	A1	B		3459	B2	B	
2202	A1	B		2428	A1	B		3460	B2	B	
2203	A1	B		2429	A1	B		3461	B2	B	
2204	B1	B		2430	A1	B		3462	A1	B	
2205	A1	B		2431	A1	B		3463	A1	B	
2206	A1	B		2480	A1	B		3464	A1	B	
2207	A1	B		2500	A2	B		3465	A1	B	
2208	B1	B		2501	A3	B		3466	A1	B	
2209	A1	B		2502	A3	B		3467	A1	B	
2210	A1	B		2509	A1	B		3468	A1	B	
2211	A1	B		2512	B3	B		3469	A1	B	
2212	A1	B		2513	B3	B		3470	A1	B	
2213	B1	B		2514	B3	B		3471	A1	B	
2214	B1	B		2515	B3	B		3472	A1	B	
2217	A1	B		2516	A3	B		3473	A1	B	
2218	A1	B		2517	A3	B		3474	A1	B	
2219	A1	B		2518	B3	B		3475	B3	B	
2220	A1	B		2520	A3	B		3476	B2	B	
2221	B1	B		2521	A3	B		3501	B3	B	
2222	B1	B		2522	A3	B		3502	A3	B	
2223	A1	B		2523	A3	B		3503	A3	B	
2224	B1	B		2524	A3	B		3504	A3	B	
2225	A1	B		2525	A3	B		3505	B3	B	
2226	A1	B		2526	A3	B		3520	A2	B	
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2230	A1	B		2530	A3	B		3522	A2	B	
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2234	A1	B		2534	A3	B		3526	A2	B	
2235	A1	B		2535	A3	B		3527	A1	B	
2236	A1	B		2536	A3	B		3528	A2	B	
2237	A1	B		2546	A2	B		3529	A2	B	
2239	A1	B		3223	A1	B		3530	A2	B	
2241	B1	B		3225	A1	B		3531	A2	B	
2243	B1	B		3226	A1	B		5201	B1	B	
2253	B1	B		3227	A1	B		5202	B1	B	
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2257	B1	B		3229	A1	B		5204	B1	B	
2258	B1	B		3230	A1	B		5205	A1	B	
2305	A1	B		3231	A1	B		5206	A1	B	
2353	B2	B		3233	B1	B		5207	A1	B	
2354	B2	B		3235	B1	B		5301	A2	B	
2355	B1	B		3236	A1	B		5308	B2	B	
2356	A1	B		3237	B1	B		5309	B2	B	
2357	A1	B		3238	A1	B		5310	B2	B	
2358	A1	B		3239	B1	B		5401	B3	B	
2359	B2	B		3240	B1	B		5501	A1	B	
2360	B2	B		3241	A1	B		5502	A1	B	
2364	B2	B		3242	A1	B		5505	A1	B	
2365	B2	B		3243	B1	B		5506	A1	B	
2366	A2	B		3244	B1	B		5507	A3	B	
2367	A2	B		3245	A1	B		5508	A3	B	
2368	B1	B		3246	A1	B		5509	B3	B	
2378	B2	B		3247	A1	B		5510	A2	B	
2379	B2	B		3248	A1	B		5511	A2	B	
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2395	A3	B		3250	B1	B		6202	A1	B	
2396	A3	B		3251	A1	B		6203	A1	B	
2397	A3	B		3252	A1	B		6204	A1	B	
2401	B3	B		3254	A1	B		6205	A1	B	
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2412	B3	B		3266	A1	B		6211	A1	B	
2413	B3	B		3273	B1	B		6212	A1	B	
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Inverter Diagram(AMBIT)



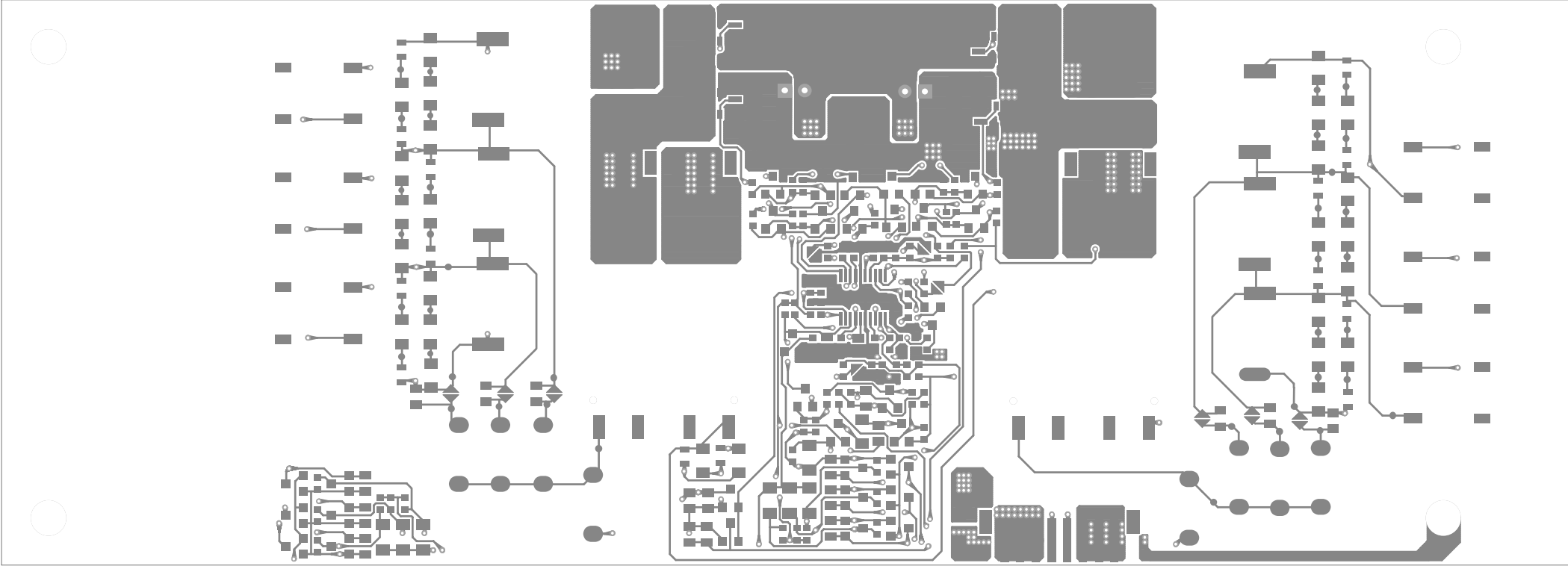
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1. 1	90.0007.01	CARTON 560*460*216 mm
2. 1	91.0229.03	TRAY-229b T50I026.00
3. 1	T50I026D00	INVERTER LCD 18" FOR POGOII LG@
4. 2	09.0142.01	CAP OS 20SP22M (SANYO)
5. 2	11.2041.14	CAP DISC CC45SL3FD470JYPN(TDK)@
6. 2	11.2029.14	CAP DISC CC45SL3FD120JYPN(TDK)@
7. 2	T50I026S00	INVERTER LCD 18" FOR POGOII LG@
8. 3	08.0000.04	CAP C 101J/50V/NPO/0603 @
9. 3	08.0003.03	CAP C 103J/50V/X7R/0603 @
10. 3	08.0003.01	CAP C 103J/50V/X7R/0805
11. 3	08.0100.01	CAP C 103K/50V/X7R/0603 @
12. 3	08.0022.02	CAP C 104Z/25V/Y5V/0603 @
13. 3	08.0032.01	CAP C 104Z/25V/Y5V/0805
14. 3	08.0183.01	CAP C 105Z/10V/Y5V/0603 @
15. 3	08.0020.04	CAP C 105Z/16V/Y5V/0805 @
16. 3	08.0355.01	CAP C 152K/50V/X7R/0603
17. 3	08.0557.14	CAP C 225K/25V/X7R/1206(TDK) @
18. 3	08.0027.01	CAP C 333K/50V/X7R/0805 @
19. 3	08.0674.21	CAP C 471K/250V/X7R/0805 (HEC)@
20. 3	08.0778.01	CAP C 105K/6.3V/X5R/0603 @
21. 3	326.0534.01	CONN B8B-PH-SM3-TB (JST)
22. 3	326.0246.01	CONN SM02B-BHSS-1 (JST)
23. 3	306.1007.01	DIO SW LS4148 LL-34(TELEFUNKEN
24. 3	306.1003.01	DIO SW BAV99 SOT23 A7,A7P,JE @
25. 3	306.1003.02	DIO SW DAN217 SOT23 (ROHM)
26. 3	306.1752.29	DIO ZEN BZM55B75-TR(TELEFUNKEN@
27. 3	306.1528.02	DIO ZEN RLZ5.6B/TE-11 (ROHM)
28. 3	326.7021.01	FUSE 429005 5A (LITTEL) @
29. 3	316.0490.01	IND SPC-1005P-354 SMD (TMP)
30. 3	306.0602.01	NFET 2N7002(E)(LT1) SOT23 @
31. 3	306.0038.01	TR NPN MMBT3904LT1 SOT23(MOTO)
32. 3	306.0015.01	TR NPN PMBS3904 SOT23(PHILIPS)
33. 3	306.0631.01	NFET BSS138 SOT23 (ZETEX) @
34. 3	306.0039.01	TR PNP MMBT3906LT1 SOT23(MOTO)
35. 3	306.0016.01	TR PNP PMBS3906 SOT23(PHILIPS)
36. 3	306.6003.06	NFET Si4416DY SO8 (SILICONIX) @
37. 3	306.2015.17	PFET FDS4435 SO8 (FAIRCHILD)
38. 3	306.2131.17	PFET Si4435DY SO8 FAIRCHILD @
39. 3	306.2015.01	PFET Si4435DYA SO8 (SILICONIX)@
40. 3	313.0005.03	RES C 0 ohm/0603/5%
41. 3	313.0005.02	RES C 0 ohm /1206/5%
42. 3	314.0011.03	RES C 100K ohm/0603/1%
43. 3	313.0032.02	RES C 10K ohm/0603/5%
44. 3	313.0032.01	RES C 10K ohm/0805/5%
45. 3	313.0271.01	RES C 2M ohm/1206/5%
46. 3	314.0365.01	RES C 1K ohm/0805/1%
47. 3	314.0012.03	RES C 1M ohm/0603/1%
48. 3	314.0125.02	RES C 93.1K ohm/0603/1%
49. 3	313.0088.02	RES C 2.2M ohm/0603/5%
50. 3	314.1022.01	RES C 226 ohm/0603/1%
51. 3	313.0092.01	RES C 200K ohm/0603/5%
52. 3	313.0299.01	RES C 220 ohm/1206/5%
53. 3	314.0751.01	RES C 255K ohm/0603/1%
54. 3	314.0223.01	RES C 3K ohm/0603/1%
55. 3	314.0403.01	RES C 33 ohm/0603/1%
56. 3	314.1105.01	RES C 33K ohm/0603/1% @
57. 3	314.0557.01	RES C 340K ohm/1206/1%
58. 3	314.0824.01	RES C 511K ohm/0603/1%
59. 3	314.0852.01	RES C 51K ohm/0603/1%
60. 3	314.1024.01	RES C 294K ohm/0603/1% @
61. 3	314.0283.01	RES C 68.1K ohm/0603/1%
62. 3	304.3040.02	IC OZ960DS-1.0 +-4% SSOP20(O2)
63. 3	308.0506.01	CAP C 682J/50V/X7R/0603 @
64. 3	313.0199.01	RES C 2K ohm/0603/5%
65. 3	308.0365.01	CAP C 474K/10V/X7R/0603 @
66. 3	322.0528.39	XFMR*XT-1955-12508 A(XTREME)
67. 3	364.0004.02	SOLDER PASTE SE48-M954-2(KOKI)
68. 3	319.0949.01	PCB T50I026.00 REV:2 2L
69. 3	392.0045.01	LABEL 20*10mm U13.010.C

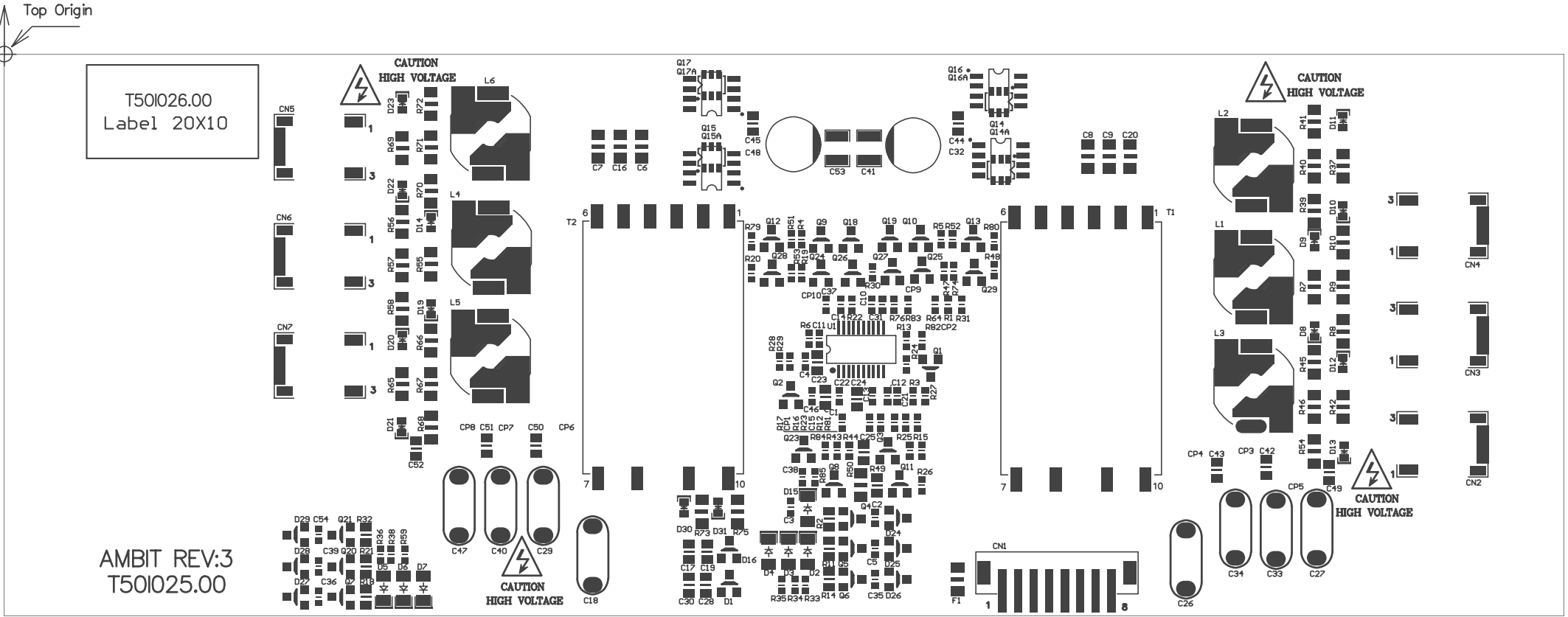


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IC	IC OZ960DS1.0 +-4% SSOP20	O2	OZ960DS	U1	1
Transistor	2N7002(E) 60V 115mA 7.5OHM	MOTO	2N7002(E)	Q18 Q19 Q2 Q23 Q26Q27	6
	MMBT3904LT1 SOT23 40V 200mA	MOTO	MMBT3904	Q10 Q20 Q21 Q24 Q25 Q3 Q4 Q5	12
	PMBS3904 SOT23 40V 200mA	PHILIPS	PMBS3904	Q6 Q7 Q8 Q9	
	MMBT3906LT1 SOT23 40V 200mA	MOTO	MMBT3906	Q11 Q12 Q13 Q28 Q29	5
	PMBS3906 SOT23 40V 200mA	PHILIPS	PMBS3906		
	BSS138 SOT23 50V 200mA 3.5ohm	ZETEX	BSS138	Q1	1
	Si4416DY SO8 30V/9A/0.018ohm	SILICONIX	Si4416DY	Q16A Q17A	2
	4435 SO8 30V	FAIRCHILD	4435	Q14A Q15A	2
		SILICONIX			
Fuse	FUSE 429005 5A 5A/24V/1206/FAST	LITTLE	429005	F1	1
Inductor	SPC-1005P-354 350mHK/1.4 Kohm	TMP	SPC-1005P-354	L1 L2 L3 L4 L5 L6	6
Capacitor	CAP OS 20SP22M 22uF/20V/6.3*5.0/105C	SANYO		C32 C48	2
	CAP CC45SL3FD470JYPN 47PF/3KV	TDK		C27 C29 C33 C34 C40 C47	6
	CAP CC45SL3FD120JYPN 12PF/3KV	TDK		C18 C26	2
	CAP C 101J/50V/NPO/0603	Murata		C14	1
		Phycomp			
		TDK			
		AVX			
		TaiYuden			
	CAP C 103J/50V/X7R/0603	Murata		C38	1
		Phycomp			
		TDK			
		AVX			
		TaiYuden			
	CAP C 103J/50V/X7R/0805	Murata		C17 C19 C28 C30	4
		Phycomp			
TDK					
AVX					
TaiYuden					
CAP C 103K/50V/X7R/0603	Murata		C11 C12	2	
	Phycomp				
	TDK				
	AVX				
	TaiYuden				

Inverter Diagram PWB(AMBIT)



AMBIT	FILM NUMBER	T50I025-00-2C1 -02	
	SCALE 10MM	Mechanical Layer 1	
	P/N	19.XXXX.XX	
	Rev.	2	20-Dec-2001



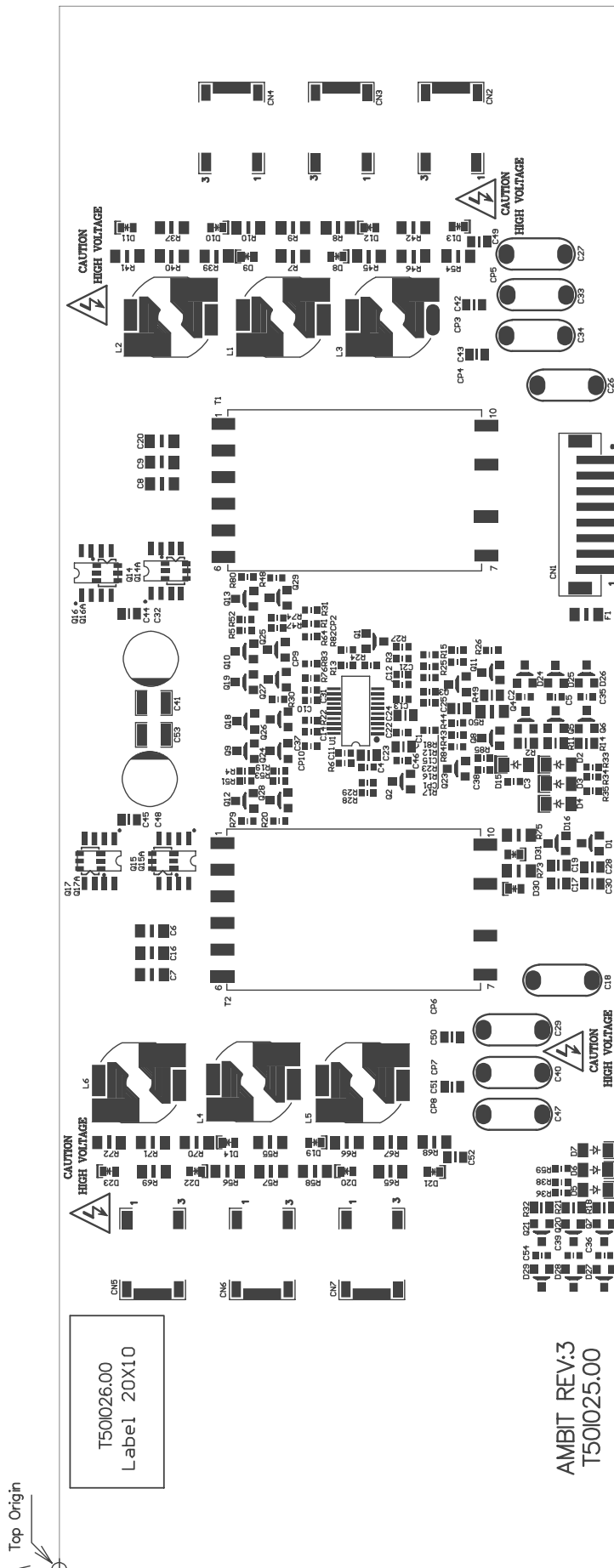
AMBIT	FILM NUMBER	T50I025-00-2SF -03	
	SCALE 10MM	Mechanical Layer 1	
	P/N	19.0949.02	
	Rev.	3	19-Feb-2002

Inverter Diagram PWB(AMBIT)

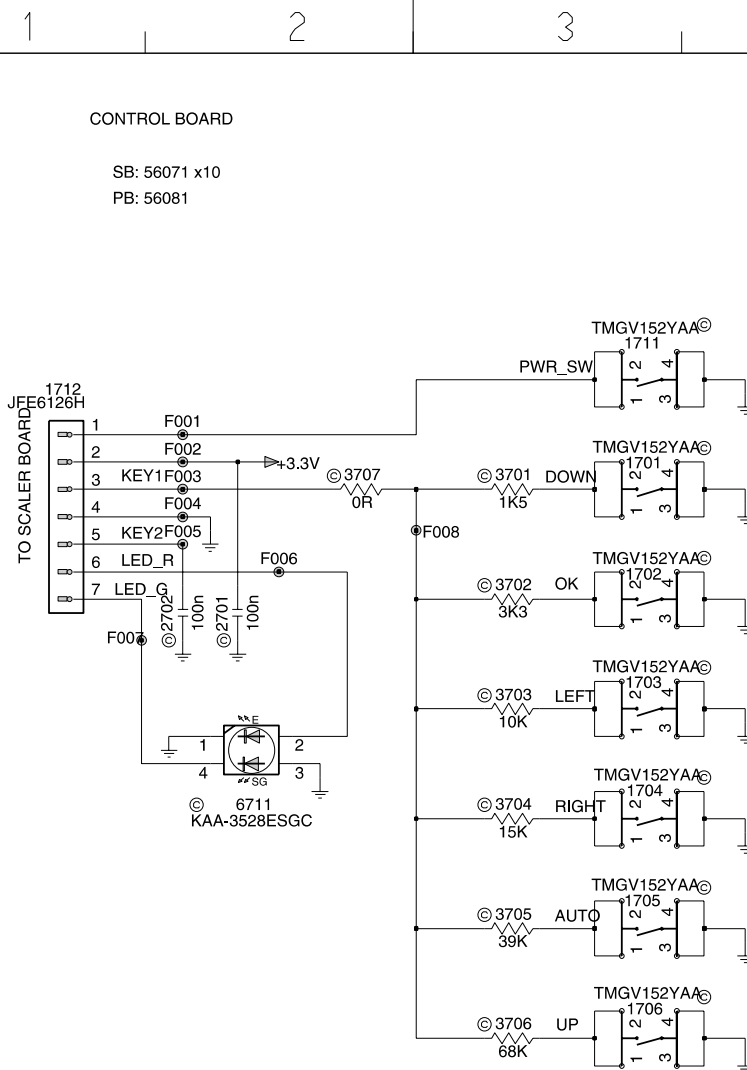
Max 101830

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	SCALE 10MM 		
	Mechanical Layer 1		
	P/N	19.0949.02	
	Rev.	3	19-Feb-2002

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NOTES:

© STAND FOR CHIP COMPONENTS.

CN: TYT12-

CLASS NO.

CONTROL BOARD

3138 158 54081

01-11-30

3

DA6B2L/00C

NAME YS Tseng

SUPERS

1

10

130

- 1

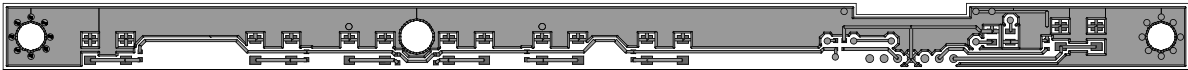
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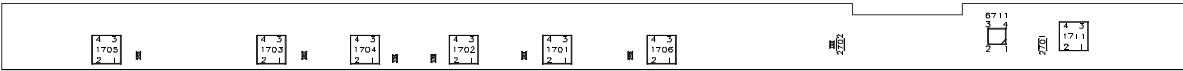
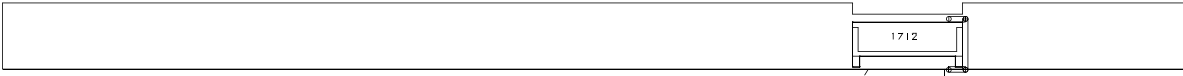
DATE 01-11-30

©

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1702 C
1703 D
1704 D
1705 E
1706 E
1711 B
1712 B
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3701 C
3702 C
3703 D
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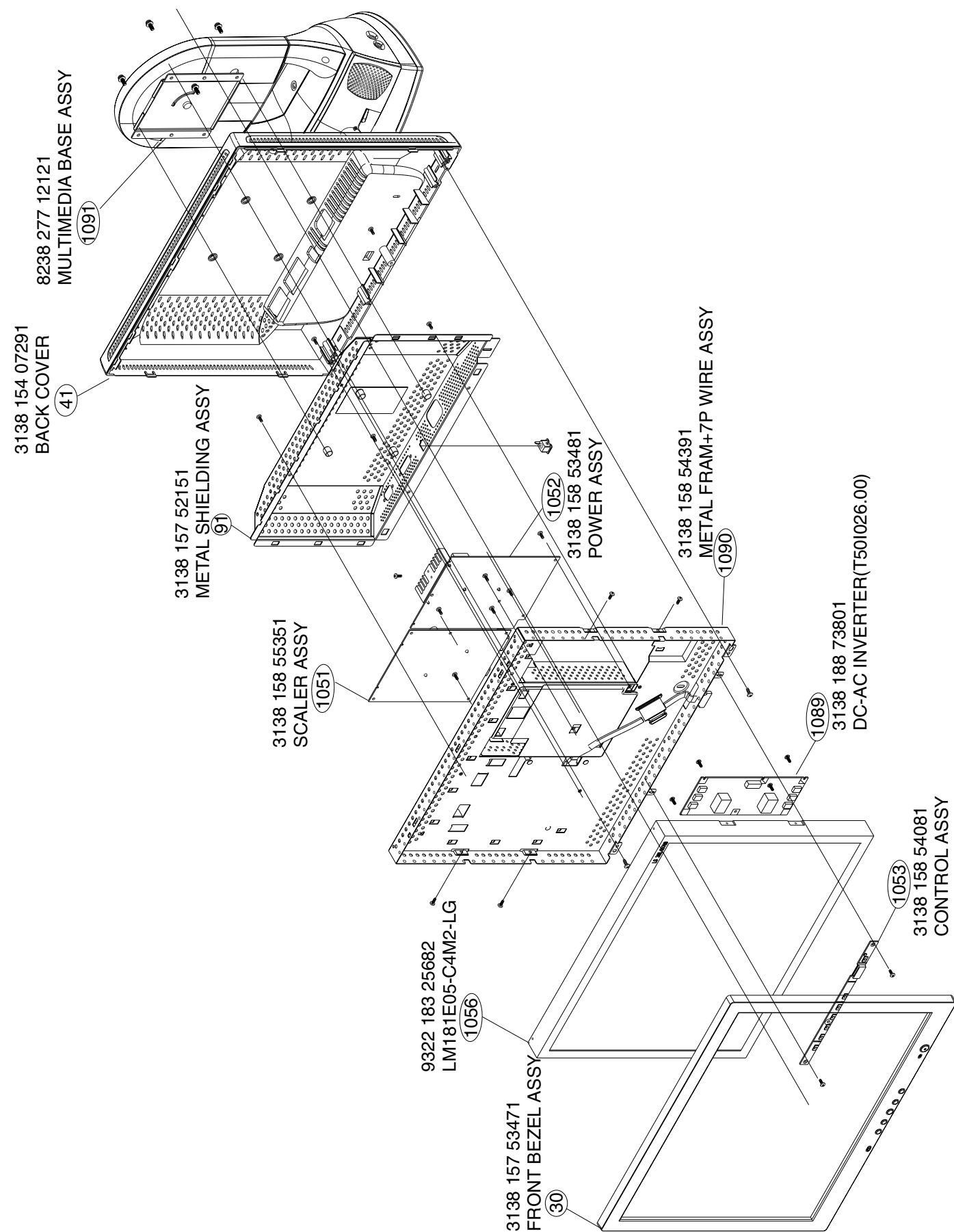


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		1703	A2 B
		1704	A2 B
		1705	A1 B
		1706	A3 B
		1711	A5 B
		2701	A5 B
		2702	A4 B
		3701	A3 B
		3702	A2 B
		3703	A2 B
		3704	A2 B
		3705	A1 B
		3706	A3 B
		3707	A4 B
		6711	A4 B



Exploded View

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Model: DJ9B3L/00Z

Item	Code number	Description
30	313815753471	FRONT BEZEL ASSY
31	313815407231	BEZEL
32	313815407241	POWER KNOB
33	313815407251	CONTROL KNOB
34	313815405181	LENS
41	313815407291	BACK COVER
1090	313815854391	METAL FRAME+7P WIRE ASSY
1091	823827712121	MULTIMEDIA BASE ASSY(RAY 7035)
125	313815521291	D.F.U.
450	313815633611	CARTON
451	313815633621	CUSHION-R
452	313815633631	CUSHION-L
1087	313817878741	MAIN CORD ASSY
1088	313818872051	CORD SUB-D 15/2M0/SUB-D 15GRAY
1089	313818873801	DC-AC INVERTER (T50I026.)
1102 ▲	242208610239	FUSE 5X20 HT 3A15 250V IEC B
1402 ▲	243854300085	RES XTL SM 24M 0 7P SMD49 R
7061	932217683682	IC L4973V5.1 (ST) L
7071	932216562682	IC L4978 (ST) L
7101	932213028682	IC L5991 (ST) L
7102	932217477687	FET POW STU10NB80 (ST) L
7104	932214014667	OPT CP TCET1103(G) (VISH) L
7106	933771100686	IC TL431CLP S (MOTA) R
7202	932214526668	IC SM M24C02-WMN6 (ST) R
7205	932217546668	IC M61323FP (MITJ) R
7206	932202899668	FET POW SM SI4835DY (VISH) R
7208	935241100118	IC SM 74LVC126AD (PHSE) R
7212	935245720115	IC SM 74HCT1G14GW (PHSE) R
7301	932216881671	IC SM GM5020 (GEMI) Y
7303	932217704668	IC SM LD1086DT25 (ST) R
7401	932216723671	IC SM MC68HC908LD60 (MOTA) Y
7402	932212662682	IC M24C16-BN6 (ST) L
7503	932216677682	IC SM M12L16161A-7T (ESMT) L
7505	932217686668	IC SM THC63LVDM83A (THIN) R

Recommended parts list of Inverter board :(T501026.00, Ambit)

Pos.	Service code	Description	supplier code
1075	8238 277 11561	DC-AC INVERTER (T501026.00, Ambit)	
L1,L2	9965 000 12391	IND SPC-10055P-354-SMD(TMP)	16.0490.01
F1	9965 000 12955	FUSE 4290005 5A(LITTLE)	26.7021.01
Q18	4822 130 11057	2N7002	06.0602.01
Q3	9965 000 05478	TR NPN PMBS3904 SOT23(PHILIPS)	06.0015.01
Q1	9965 000 12956	NFET BSS138 SOT23(ZETEX)	06.0631.01
Q15A	9965 000 12959	PFET SI4435DY SO8(FAIRCHILD)	06.2131.17
Q11	9965 000 12957	TR PNP PMBS3906 SOT23(PHILIPS)	06.0016.01
Q17A	9965 000 12958	NFET SI4416DY SO8(LILICONIX)	06.6003.01
U1	9965 000 12960	IC OZ960DS-0.1+/-4% SSOP20(02)	04.3040.02
T1	9965 000 12961	XFMR XT-1955-12508 A(XTREME)	22.0528.39

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Parts indicated on exploded view : CTVs : DJ9B3L/00Z			2246	223858615636	CER2 0603 X7R 50V 10N PM10 R	2369	222224119876	CER2 1206 Y5V 10V 10U P8020 R
30	313815753471	FRONT BEZEL ASSY	2247	223858615636	CER2 0603 X7R 50V 10N PM10 R	2370	223878615649	CER2 0603 X7R 16V 1 N PM10 R
31	313815407231	BEZEL	2248	203803516304	ELCAP RXJ 25V S 470U PM20 B	2371	223878615649	CER2 0603 X7R 16V 1 N PM10 R
32	313815407241	POWER KNOB	2249	223824619863	CER2 0603 Y5V 10V 1U P8020 R	2372	223878615649	CER2 0603 X7R 16V 1 N PM10 R
33	313815407251	CONTROL KNOB	2251	223886715159	CER1 0603 NP0 50V 15P PM5 R	2373	223878615649	CER2 0603 X7R 16V 1 N PM10 R
34	313815405181	LENS	2252	223886715159	CER1 0603 NP0 50V 15P PM5 R	2374	223878615649	CER2 0603 X7R 16V 1 N PM10 R
41	313815407291	BACK COVER	2253	223886715159	CER1 0603 NP0 50V 15P PM5 R	2375	223878615649	CER2 0603 X7R 16V 1 N PM10 R
91	313815752151	METAL SHIELDING ASSY	2254	223886715159	CER1 0603 NP0 50V 15P PM5 R	2376	223878615649	CER2 0603 X7R 16V 1 N PM10 R
98	313810440571	HOUSING COVER	2255	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2377	222224119876	CER2 1206 Y5V 10V 10U P8020 R
99	313815551621	PLASTIC COVER-SNI	2256	222224119876	CER2 1206 Y5V 10V 10U P8020 R	2379	223886715331	CER1 0603 NP0 50V 330P PM5 R
1088	313818872051	CORD SUB-D 15/2M0/SUB-D 15GRAY	2258	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2384	223878615649	CER2 0603 X7R 16V 1 N PM10 R
1085	932218325682	TFT-LCD LM181E05-C4M2 (LGPH) B	2259	223886715339	CER1 0603 NP0 50V 33P PM5 R	2385	223878615649	CER2 0603 X7R 16V 1 N PM10 R
1086	313818873181	CBLE 334034 40/220/30 321163	2260	223886715339	CER1 0603 NP0 50V 33P PM5 R	2386	223878615649	CER2 0603 X7R 16V 1 N PM10 R
1089	313818873801	DC-AC INVERTER (T50I026.)	2301	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2387	203803516304	ELCAP RXJ 25V S 470U PM20 B
1090	313815854391	METAL FRAME+7P WIRE ASSY	2302	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2388	203803516304	ELCAP RXJ 25V S 470U PM20 B
1091	823827712121	MULTIMEDIA BASE ASSY(RAY 7035)	2303	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2389	203803516304	ELCAP RXJ 25V S 470U PM20 B
1093	313818872241	DSUB CALE	2304	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2396	202001293747	ELCAP SM RV2 25V 47U PM20 R
Various			2305	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2398	222224119876	CER2 1206 Y5V 10V 10U P8020 R
450	313815633611	CARTON	2306	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2401	223878615649	CER2 0603 X7R 16V 1 N PM10 R
451	313815633621	CUSHION-R	2307	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2402	223878615649	CER2 0603 X7R 16V 1 N PM10 R
452	313815633631	CUSHION-L	2308	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2403	223878615649	CER2 0603 X7R 16V 1 N PM10 R
453	313815633641	P.E. BAG-ANTI-STAT.	2309	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2404	223878615649	CER2 0603 X7R 16V 1 N PM10 R
Accessories			2310	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2405	222224119876	CER2 1206 Y5V 10V 10U P8020 R
125	313815521291	D.F.U.	2311	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2406	223886715159	CER1 0603 NP0 50V 15P PM5 R
145	313810656141	E-D.U.F. BAG	2312	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2407	223886715159	CER1 0603 NP0 50V 15P PM5 R
1087 ▲	313817878741	MAIN CORD ASSY	2313	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2408	223878615649	CER2 0603 X7R 16V 1 N PM10 R
Main Panel			2314	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2412	223878615649	CER2 0603 X7R 16V 1 N PM10 R
1051	313815855351	SCALER PCB ASSY	2315	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2413	223878615649	CER2 0603 X7R 16V 1 N PM10 R
1052	313815853481	POWER ASSY	2316	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2414	223858115649	CER2 1206 X7R 50V 1 N PM10 R
1053	313815854081	CONTROL ASSY	2317	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2500	202001293747	ELCAP SM RV2 25V 47U PM20 R
Scaler Panel			2318	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2503	223878615649	CER2 0603 X7R 16V 1 N PM10 R
1051	313815855351	SCALER PCB ASSY	2319	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2504	223878615649	CER2 0603 X7R 16V 1 N PM10 R
→→			2320	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2505	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2201	223824615654	CER2 0603 X7R 10V 220N PM10 R	2321	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2506	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2202	223824615654	CER2 0603 X7R 10V 220N PM10 R	2322	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2507	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2203	223824615654	CER2 0603 X7R 10V 220N PM10 R	2323	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2508	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2204	223824615654	CER2 0603 X7R 10V 220N PM10 R	2324	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2509	222224119876	CER2 1206 Y5V 10V 10U P8020 R
2211	223886715331	CER1 0603 NP0 50V 330P PM5 R	2325	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2510	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2212	223886715159	CER1 0603 NP0 50V 15P PM5 R	2326	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2511	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2213	223886715331	CER1 0603 NP0 50V 330P PM5 R	2327	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2512	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2214	223886715159	CER1 0603 NP0 50V 15P PM5 R	2328	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2513	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2215	223886715479	CER1 0603 NP0 50V 47P PM5 R	2329	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2514	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2216	223886715479	CER1 0603 NP0 50V 47P PM5 R	2330	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2515	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2217	222224119876	CER2 1206 Y5V 10V 10U P8020 R	2331	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2516	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2218	222224119876	CER2 1206 Y5V 10V 10U P8020 R	2332	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2517	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2219	222224119876	CER2 1206 Y5V 10V 10U P8020 R	2333	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2518	222224119876	CER2 1206 Y5V 10V 10U P8020 R
2220	222224119876	CER2 1206 Y5V 10V 10U P8020 R	2334	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2519	222224119876	CER2 1206 Y5V 10V 10U P8020 R
2221	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2335	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2520	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2222	222224119876	CER2 1206 Y5V 10V 10U P8020 R	2336	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2521	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2223	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2337	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2522	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2225	222224119876	CER2 1206 Y5V 10V 10U P8020 R	2338	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2523	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2226	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2339	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2524	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2228	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2340	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2525	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2230	222224119876	CER2 1206 Y5V 10V 10U P8020 R	2341	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2526	222224119876	CER2 1206 Y5V 10V 10U P8020 R
2231	202001293747	ELCAP SM RV2 25V 47U PM20 R	2342	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2527	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2232	202001293747	ELCAP SM RV2 25V 47U PM20 R	2343	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2528	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2233	202001293747	ELCAP SM RV2 25V 47U PM20 R	2344	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2529	222224119876	CER2 1206 Y5V 10V 10U P8020 R
2234	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2345	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2530	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2236	223858615636	CER2 0603 X7R 50V 10N PM10 R	2346	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2531	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2237	202001293747	ELCAP SM RV2 25V 47U PM20 R	2347	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2532	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2238	223858615636	CER2 0603 X7R 50V 10N PM10 R	2348	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2533	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2239	202001293747	ELCAP SM RV2 25V 47U PM20 R	2349	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2534	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2240	223858615636	CER2 0603 X7R 50V 10N PM10 R	2350	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2535	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2241	202001293747	ELCAP SM RV2 25V 47U PM20 R	2351	222224119876	CER2 1206 Y5V 10V 10U P8020 R	2536	222224119876	CER2 1206 Y5V 10V 10U P8020 R
2242	223858615636	CER2 0603 X7R 50V 10N PM10 R	2352	222224119876	CER2 1206 Y5V 10V 10U P8020 R	2540	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2243	202001293747	ELCAP SM RV2 25V 47U PM20 R	2353	222224119876	CER2 1206 Y5V 10V 10U P8020 R	2541	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2244	223858615636	CER2 0603 X7R 50V 10N PM10 R	2354	222224119876	CER2 1206 Y5V 10V 10U P8020 R	2542	223878615649	CER2 0603 X7R 16V 1 N PM10 R
2245	223858615636	CER2 0603 X7R 50V 10N PM10 R	2355	222224119876	CER2 1206 Y5V 10V 10U P8020 R	2543	223878615649	CER2 0603 X7R 16V 1 N PM10 R
			2356	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2544	223878615649	CER2 0603 X7R 16V 1 N PM10 R
			2357	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2545	223878615649	CER2 0603 X7R 16V 1 N PM10 R
			2358	223878615649	CER2 0603 X7R 16V 1 N PM10 R	2546	222224119876	CER2 1206 Y5V 10V 10U P8020 R
			2364	223886715568	CER1 0603 NP0 50V 5P6 PM0P5 R	2547	223878615649	CER2 0603 X7R 16V 1 N PM10 R
			2365	223886715568	CER1 0603 NP0 50V 5P6 PM0P5 R	2548	223878615649	CER2 0603 X7R 16V 1 N PM10 R
			2368	222224119876	CER2 1206 Y5V 10V 10U P8020 R	2549	222224119876	CER2 1206 Y5V 10V 10U P8020 R

Spare Parts List(Continued)

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2550	223878615649	CER2 0603 X7R 16V 1 N PM10 R	3403	319802131030	RST SM 0603 10K PM5 COL	5206	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
2551	223878615649	CER2 0603 X7R 16V 1 N PM10 R	3404	319802190030	RST SM 0603 JUMP. 0R05 COL	5207	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
2552	222224119876	CER2 1206 Y5V 10V 10U P8020 R	3405	319802190030	RST SM 0603 JUMP. 0R05 COL	5301	242254944195	IND FXD 0805 EMI 1 MHZ 220R R	
			3407	319802190030	RST SM 0603 JUMP. 0R05 COL	5302	242253600035	IND FXD TSL0808 S 10U PM10 A	
			3409	319802190030	RST SM 0603 JUMP. 0R05 COL	5303	242253600035	IND FXD TSL0808 S 10U PM10 A	
	3214	319802190030	RST SM 0603 JUMP. 0R05 COL	3411	319802190030	RST SM 0603 JUMP. 0R05 COL	5304	242253600035	IND FXD TSL0808 S 10U PM10 A
	3215	319802190030	RST SM 0603 JUMP. 0R05 COL	3413	319802190030	RST SM 0603 JUMP. 0R05 COL	5308	242254943409	IND FXD 1206 EMI 1 MHZ 50R R
	3216	319802190030	RST SM 0603 JUMP. 0R05 COL	3415	319802190030	RST SM 0603 JUMP. 0R05 COL	5309	242254943409	IND FXD 1206 EMI 1 MHZ 50R R
	3217	319802190030	RST SM 0603 JUMP. 0R05 COL	3417	319802131030	RST SM 0603 10K PM5 COL	5310	242254943409	IND FXD 1206 EMI 1 MHZ 50R R
	3218	319802190030	RST SM 0603 JUMP. 0R05 COL	3419	319802131030	RST SM 0603 10K PM5 COL	5311	242254943409	IND FXD 1206 EMI 1 MHZ 50R R
	3219	319802190030	RST SM 0603 JUMP. 0R05 COL						
	3220	319802190030	RST SM 0603 JUMP. 0R05 COL	3420	319802190030	RST SM 0603 JUMP. 0R05 COL	5312	242254943409	IND FXD 1206 EMI 1 MHZ 50R R
	3221	319802190030	RST SM 0603 JUMP. 0R05 COL	3424	235003510103	RST NETW SM ARV24 4X 10K PM5 R	5401	242254943409	IND FXD 1206 EMI 1 MHZ 50R R
3222	319802190030	RST SM 0603 JUMP. 0R05 COL	3425	235003510103	RST NETW SM ARV24 4X 10K PM5 R	5501	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
3223	319802132220	RST SM 0603 2K2 PM5 COL	3426	319802131030	RST SM 0603 10K PM5 COL	5502	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
			3427	319802190030	RST SM 0603 JUMP. 0R05 COL	5503	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
3225	319802132220	RST SM 0603 2K2 PM5 COL	3428	319802131030	RST SM 0603 10K PM5 COL	5504	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
3226	319802190030	RST SM 0603 JUMP. 0R05 COL	3430	319802131030	RST SM 0603 10K PM5 COL	5505	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
3227	319802137590	RST SM 0603 75R PM5 COL	3432	319802131030	RST SM 0603 10K PM5 COL	5506	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
3228	319802190030	RST SM 0603 JUMP. 0R05 COL	3433	319802131030	RST SM 0603 10K PM5 COL	5507	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
3229	319802137590	RST SM 0603 75R PM5 COL	3434	235003510101	RST NETW SM ARV24 4X1 R PM5 R	5508	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
3230	319802190030	RST SM 0603 JUMP. 0R05 COL							
3231	319802137590	RST SM 0603 75R PM5 COL	3435	319802131030	RST SM 0603 10K PM5 COL	5509	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
3232	319802190030	RST SM 0603 JUMP. 0R05 COL	3441	319802131030	RST SM 0603 10K PM5 COL	5510	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
3233	319802132220	RST SM 0603 2K2 PM5 COL	3442	319802131030	RST SM 0603 10K PM5 COL	5511	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
3235	319802132220	RST SM 0603 2K2 PM5 COL	3443	319802190030	RST SM 0603 JUMP. 0R05 COL	5512	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
			3444	319802131030	RST SM 0603 10K PM5 COL	5513	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
3236	319802190030	RST SM 0603 JUMP. 0R05 COL	3445	319802190030	RST SM 0603 JUMP. 0R05 COL	5514	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
3237	319802137590	RST SM 0603 75R PM5 COL	3448	232270260184	RST SM 0603 RC21 180K PM5 R	5515	242254943409	IND FXD 1206 EMI 1 MHZ 50R R	
3238	319802190030	RST SM 0603 JUMP. 0R05 COL	3450	232270260184	RST SM 0603 RC21 180K PM5 R				
3239	319802137590	RST SM 0603 75R PM5 COL	3451	319802131040	RST SM 0603 1 K PM5 COL		6201	319801010620	DIO SIG SM BAV99 (COL) R
3240	319802190030	RST SM 0603 JUMP. 0R05 COL	3452	319802131040	RST SM 0603 1 K PM5 COL		6202	319801010620	DIO SIG SM BAV99 (COL) R
3241	319802137590	RST SM 0603 75R PM5 COL					6203	319801010620	DIO SIG SM BAV99 (COL) R
3242	319802131010	RST SM 0603 1 R PM5 COL	3453	319802131030	RST SM 0603 10K PM5 COL		6204	319801010620	DIO SIG SM BAV99 (COL) R
3244	319802131010	RST SM 0603 1 R PM5 COL	3455	319802131030	RST SM 0603 10K PM5 COL		6205	319801010620	DIO SIG SM BAV99 (COL) R
3246	319802131010	RST SM 0603 1 R PM5 COL	3456	319802131030	RST SM 0603 10K PM5 COL		6206	319801010620	DIO SIG SM BAV99 (COL) R
3247	319802131530	RST SM 0603 15K PM5 COL	3457	319802131030	RST SM 0603 10K PM5 COL		6207	319801010620	DIO SIG SM BAV99 (COL) R
			3458	319802131030	RST SM 0603 10K PM5 COL		6208	319801010620	DIO SIG SM BAV99 (COL) R
3248	319802131530	RST SM 0603 15K PM5 COL	3459	319802131030	RST SM 0603 10K PM5 COL		6209	319801010620	DIO SIG SM BAV99 (COL) R
3250	319802131010	RST SM 0603 1 R PM5 COL	3461	319802131030	RST SM 0603 10K PM5 COL		6210	319801010620	DIO SIG SM BAV99 (COL) R
3252	319802131010	RST SM 0603 1 R PM5 COL	3463	319802190030	RST SM 0603 JUMP. 0R05 COL				
3253	319802131010	RST SM 0603 1 R PM5 COL	3464	319802131530	RST SM 0603 15K PM5 COL		6211	319801010620	DIO SIG SM BAV99 (COL) R
3254	232271191032	RST SM 1206 JUMP. MAX 0R05 R	3465	319802131010	RST SM 0603 1 R PM5 COL		6212	319801010620	DIO SIG SM BAV99 (COL) R
3261	319802131010	RST SM 0603 1 R PM5 COL					6213	319801010620	DIO SIG SM BAV99 (COL) R
3262	319802131010	RST SM 0603 1 R PM5 COL	3469	319802133310	RST SM 0603 330R PM5 COL		6214	319801010620	DIO SIG SM BAV99 (COL) R
3263	319802131010	RST SM 0603 1 R PM5 COL	3471	319802133310	RST SM 0603 330R PM5 COL		6215	319801010620	DIO SIG SM BAV99 (COL) R
3264	319802131010	RST SM 0603 1 R PM5 COL	3473	319802190030	RST SM 0603 JUMP. 0R05 COL		6216	319801010620	DIO SIG SM BAV99 (COL) R
3273	319802131530	RST SM 0603 15K PM5 COL	3474	319802190030	RST SM 0603 JUMP. 0R05 COL		6217	319801010620	DIO SIG SM BAV99 (COL) R
			3501	235003510229	RST NETW SM ARV24 4X 22R PM5 R		6218	319801010620	DIO SIG SM BAV99 (COL) R
3274	319802131530	RST SM 0603 15K PM5 COL	3502	319802132290	RST SM 0603 22R PM5 COL		6219	319801010620	DIO SIG SM BAV99 (COL) R
3276	319802131010	RST SM 0603 1 R PM5 COL	3503	319802132290	RST SM 0603 22R PM5 COL		6220	319801010620	DIO SIG SM BAV99 (COL) R
3279	319802131010	RST SM 0603 1 R PM5 COL	3504	319802190030	RST SM 0603 JUMP. 0R05 COL				
3280	319802132210	RST SM 0603 220R PM5 COL	3505	319802131030	RST SM 0603 10K PM5 COL		6221	319801010620	DIO SIG SM BAV99 (COL) R
3281	319802132210	RST SM 0603 220R PM5 COL	3510	319802190030	RST SM 0603 JUMP. 0R05 COL		6222	319801010620	DIO SIG SM BAV99 (COL) R
3282	319802132210	RST SM 0603 220R PM5 COL					6223	319801010620	DIO SIG SM BAV99 (COL) R
3288	319802131030	RST SM 0603 10K PM5 COL	3511	319802131030	RST SM 0603 10K PM5 COL		6224	319801010620	DIO SIG SM BAV99 (COL) R
3290	232271161228	RST SM 1206 RC01 2R2 PM5 R	3513	232271161228	RST SM 1206 RC01 2R2 PM5 R		6230	933742280215	DIO SIG SM BAT54 (PHSE) R
3293	319802190030	RST SM 0603 JUMP. 0R05 COL	3514	319802190030	RST SM 0603 JUMP. 0R05 COL		6231	933742280215	DIO SIG SM BAT54 (PHSE) R
3295	319802190030	RST SM 0603 JUMP. 0R05 COL	3515	319802131030	RST SM 0603 10K PM5 COL		6232	933742280215	DIO SIG SM BAT54 (PHSE) R
			3520	235003510121	RST NETW SM ARV24 4X120R PM5 R		6233	933137390215	DIO REG SM BZX84-C5V1 (PHSE) R
3304	319802131020	RST SM 0603 1K PM5 COL	3521	235003510121	RST NETW SM ARV24 4X120R PM5 R		6234	933516950215	DIO REG SM BZX84-C3V3 (PHSE) R
3305	235003510121	RST NETW SM ARV24 4X120R PM5 R	3522	235003510121	RST NETW SM ARV24 4X120R PM5 R		6235	933742280215	DIO SIG SM BAT54 (PHSE) R
3306	319802132290	RST SM 0603 22R PM5 COL	3523	235003510121	RST NETW SM ARV24 4X120R PM5 R				
3307	319802132290	RST SM 0603 22R PM5 COL	3524	235003510121	RST NETW SM ARV24 4X120R PM5 R		6236	319801010620	DIO SIG SM BAV99 (COL) R
3308	319802132290	RST SM 0603 22R PM5 COL	3525	235003510121	RST NETW SM ARV24 4X120R PM5 R		6237	319801010620	DIO SIG SM BAV99 (COL) R
3309	319802190030	RST SM 0603 JUMP. 0R05 COL					6238	319801010620	DIO SIG SM BAV99 (COL) R
3310	319802190030	RST SM 0603 JUMP. 0R05 COL	3526	235003510121	RST NETW SM ARV24 4X120R PM5 R		6239	319801010620	DIO SIG SM BAV99 (COL) R
3312	232271191032	RST SM 1206 JUMP. MAX 0R05 R	3527	235003510121	RST NETW SM ARV24 4X120R PM5 R		6401	933913910115	DIO SIG SM BAS32L (PHSE) R
3313	319802132220	RST SM 0603 2K2 PM5 COL	3528	235003510121	RST NETW SM ARV24 4X120R PM5 R				
3316	319802190030	RST SM 0603 JUMP. 0R05 COL	3529	235003510121	RST NETW SM ARV24 4X120R PM5 R		7201	932214526668	IC SM M24C02-WMN6 (ST) R
			3530	235003510121	RST NETW SM ARV24 4X120R PM5 R	7202	932214526668	IC SM M24C02-WMN6 (ST) R	
3317	319802190030	RST SM 0603 JUMP. 0R05 COL	3531	235003510121	RST NETW SM ARV24 4X120R PM5 R	7205	932217546668	IC M61323FP (MITJ) R	
3318	319802131030	RST SM 0603 10K PM5 COL				7206	932202899668	FET POW SM Si4835DY (VISH) R	
3319	232270260205	RST SM 0603 RC21 2M PM5 R				7207	932209265685	TRA SIG SM MUN2211J (ONSE) R	
3323	232271191032	RST SM 1206 JUMP. MAX 0R05 R				7208	935241100118	IC SM 74LVC126AD (PHSE) R	
3326	319802131030	RST SM 0603 10K PM5 COL				7209	935245720115	IC SM 74HCT1G14GW (PHSE) R	
3327	232271191032	RST SM 1206 JUMP. MAX 0R05 R				7210	935245720115	IC SM 74HCT1G14GW (PHSE) R	
3328	232271191032	RST SM 1206 JUMP. MAX 0R05 R				7211	935245720115	IC SM 74HCT1G14GW (PHSE) R	
3329	319802131030	RST SM 0603 10K PM5 COL							
3401	232270260205	RST SM 0603 RC21 2M PM5 R	5201	242254943409	IND FXD 1206 EMI 1 MHZ 50R R				
3402	319802131030	RST SM 0603 10							

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71212	935245720115	IC SM 74HCT1G14GW (PHSE) R		3051	213810113103	RST CRB CFR-12 A 10K PM5 A	6111	319801010070	DIO SIG BAV21 (COL) A
7301	932216881671	IC SM GM5020 (GEMI) Y		3052	319802151010	RST SM 0805 1 R PM5 COL R	6112	933952580685	DIO SIG SM BAV103 (TEGO) R
7303	932217704668	IC SM LD1086DT25 (ST) R		3053	213810113103	RST CRB CFR-12 A 10K PM5 A	6114	932216287683	DIO REC PG4 7 A (PAJI) R
7309	932202899668	FET POW SM SI4835DY (VISH) R		3054	319802151030	RST SM 0805 10 K PM5 COL R			
7310	932209265685	TRA SIG SM MUN2211J (ONSE) R		3056	319802151010	RST SM 0805 1 R PM5 COL R			
7401	932216723671	IC SM MC68HC908LD60 (MOTA) Y		3059	319802151030	RST SM 0805 10 K PM5 COL R			
7402	932212662682	IC M24C16-BN6 (ST) L		3061	319802151830	RST SM 0805 18 K PM5 COL R	7051	933508380215	TRA SIG SM BSR14 (PHSE) R
7403	932209265685	TRA SIG SM MUN2211J (ONSE) R		3062	232273469102	RST SM 0805 RC12H 9K1 PM1 R	7052	933508380215	TRA SIG SM BSR14 (PHSE) R
7501	932216677682	IC SM M12L16161A-7T (ESMT) L		3063	213810100369	RST JUMP CR-12 A MAX 0R01 A	7061	932217683682	IC L4973V5.1 (ST00) L
7502	932216677682	IC SM M12L16161A-7T (ESMT) L		3071	319802151830	RST SM 0805 18 K PM5 COL R	7071	932216562682	IC L4978 (ST00) L
							7101	932213028682	IC L5991 (ST00) L
7503	932216677682	IC SM M12L16161A-7T (ESMT) L		3072	232273469102	RST SM 0805 RC12H 9K1 PM1 R	7102	932216376687	FET POW STU9NC80Z(ST00) L
7504	932209265685	TRA SIG SM MUN2211J (ONSE) R		3073	232271161278	RST SM 1206 RC01 2R7 PM5 R	7102	932217477687	FET POW STU10NB80 (ST00) L
7505	932217686668	IC SM THC63LVDM83A (THIN) R		3074	232271161278	RST SM 1206 RC01 2R7 PM5 R	7104	932214014667	OPT CP TCET1103(G) (VISH) L
7506	932217686668	IC SM THC63LVDM83A (THIN) R		3080	319802151020	RST SM 0805 1 K PM5 COL R	7106	933771100686	IC TL431CLP S (MOTA) R
				3091	319802190020	RST SM 0805 JUMP. 0R05 COL R	7107	933967380685	TRA SIG SM BC858C (ONSE) R
				3092	319802190020	RST SM 0805 JUMP. 0R05 COL R			
				3093	319802190020	RST SM 0805 JUMP. 0R05 COL R			
				3101	212261200061	NTC DC SCK-164 S 16R PM15 B			
				3102	232271161274	RST SM 1206 RC01 270K PM5 R			
				3103	232271161274	RST SM 1206 RC01 270K PM5 R			
				3104	213811611004	RST MFLM MFR-5S A100K PM1 A			
				3105	232271161184	RST SM 1206 RC01 180K PM5 R			
				3106	232273462703	RST SM 0805 RC12H 27K PM1 R			
				3107	319802151030	RST SM 0805 10 K PM5 COL R			
				3108	319802151030	RST SM 0805 10 K PM5 COL R			
				3109	319802151030	RST SM 0805 10 K PM5 COL R			
				3110	232271161274	RST SM 1206 RC01 270K PM5 R			
				3111	319802152240	RST SM 0805 220 K PM5 COL R			
				3112	213811612209	RST MFLM MFR-5S A22R PM1 A			
				3113	213811611002	RST MFLM MFR-5S A1K PM1 A			
				3114	213811612204	RST MFLM MFR-5S A220K PM1 A			
				3115	212010590708	RST MOX 1W RSS A0R33 PM5 B			
				3116	232273462702	RST SM 0805 RC12H 2K7 PM1 R			
				3117	232271161568	RST SM 1206 RC01 5R6 PM5 R			
				3118	232273461503	RST SM 0805 RC12H 15K PM1 R			
				3120	232271161184	RST SM 1206 RC01 180K PM5 R			
				3121	213811611509	RST MFLM MFR-5S A15R PM1 A			
				3122	319802151520	RST SM 0805 1K 5 PM5 COL R			
				3123	232273469102	RST SM 0805 RC12H 9K1 PM1 R			
				3124	319802151020	RST SM 0805 1 K PM5 COL R			
				3125	319802152240	RST SM 0805 220 K PM5 COL R			
				3126	232273462402	RST SM 0805 RC12H 2K4 PM1 R			
				3127	213811611205	RST MFLM MFR-5S A1M2 PM1 A			
				3128	232272461005	RST SM 1206 RC02H 1M PM1 R			
				3129	319802151090	RST SM 0805 10 R PM5 COL R			
				3131	232272461005	RST SM 1206 RC02H 1M PM1 R			
				3132	232272461005	RST SM 1206 RC02H 1M PM1 R			
				3133	319802154740	RST SM 0805 470 K PM5 COL R			
				3134	232271161478	RST SM 1206 RC01 4R7 PM5 R			
				3136	232271191032	RST SM 1206 JUMP. MAX 0R05 R			
				3181	212266200101	PTC DC 1A85 30V S 0R045 PM30 A			
				3191	319802190020	RST SM 0805 JUMP. 0R05 COL R			
				3192	232271191032	RST SM 1206 JUMP. MAX 0R05 R			
				5061	313816875391	CHOKE COIL 150UH PM10UH K9257			
				5071	313816875391	CHOKE COIL 150UH PM10UH K9257			
				5101	313818872091	FIL MAINS 12MH 1A5 902Y1R3			
				5103	313818872181	TFM SMT LAYER PQ2620 WIRE			
				5105	313816874511	FERRITE BEAD			
				5106	313816874511	FERRITE BEAD			

LCD COLOUR ANALYZER - CA110

1. SUMMARY

The LCD Colour Analyzer CA-110 was designed to upgrade the white-balance process on production lines for colour LCD televisions and computer colour LCD panels in the colour LCD industry. The CA-110 consists of a main unit and a measuring probe.

The measuring probe utilizes an optical system suitable for measurement of colour LCDs and is equipped with a viewfinder to verify the area to be measured.

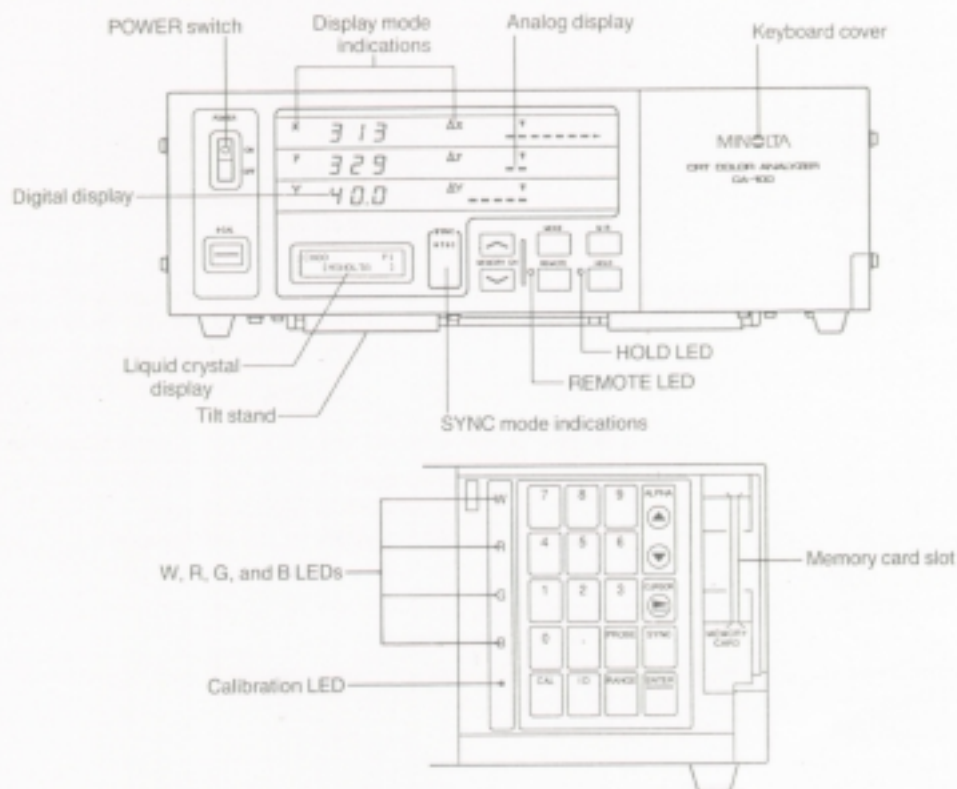
2. APPLICATIONS

- * White-balance adjustment and inspection on LCD production lines.
- * Quality control and shipping inspection by LCD manufacturers.
- * Inspection of LCDs upon receipt by computer manufacturers.

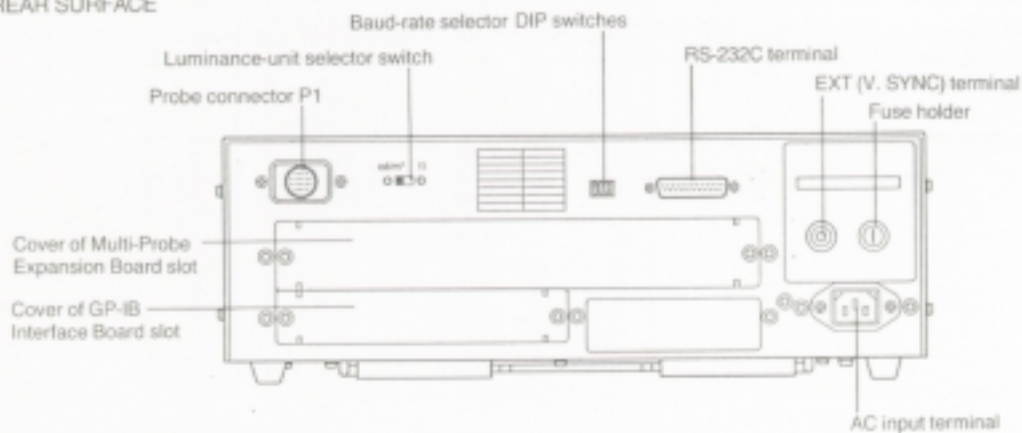
NAMES OF PARTS

Main Unit

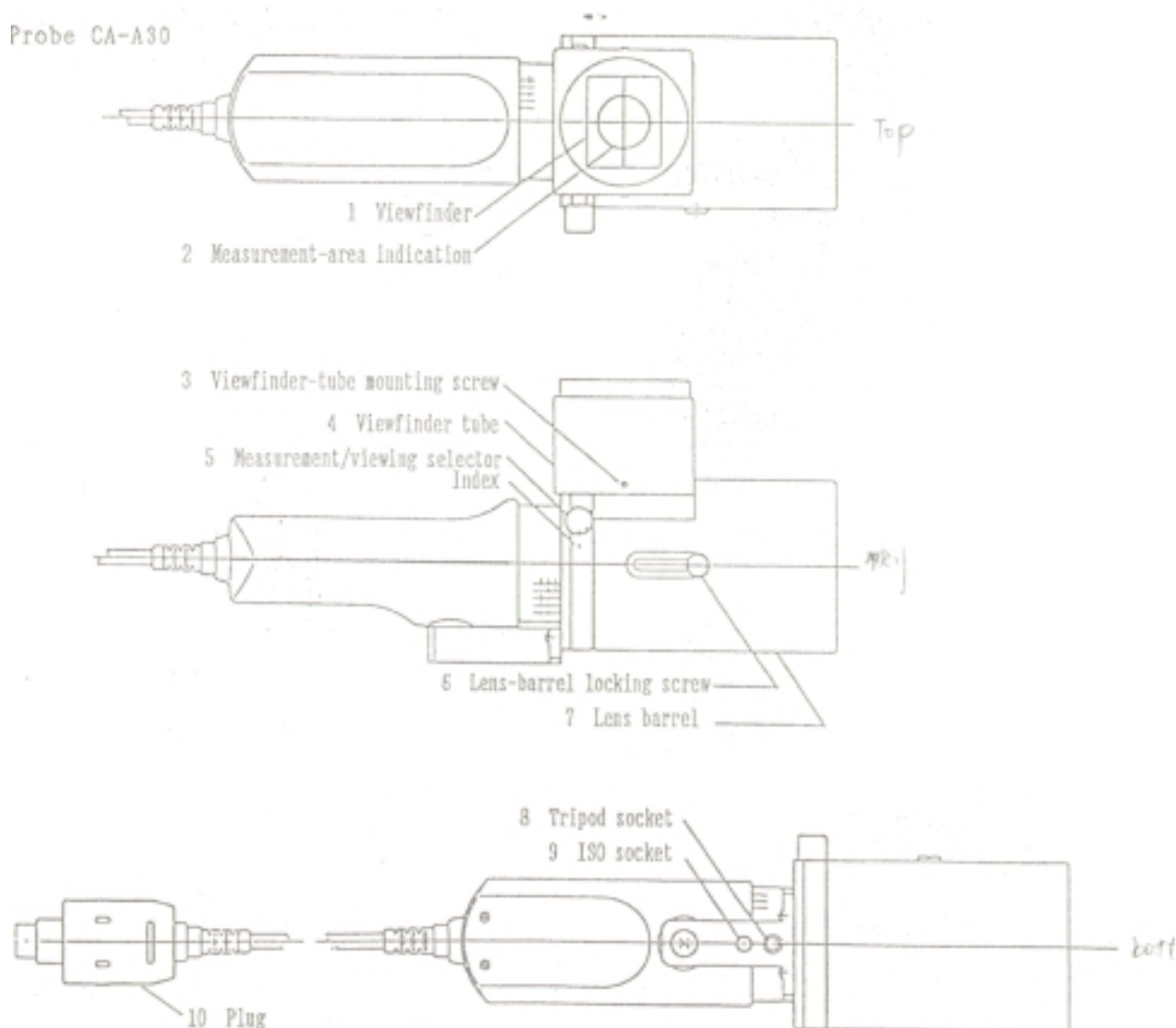
FRONT SURFACE



REAR SURFACE



Probe CA-A30



1. Viewfinder
2. Measurement-area indication
3. Viewfinder-tube mounting screw

4. Viewfinder tube

5. Measurement/viewing selector

6. Lens-barrel locking screw

7. Lens barrel

8. Tripod socket

9. ISO socket

10. Plug

Shows image seen by measuring probe.

Indicates area to be measured.

Removing these two screws (one on each side) allows the viewfinder tube to be removed to clean viewfinder, etc.

Can be moved to minimize the effects of surrounding light and provide the best view of the viewfinder image.

Moves internal mirror; set to $\bigcirc$ for measurement and to $\bullet$ for viewing or for zero calibration.

Locks lens barrel at a fixed position.

Can be moved back and forth to set measurement angle.

Can be used to mount measurement probe on a tripod. Depth: 6mm.

Can be used to mount measurement probe. ISO $\varnothing 5\text{mm}$, depth: 6mm

Used to connect measuring probe to main unit or optional Multi-Probe Expansion Board.

ZERO CALIBRATION

Zero calibration is performed to determine the output of the measuring probe when no light reaches the sensor and to set this as the zero point to which all other measurements are referenced. Zero calibration must be performed after the POWER switch has been set ON before taking any measurements.

To perform zero calibration :

- * Before performing zero calibration, check that the measuring probe has been connected to probe connector P1.

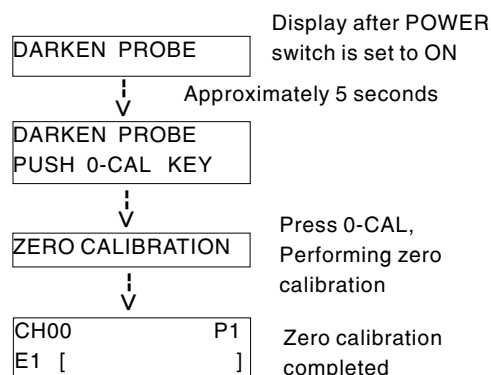
1. Check that the POWER switch is set to ON.

2. Set the measuring/viewing selector to the (viewing) position. (An image can be seen in the viewfinder, but no light will reach the sensor.)

3. Press 0-CAL.

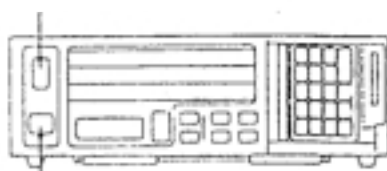
- * If zero calibration is being performed immediately after the POWER switch has been set to ON, press 0-CAL after "PUSH 0-CAL KEY" appears in the liquid crystal display.

4. Set the measuring/viewing selector to the position. Measurements will be started immediately.

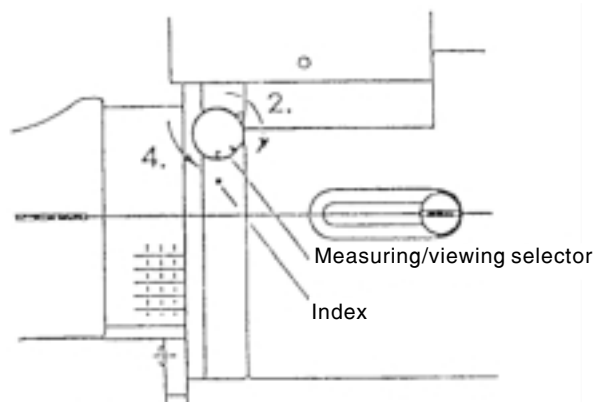


"E1" will appear in the liquid crystal display the first time the CA-110 is used after shipment because no standard color has been set.

1. POWER switch



3. 0-CAL



- "E1" will appear in the liquid crystal display the first time the CA-110 is used after shipment because no standard color has been set.
- Zero calibration can be performed at any time, even if "PUSH 0-CAL KEY" is not shown in the liquid crystal display.

Note:

- If the luminance of the LCD to be measured is 5.00cd/m² (1.46 fL) or less, wait at least five minutes after setting POWER switch to ON before performing zero calibration. Also, when measuring LCDs of low luminance, zero calibration should be performed approximately once an hour to ensure accuracy.
- If the ambient temperature changes after zero calibration has been performed, perform zero calibration again.
- Do not press any key while zero calibration is being performed. If a key is pressed, the time required for zero calibration will become longer.

To check if zero calibration was performed correctly, place the receptor area of the probe face down on a flat surface so that no light reaches the receptor area.

If the display shown at right appears in the liquid crystal display, perform zero calibration again.

- Even when "OFFSET ERROR" appears in the liquid crystal display, if light reaches the receptor area of the measuring probe, measured values will appear in the digital and analog displays. However, these values will not be accurate.

If any other display is shown, zero calibration was performed correctly.

OFFSET ERROR
PUSH 0-CAL KEY

SETTING MEASUREMENT AREA

Measurement areas of $\varnothing 25\text{mm}$ and $\varnothing 50\text{mm}$ can be selected by extending or retracting the lens barrel. The $\varnothing 25\text{mm}$ measurement area can be used for measuring LCDs with 2 - inch or greater diagonals; the $\varnothing 50\text{mm}$ measurement area can be used for measuring LCDs with 4 - inch or greater diagonals.

Set the measurement area :

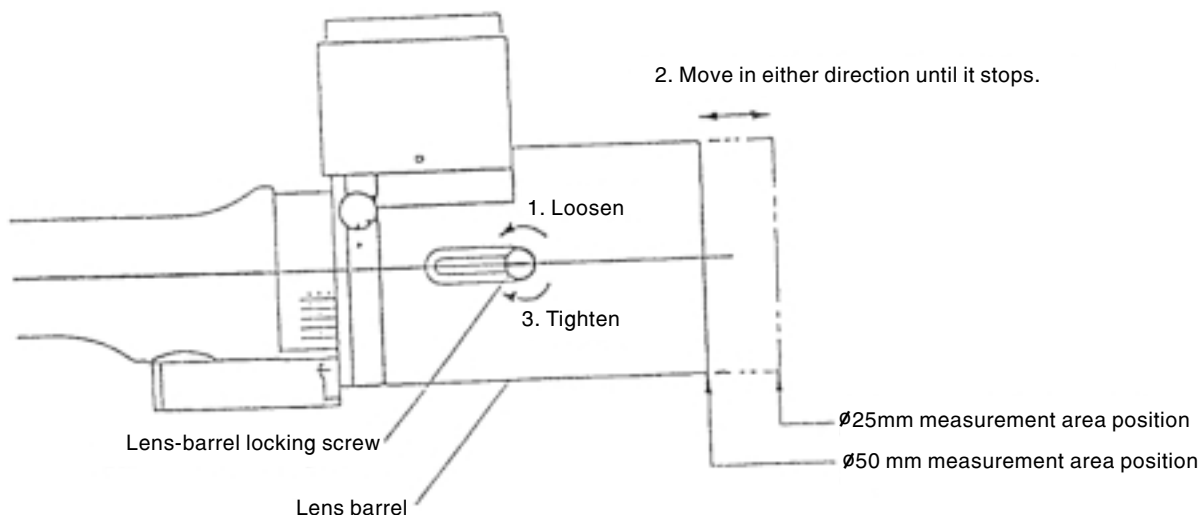
Using a slotted screwdriver, loosen the lens - barrel locking screw.

Slide the lens barrel to the position corresponding to the desired measurement area. The lens barrel should be slid in the desired direction until it stops.

Extending the lens barrel fully sets the $\varnothing 25\text{mm}$ measurement area: retracting the lens barrel fully sets the $\varnothing 50\text{mm}$ measurement area.

Use the screwdriver to tighten the lens - barrel locking screw and lock the lens barrel in position.

Changing the measurement area also changes the measurement angle. this may result in differences between values measured with the $\varnothing 25\text{mm}$ measurement area and those measured with the $\varnothing 50\text{mm}$ measurement area to the viewing - angle characteristics of the LCD. For this reason. it is recommended that the measurement area be constant for all measurements.



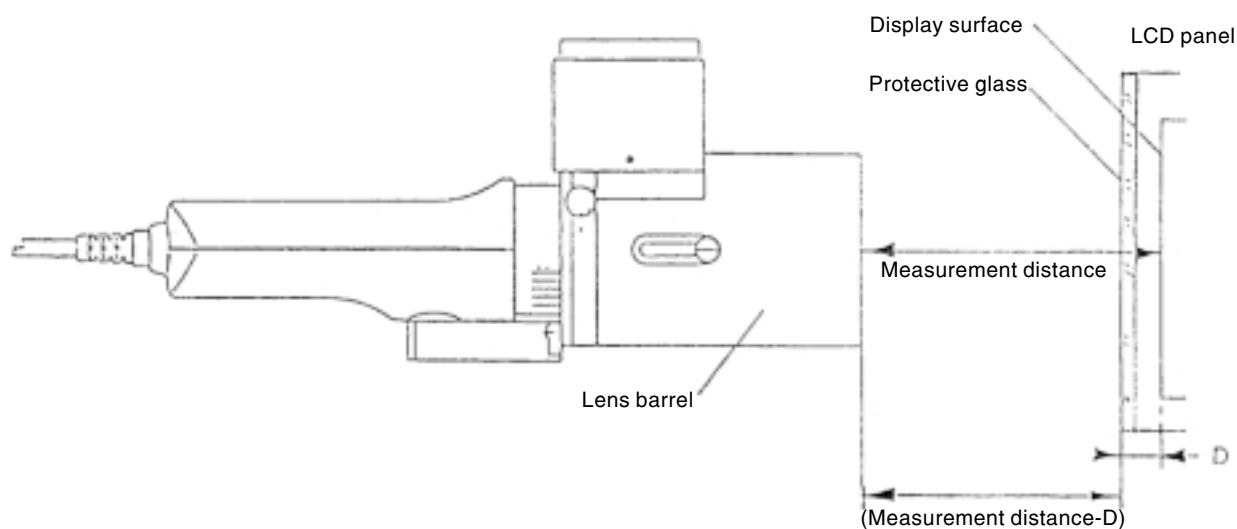
SETTING MEASUREMENT DISTANCE

The measurement distance (the distance from the front of the measuring probe's lens barrel to the display surface of the LCD) should be set using a ruler according to the procedure below.

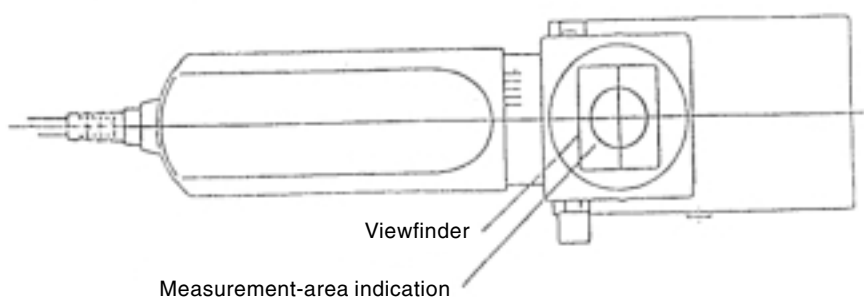
1. Mount the measuring probe on a tripod or other stand and mount the LCD on a suitable stand.
2. While using a ruler to measure the distance from the front of the measuring probe's lens barrel to the LCD's display surface, move the measuring probe or the LCD until the distance is the correct distance for the measurement area in use.

measurement area	∅ 25mm	∅ 50mm
measurement distance*	135mm+/-5mm	210mm+/-10mm

* Distance from the tip of the measuring probe's lens barrel to the LCD's display surface.



3. While looking through the viewfinder, move the measuring probe or LCD until the LCD section to be measured is inside the measurement-area indication in the viewfinder.



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After installation of Chroma 2250, CA110 and LCD monitor.

LCD monitor & Chroma 2250 :

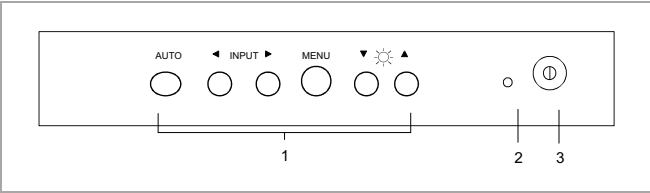
- Turn on LCD monitor.
- Turn on Chroma 2250 as shown in Fig. 7.
- Provide CROSS-Hatch pattern at timing H=60.023KHz/1024 pixels V=75.029Hz/768 lines by Chroma 2250 (as shown in Fig.3 & Fig. 8.).

LCD Colour Analyzer CA-110 :

- Put on the cover (black) of Probe CA-A30 as shown in Fig.9.
- Set "Measurement/viewing selector" to ● for zero calibration of CA-110.
- Turn on CA-110 as shown in Fig. 10.
- Press "0-CAL" button as shown in Fig. 11.

Step A: Access Factory Mode

Access Factory Mode



- 1 = Buttons for the OSD menu (On-Screen-Display)
- 2 = Power indicator
- 3 = Power button

Fig. 1

Factory Mode:

How to Get into Factory Mode Menu

Turn off LCD monitor.

Push AUTO "○" & "▲" & "Ⓢ" buttons simultaneously, then release Power button only, untill picture comes on the screen. Press MENU "○" button, bring up Factory mode indication as shown in Fig 2.



Fig. 2

Factory Mode indication==>

Use "○" button to select factory adjustment indication (for example: MAXDATA 180P V1.88 02-04-07), which is the entrance of the factory adjustment menu, press MENU "○" button to access it. The window shows as below.

Fig. 7



Fig.8

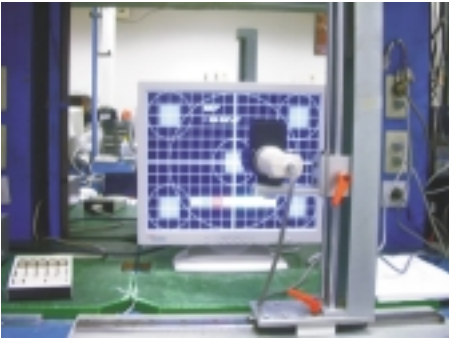


Fig. 9

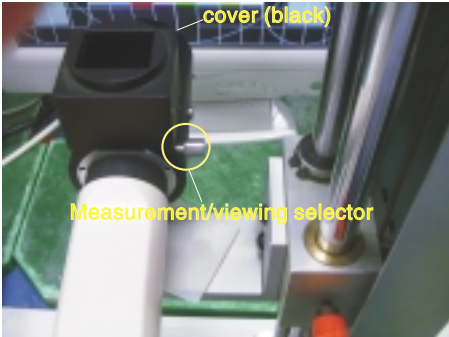


Fig. 10
(TURN ON)



Fig. 11
(Press 0-CAL)

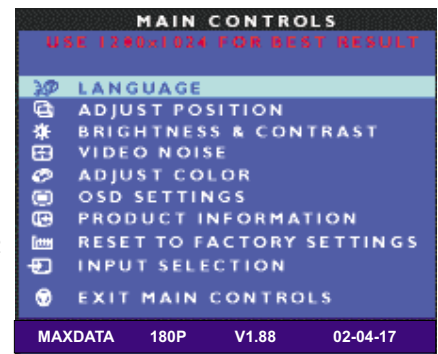


Step B: Brightness & Contrast setting (pre-setting)

Brightness : at 100%
Contrast : at 70%



Fig. 12

**Probe CA-A30 :**

- Remove the cover (black) of CA-A30 as shown in Fig.13.
- Move the "Lens barrel" back and forth to get the optimal image as shown in Fig. 14 & Fig. 15.
- Then, turn the "Measurement/viewing selector" counterclockwise as shown in Fig. 13.
- At this moment the indicator on CA-110 as shown in Fig. 16. (example only)



Fig. 13

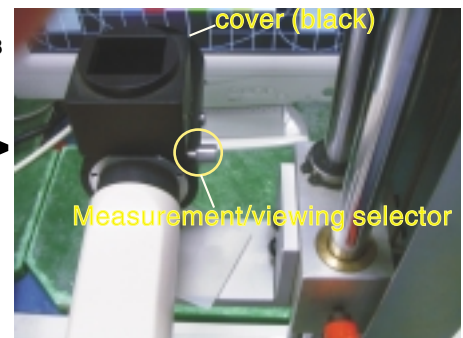


Fig. 14



unclear image

Fig. 15



clear image

Fig. 16

**Step C: Colour Temperature : Original Panel & 9300K & 6500K****White pattern & OSD position :**

- Provide the "Full White" pattern by Chroma 2250 as shown in Fig. 17.



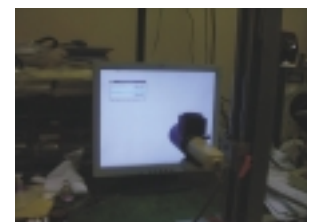
Fig. 17



OSD position : Move to optimal position as shown in Fig. 18.



Fig. 18



Go to cover page



Fig. 19



Luminance (Y) :
Above 200 NIT in the center of the screen as shown in Fig. 19.

FACTORY :
- Select "MAXDATA..." as shown in Fig. 20.
- Press Menu button, bring up Fig. 22.

ORIGINAL PANEL COLOR :

- Select "ORIGINAL PANEL COLOR" by OSD as shown in Fig. 21.
- Measure Luminance (Y) as shown in Fig. 23.

If the Luminance (Y) is below 200 Nit in the center of the screen, then adjust R,G,B Gain till the Luminance (Y) over 200Nit.



After that, check the 32 Gray Scale pattern, all the black and white scale should be visible on the screen as shown in Fig. 26.

9300K : As shown in Fig. 22 & 23.

- select 9300K R G B.
 - Value of 9300K R G B.**
 - select value of R or G or B
 - decrease or increase the value of 9300K R G B.
 - confirm/save the value change.
 - Adjust the colour temperature of 9300K of R,G,B to x,y value as below.
- R (as shown in Fig. 22), (for example, production value of R is 220)
G (as shown in Fig. 22), (for example, production value of G is 250)
B (as shown in Fig. 22), (for example, production value of B is 255)

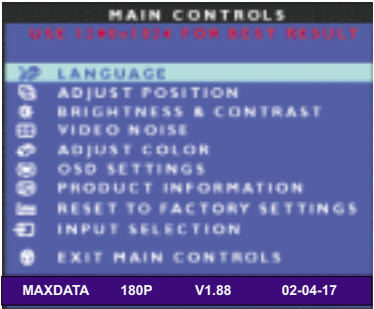


$x \text{ (center)} = 0.281 \pm 0.005$
 $y \text{ (center)} = 0.311 \pm 0.005$

Luminance (Y) : above 200 Nit in the center of the screen.
(experience: the value of "Y" too low, increase the value of "y" firstly.)

(experience: the value of "y" too high, decrease the value of "G".
the value of "x" too low, decrease the value of "R".)

Fig. 20



Factory Mode==>

Panel color==>

Fig. 21

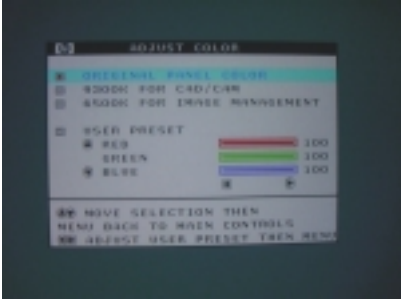


Fig. 22

MAXDATA 180P V1.88 02-04-17			
SUB —BRI: 00		SUB —CON: 50	
9300K	R 228	G 251	B 255
6500K	R 252	G 255	B 226
OFFSET	R 86	G 84	B 83
GAIN	R 181	G 188	B 182
AUTO —SUB:	RESERVE1: 205		
RESERVE2: 255	RESERVE3: 255		

Fig. 23



6500K : As shown in Fig. 22 & 24.

- select 6500K R G B.

Value of 6500K R G B.

- select value of R or G or B
- decrease or increase the value of 6500K R G B.
- confirm/save the value change.
- Adjust the colour temperature of 6500K of R,G,B to x,y value as below.

R (as shown in Fig. 22), (for example, production value of R is 241)

G (as shown in Fig. 22), (for example, production value of G is 255)

B (as shown in Fig. 22), (for example, production value of B is 226)



$x(\text{center}) = 0.312 \pm 0.005$

$y(\text{center}) = 0.338 \pm 0.005$

Luminance (Y) : above 200 Nit in the center of the screen.

(experience: the value of "Y" too low, increase the value of "y" firstly.)

(experience: the value of "y" too high, decrease the value of "G".
the value of "x" too low, decrease the value of "R".)

Full white, Geometry, 32 Gray scale & Pixel on/off pattern check

Full White pattern check by eye :

- Provides the full white pattern by Chroma 2250.
- Check the image of each timing (total: 14 timings) to see if there is any colour's difference on the screen.

For instance :

at timing 60K/75Hz 1024x768 --> as shown in Fig. 24.

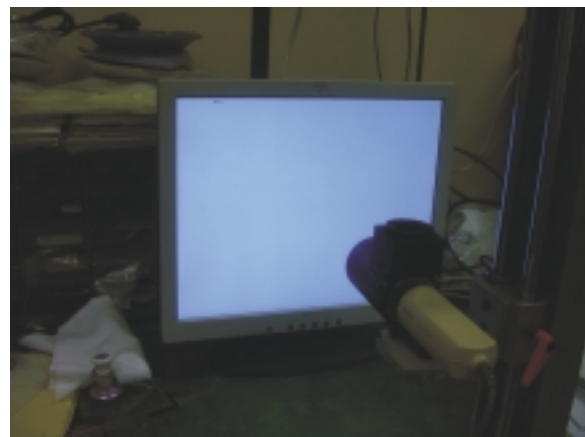


Fig. 24

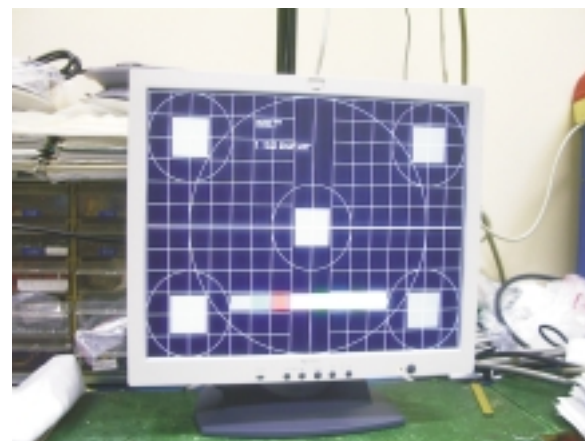


Fig. 25

Geometry check and setting by eye :

- Provides the cross-hatch pattern by Chroma 2250.
- Check the geometry of each timing (total: 14 timings) to adjust the geometry of image to optimal position.

For instance :

at timing 31.5K/70Hz 720x400 --> as shown in Fig. 25.



Fig. 26

32 Gray Scale pattern check by eye :

- Provides the 32 gray scale pattern by Chroma 2250.
- Check the image of each timing (total: 14 timings) to see if all the black and white scale are visible on the screen or not.

For instance :

at timing 60K/75Hz 1024x768 --> as shown in Fig. 26.

Pixel on/off pattern check by eye :

- Provides the cross talk pattern by Chroma 2250.
- Check the image of each timing (total: 14 timings) to see if there is any Clock or/and Phase phenomenon on the screen.

For instance :

at timing 60K/75Hz 1024x768 --> as shown in Fig. 27.



Fig. 27

0. Warning

All ICs and many other semi-conductors are susceptible to electrostatic discharges (ESD). Careless handling during repair can reduce life drastically. When repairing, make sure that you are connected with the same potential as the mass of the unit via a wrist wrap with resistance. Keep components and tools also at the same potential !

1. Servicing of SMDs (Surface Mounted Devices)

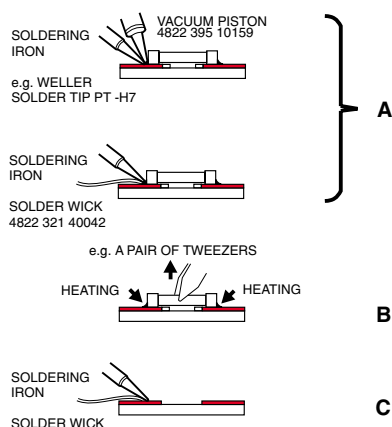
1.1 General cautions on handling and storage

- Oxidation on the terminals of SMDs results in poor soldering. Do not handle SMDs with bare hands.
- Avoid using storage places that are sensitive to oxidation such as places with sulphur or chlorine gas, direct sunlight, high temperatures or a high degree of humidity. The capacitance or resistance value of the SMDs may be affected by this.
- Rough handling of circuit boards containing SMDs may cause damage to the components as well as the circuit boards. Circuit boards containing SMDs should never be bent or flexed. Different circuit board materials expand and contract at different rates when heated or cooled and the components and/or solder connections may be damaged due to the stress. Never rub or scrape chip components as this may cause the value of the component to change. Similarly, do not slide the circuit board across any surface.

1.2 Removal of SMDs

- Heat the solder (for 2-3 seconds) at each terminal of the chip. By means of litz wire and a slight horizontal force, small components can be removed with the soldering iron. They can also be removed with a solder sucker (see Fig. 1A)

Fig. 1 DISMOUNTING



- While holding the SMD with a pair of tweezers, take it off gently using the soldering iron's heat applied to each terminal (see Fig. 1 B).
- Remove the excess solder on the solder lands by means of litz wire or a solder sucker (see Fig. 1C).

1.3 Caution on removal

- When handling the soldering iron, use suitable pressure and be careful.
- When removing the chip, do not use undue force with the pair of tweezers.
- The soldering iron to be used (approx. 30 W) should

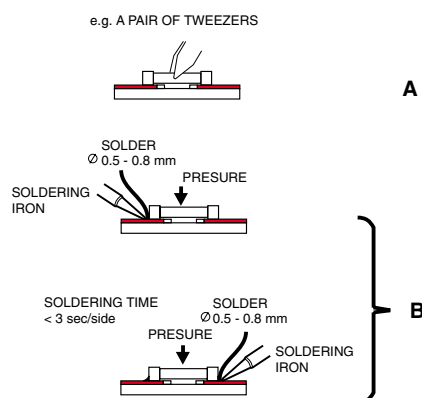
preferably be equipped with a thermal control (soldering temperature: 225 to 250 °C).

- The chip, once removed, must never be reused.

1.4 Attachment of SMDs

- Locate the SMD on the solder lands by means of tweezers and solder the component on one side. Ensure that the component is positioned correctly on the solder lands (see Fig. 2A).
- Next complete the soldering of the terminals of the component (see Fig. 2B).

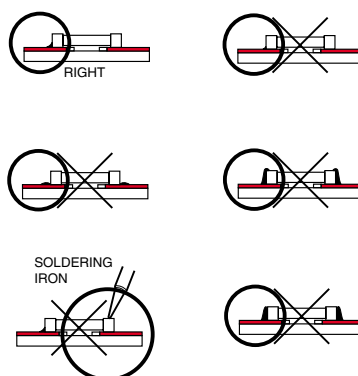
Fig. 2 MOUNTING



2. Caution when attaching SMDs

- When soldering the SMD terminals, do not touch them directly with the soldering iron. The soldering should be done as quickly as possible, care must be taken to avoid damage to the terminals of the SMDs themselves.
- Keep the SMD's body in contact with the printed board when soldering.
- The soldering iron to be used (approx. 30 W) should preferably be equipped with a thermal control (soldering temperature: 225 to 250 °C).
- Soldering should not be done outside the solder land.
- Soldering flux (of rosin) may be used, but should not be acidic.
- After soldering, let the SMD cool down gradually at room temperature.
- The quantity of solder must be proportional to the size of the solder land. If the quantity is too great, the SMD might crack or the solder lands might be torn loose from the printed board (see Fig. 3).

Fig. 3 Examples



GENERAL PRODUCT SPECIFICATION

- . ANALOG AND DIGITAL DUAL INPUT
- . AUTO PICTURE ADJUSTMENT
- . 16 FACTORY PRESET MODES AND 32 PRESET MODES WHICH CAN BE RECOVERED TO PRESET MODES
- . USER FRIENDLY OSD DISPLAY FOR MODE IDENTIFICATION /ADJUSTMENT
- . DDC 2B COMMUNICATION CAPABILITY
- . MAX. RESOLUTION 1280*1024 NONINTERLACED AT 75 HZ
- . 18 COLOR TFT LCD FLAT PANEL
- . EASY TILT & SWIVEL BASE
- . FULL RANGE POWER SUPPLY 90 264 VAC
- . CE ENVIRONMENTAL POLICY
- . ANTI-GLARE TO REDUCE LIGHT REFLECTION
- . POWER MANAGEMENT CAPABILITY
- . SOG SUPPORT
- . TCO 99
- . AUDIO SUPPORT
- . PROVIDE USB HUB (OPTION)
- . WALL MOUNT KIT (OPTION)
- . PROTECTIVE COVER (OPTION)

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01-12-21							
NAME	EDWARD CHANG	SUPERS.	23	590	—	1	10
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1.0 FOREWORD

This specification describes a 18" SXGA multi-scan color TFT LCD monitor with max. resolution up to 1280*1024 /76 Hz non-interlaced.

2.0 PRODUCT PROFILE

This display monitor unit is a color display monitor enclosed in global styling cabinet which has an integrated tilt and swivel base.

2.1 LCD

Type NR. : LM181E05-C3 (LG-Philips)
 Outside dimensions : 389(w)317.2(h)27(d) (Typ) mm
 Pitch (mm) : 0.2805 (per one triad) x 0.2805mm
 Color pixel arrangement : RGB vertical stripes
 Display surface : low reflection, antiglare with hard coating
 Number of color : 256 gray levels(8 bits)
 Backlight : CCFL edge light system
 Active area(WxH) : 359x287.2mm (18 diagonal)
 View angle : Horizontal & Vertical 160 degree (CR>=10)
 Contrast ratio : 350:1
 White luminance : 250 nits(Typ)

2.2 Scanning frequencies

Hor. : 30 82 K Hz
 Ver. : 56 - 76 Hz

2.3 Video dot rate

: < 140 MHz

2.4 Power input

: 90-264 VAC, 50/60 2 Hz

2.5 Power consumption

: < 60W maximum (exclude USB option)

2.6 Dimensions

: 426 * 454 * 180 mm

2.7 Weight

: 8.4 KGS

2.8 Functions :

- (1) D-shell analog R/G/B separate inputs, H/V sync separated, Composite (H+V) TTL level, SOG sync
- (2) DVI digital Panel Link TMDS input

2.9 Ambient temperature : 5°C - 35°C

2.10 Regulatory compliance :

- (1) Safety : UL, CSA, B-mark, SISIR, SEMKO, EN60950/IEC950, CB, RFS(NZ)
- (2) EMI : FCC, VCCI, CE, C-Tick, MPRIII, BCIQ
- (3) Environmental Issue: TCO99
- (4) Ergonomic Requirements : TUV/GS, TUV/ISO9241-3/-8

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3.0 Electrical characteristics

3.1 Interface signals

The input signals can be applied in three different modes :

1). D-shell Analog

Input signal : Video, Hsync., Vsync

Video : 0.7 Vp-p, input impedance, 75 ohm @DC

Sync. : Separate sync TTL level, input impedance 2k2 ohm terminate

Hor. sync Positive/Negative

Ver. sync Positive/Negative

Composite sync TTL level, input impedance 2k2 ohm terminate
(Positive/Negative)

Sync on green video 0.3 Vp-p Negative (Video 0.7 Vp-p Positive)

2). Intel DVI Digital

Input signal : Single channel TMDS signal

3.1.1 Audio

Input signal level : 500mVrms

Headphone output signal level : 32ohm 0.7+0.7 Vrms

Input signal connector : 3.5 mm mini jack

Loud speaker : 2+2 W stereo firing

3.1.2 Microphone

Sensitivity: -65+/- 5 dB re 1V/ubar at 1khz

Output impedance : 600 ohm typical

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3.2 Interface

3.2.1 D-Sub Cable

Length : 1.8 M+/- 50 mm (fixed)
 Connector type : D-sub male with DDC2B pin assignments.
 Blue connector thumb-operated jack screws

pin assignment :

PIN No.	SIGNAL
1	Red
2	Green/SOG
3	Blue
4	Sense (GND)
5	Not connected
6	Red GND
7	Green GND
8	Blue GND
9	+5V
10	Sync GND
11	Sense (GND)
12	Bi-directional data
13	H/H+V sync
14	V-sync
15	Data clock

3.2.2 DVI Cable

The input signals are applied to the display through DVI-I cable.

Length : 1.8 M+/- 50 mm (fixed)
 Connector type : DVI-I male with DDC2B pin assignments
 White connector thumb-operated jack screws

pin assignment :

Pin No.	Description
1	TMDS data2-
2	TMDS data2+
3	TMDS data2 shield
4	NC
5	NC
6	DDC clock
7	DDC data
8	Analog V-sync
9	TMDS data1-
10	TMDS data1+
11	TMDS data1 shield
12	NC

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13	NC
14	+5V
15	Ground(return for +5V and H/Vsync)
16	Hot plug detect
17	TMDS data0-
18	TMDS data0+
19	TMDS data0 shield
20	NC
21	NC
22	TMDS clock shield
23	TMDS clock+
24	TMDS clock-
C1	Analog R
C2	Analog G
C3	Analog B
C4	Analog H-sync
C5	Analog GND (Analog R, G, B return)

3.2.3 Software control functions via OSD/control

OSD (On Screen Display) function

(1) Analog interface OSD :

Adjustable functions:

MAIN CONTROLS

LANGUAGE

ADJUST POSITION

BRIGHTNESS & CONTRAST

VIDEO NOISE

ADJUST COLOR

OSD SETTINGS

PRODUCT INFORMATION

RESET TO FACTORY SETTINGS

INPUT SELECTION

EXIT MAIN CONTROLS

MOVE SELECTION THEN

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LANGUAGE : ENGLISH , ESPANOL , FRANCAIS ,
DEUTSCH , ITALIANO JAPANESE

ADJUST POSITION : HORIZONTAL
VERTICAL

BRIGHTNESS & CONTRAST : brightness and contrast adjustment.

VIDEO NOISE : Phase adjustment, Clock adjustment

ADJUST COLOR : Original panel color , 9300K for CAD/CAM , 6500K for
image management, User preset red green blue adjust.

OSD POSITION : OSD H-position, OSD V-position

PRODUCT INFORMATION : Show Serial No./ Resolution / Video input.

RESET TO FACTORY SETTING: recall to Factory preset settings.

INPUT SELECTION : select Analog D-sub, Digital DVI-D or Analog DVI-A.

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(2) Digital interface OSD :
Adjustable functions:

MAIN CONTROLS
LANGUAGE
ADJUST POSITION
BRIGHTNESS & CONTRAST
OSD SETTINGS
PRODUCT INFORMATION
RESET TO FACTORY SETTINGS
INPUT SELECTION
EXIT MAIN CONTROLS
MOVE SELECTION THEN ok

LANGUAGE : ENGLISH , ESPANOL , FRANCAIS ,
DEUTSCH , ITALIANO , JAPANESE

ADJUST POSITION : HORIZONTAL
VERTICAL

BRIGHTNESS & CONTRAST : brightness and contrast adjustment.

OSD POSITION : OSD H-position, OSD V-position

PRODUCT INFORMATION : Show Serial No. / Resolution / Video input.

RESET TO FACTORY SETTING: return to Factory preset timings and settings.

INPUT SELECTION : select Analog D-sub, Digital DVI-D or Analog DVI-A.

3.3 Timing requirement

3.3.1 Mode storing capacity

- (1) Factory preset modes : 16
(2) Preset modes : 32

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3.3.2 Factory preset timings

The factory settings of size and centering are according to the reference timing charts
(see fig-4, fig-5)

MODE NO.	1	2	3	4
RESOLUTION	640 x 350	720 x 400	640 x 480	640x480
Dot clock(MHz)	25.175	28.321	31.501	36
f h	31.469kHz	31.468kHz	37.5kHz	36kHz
A (us)	31.778(800 dots)	31.78(900dots)	26.667 (840 dots)	23.111 (832 dots)
B (us)	3.813(96 dots)	3.813(108dots)	2.032 (54 dots)	1.556(56 dots)
C (us)	1.907(48 dots)	1.907(54dots)	3.81 (120 dots)	2.222 (80 dots)
D (us)	25.422(640 dots)	25.42(720dots)	20.317 (640 dots)	17.778 (640 dots)
E (us)	0.636(16 dots)	0.636(18dots)	0.508 (26 dots)	1.555(56 dots)
f v	70Hz(70.09)	70Hz(70.085)	75Hz	85Hz
O (ms)	14.27(449 lines)	14.27(449lines)	13.333 (500lines)	11.763 (509 lines)
P (ms)	0.064(2lines)	0.064(2 lines)	0.08 (3lines)	0.069 (3 lines)
Q (ms)	1.907(60 lines)	1.112(34 lines)	0.427(16 lines)	0.578 (25 lines)
R (ms)	11.12(350 lines)	12.71(400lines)	12.8 (480lines)	11.093 (480 lines)
S (ms)	1.175(37 lines)	0.381(13 lines)	0.026 (1 lines)	0.023 (1 lines)
SYNC. H/V	+/-	-/+	- / -	-/-
POLARITY				
SEP . SYNC	Y	Y	Y	Y

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MODE NO.	5	6	7	8
RESOLUTION	640 x 480	640 x 480	640 x 480	800 x 600
Dot clock(MHz)	31.500	30.24	25.175	36
f h	37.861kHz	35 kHz	31.5kHz	35.2kHz
A (us)	26.413(832 dots)	28.571 (864 dots)	31.778(800 dots)	28.444(1024 dots)
B (us)	1.270(40 dots)	2.116 (64 dots)	3.813(96 dots)	2.000 (72 dots)
C (us)	3.810(120 dots)	3.175(96 dots)	1.907(48 dots)	3.556 (128 dots)
D (us)	20.317(640 dots)	21.164(640 dots)	25.422(640 dots)	22.222(800 dots)
E (us)	1.016(32 dots)	2.116(64 dots)	0.636(16 dots)	0.666 (24 dots)
f v	72.809Hz	67Hz	60Hz	56Hz
O (ms)	13.735(520 lines)	15 (525 lines)	16.683 (525 lines)	17.778 (625 lines)
P (ms)	0.079(3 lines)	0.086(3 lines)	0.064 (2 lines)	0.057 (2 lines)
Q (ms)	0.528(20 lines)	1.114(39 lines)	1.049 (33 lines)	0.626 (22 lines)
R (ms)	12.678(480 lines)	13.714(480 lines)	15.253 (480 lines)	17.066 (600 lines)
S (ms)	0.45(17 lines)	0.086(3 line)	0.317 (10 line)	0.029 (1 line)
SYNC. H/V	-/-	- / -	- / -	+ / +
POLARITY				
SEP . SYNC	Y	Y	Y	Y

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NAME EDWARD CHANG

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MODE NO.	9	10	11	12
RESOLUTION	800 x 600	800 x 600	800 x 600	832 x 624
Dot clock(MHz)	49.498	40	56.251	57.28
f h	46.9kHz	37.9kHz	53.7kHz	49.7kHz
A (us)	21.333 (1056 dots)	26.4 (1056 dots)	18.631 (1048 dots)	20.11(1152 dots)
B (us)	1.616 (80 dots)	3.2 (128 dots)	1.138 (64 dots)	1.117(64 dots)
C (us)	3.232 (160 dots)	2.2 (88 dots)	2.702 (152 dots)	3.91(224 dots)
D (us)	16.162 (800 dots)	20 (800 dots)	14.222 (800 dots)	14.52(832 dots)
E (us)	0.323 (16 dots)	1 (40 dots)	0.569 (32 dots)	0.563(32 dots)
f v	75Hz	60Hz	85Hz	75Hz
O (ms)	13.333 (625 lines)	16.579 (628 lines)	11.756(631 lines)	13.41(667 lines)
P (ms)	0.064 (3 lines)	0.106 (4 lines)	0.056 (3 lines)	0.06(3 lines)
Q (ms)	0.448 (21 lines)	0.607 (23 lines)	0.503 (27 lines)	0.784(39 lines)
R (ms)	12.8 (600 lines)	15.84 (600lines)	11.179 (600 lines)	12.55(624 lines)
S (ms)	0.021 (1 line)	0.026 (1 line)	0.018 (1 lines)	0.016(1 lines)
SYNC. H/V POLARITY	+ / +	+ / +	+ / +	+ / +
SEP . SYNC	Y	Y	Y	Y

MODE NO.	13	14	15	16
RESOLUTION	800 x 600	1024 x 768	1024 x 768	1024 x 768
Dot clock(MHz)	50	78.75	65	75
f h	48.077kHz	60kHz	48.363kHz	56.5kHz
A (us)	20.80 (1040dots)	16.66 (1312dots)	20.677(1344 dots)	17.707(1328 dots)
B (us)	2.400 (120 dots)	1.219 (96 dots)	2.092(136 dots)	1.813(136 dots)
C (us)	1.280 (64 dots)	2.235 (176 dots)	2.462(160 dots)	1.920(144 dots)
D (us)	16.00 (800 dots)	13.003 (1024 dots)	15.754(1024 dots)	13.653(1024 dots)
E (us)	1.120 (56 dots)	0.203 (16 dots)	0.369(24 dots)	0.321 (24 dots)
f v	72Hz (72.188)	75Hz (75.000)	60.004Hz	70.004Hz
O (ms)	13.85 (666 lines)	13.328 (800 lines)	16.666(806 lines)	14.272(806 lines)
P (ms)	0.125 (6 lines)	0.05(3 lines)	0.124(6 lines)	0.106(6 lines)
Q (ms)	0.478 (23 lines)	0.446 (28 lines)	0.600(29 lines)	0.514(29 lines)
R (ms)	12.48 (600 lines)	12.80 (768 lines)	15.880(768 lines)	13.599(768 lines)
S (ms)	0.770 (37 line)	0.017 (1 line)	0.062(3 lines)	0.053(3 lines)
SYNC. H/V POLARITY	+ / +	+ / +	- / -	- / -
SEP . SYNC	Y	Y	Y	Y

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MODE NO.	17	18	19	20
RESOLUTION	1024 x 768	1024 x 768	1152 x 864	1152 x 864
Dot clock(MHz)	83.096	94.5	108	94.5
f h	61.1kHz	68.7kHz	67.5kHz	63.9kHz
A (us)	16.367 (1360dots)	14.561 (1376 dots)	14.815 (1600 dots)	15.661(1480 dots)
B (us)	1.348 (112 dots)	1.016 (96 dots)	1.185 (128 dots)	1.016(96 dots)
C (us)	2.022 (168 dots)	2.201 (208 dots)	2.37 (256 dots)	1.116(105 dots)
D (us)	12.323 (1024 dots)	10.836 (1024 dots)	10.667 (1152 dots)	12.19(1152 dots)
E (us)	0.674 (56 dots)	0.508 (48 dots)	0.593 (64 dots)	1.339(127 dots)
f v	76Hz	85Hz	75Hz	70Hz
O (ms)	13.142 (803 lines)	11.765 (808 lines)	13.333 (900 lines)	14.283(912lines)
P (ms)	0.049 (3 lines)	0.044 (3 lines)	0.044 (3 lines)	0.047(3lines)
Q (ms)	0.507 (31 lines)	0.524 (36 lines)	0.474 (32 lines)	0.689(44 lines)
R (ms)	12.57 (768 lines)	11.183 (768lines)	12.8 (864 lines)	13.531(864 lines)
S (ms)	0.016 (1 line)	0.014 (1 line)	0.015 (1 lines)	0.016(1 lines)
SYNC. H/V	+ / +	+ / +	- / -	+ / +
POLARITY				
SEP . SYNC	Y	Y	Y	Y

MODE NO.	21	22	23	24
RESOLUTION	1152 x 864	1152 x 870	1152 x 900	1152 x 900
Dot clock(MHz)	79.9	100	94.5	108
f h	54.0kHz	68.7kHz	61.8kHz	71.8kHz
A (us)	18.523(1480 dots)	14.56 (1456 dots)	16.169 (1528 dots)	13.926 (1054dots)
B (us)	1.952(156 dots)	1.28 (128 dots)	1.354 (128 dots)	1.185 (128 dots)
C (us)	1.352(108 dots)	1.44(144 dots)	2.201 (208 dots)	1.778 (192 dots)
D (us)	14.418(1152 dots)	11.52 (1152 dots)	12.19 (1152 dots)	10.667 (1152 dots)
E (us)	0.801(64 dots)	0.32 (32 dots)	0.424 (40 dots)	0.296 (32 dots)
f v	60Hz	75Hz	66Hz	76Hz
O (ms)	16.671(900lines)	13.333 (916 lines)	15.151 (937lines)	13.132 (943 lines)
P (ms)	0.148(8 lines)	0.044 (3 lines)	0.065 (4 lines)	0.111 (8 lines)
Q (ms)	0.445(24 lines)	0.568(39 lines)	0.501 (31 lines)	0.46 (33 lines)
R (ms)	16.004(864 lines)	12.678 (870 lines)	14.552 (900lines)	12.533 (900 lines)
S (ms)	0.074(4 lines)	0.043 (4 line)	0.033 (2 line)	0.028 (2 lines)
SYNC. H/V	+ / +	- / -	Serr-	+ / +
POLARITY				
SEP . SYNC	Y	Y	Y	Y

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MODE NO.	25	26	27	28
RESOLUTION	1280 x 960	1280 x 960	1280 x 1024	1280 x 1024
Dot clock(MHz)	108	129.895	130.223	108
f h	60kHz	75kHz	76kHz	64kHz
A (us)	16.667(1800 dots)	13.307 (1728 dots)	13.158 (1712 dots)	15.63 (1688 dots)
B (us)	1.037(112 dots)	1.047 (136 dots)	1.024 (133 dots)	1.037 (112 dots)
C (us)	2.889(312 dots)	1.725 (224 dots)	1.905 (248 dots)	2.296 (248 dots)
D (us)	11.852(1280 dots)	9.857 (1280 dots)	9.83 (1280 dots)	11.852 (1280 dots)
E (us)	0.889(96 dots)	0.678 (88 dots)	0.399(51 dots)	0.445 (48 dots)
f v	60Hz	75Hz	72Hz	60Hz
O (ms)	16.667(1000 lines)	13.333 (1002 lines)	14 (1064 lines)	16.661 (1066 lines)
P (ms)	0.05(3 lines)	0.039 (3 lines)	0.02 (2 lines)	0.047 (3 lines)
Q (ms)	0.600(36 lines)	0.48 (36 lines)	0.5 (38 lines)	0.594 (38 lines)
R (ms)	16(960 lines)	12.774 (960 lines)	13.468 (1024 lines)	16.005 (1024 lines)
S (ms)	0.017(1 lines)	0.04 (3 lines)	0.012 (0 line)	0.015 (1 line)
SYNC. H/V	+/+	+ / +	+ / +	+ / +
POLARITY				
SEP . SYNC	Y	Y	Y	Y

MODE NO.	29	30	31	32
RESOLUTION	1280 x 1024	1280 x 1024	1280 x 1024	688 x 556
Dot clock(MHz)	135	138.008	117	27
f h	80kHz	81.1kHz	71.7kHz	31.25kHz
A (us)	12.504(1688 dots)	12.326 (1664 dots)	13.949 (1632 dots)	32 (864 dots)
B (us)	1.067(144 dots)	0.474 (64 dots)	0.957 (112 dots)	3.852 (104 dots)
C (us)	1.837(248 dots)	2.133 (288 dots)	1.915 (224 dots)	1.778 (48 dots)
D (us)	9.481(1280 dots)	9.481 (1280 dots)	10.94 (1280 dots)	25.481 (688 dots)
E (us)	0.119(16 dots)	0.238 (32 dots)	0.137 (16 dots)	0.889 (24 dots)
f v	75Hz	76Hz	67Hz	50Hz
O (ms)	13.329(1066 lines)	13.139 (1066 lines)	14.883 (1067 lines)	20 (625 lines)
P (ms)	0.038(3 lines)	0.099 (8 lines)	0.112 (8 lines)	0.128 (4 lines)
Q (ms)	0.475(38 lines)	0.394 (32 lines)	0.46 (33 lines)	1.408 (44 lines)
R (ms)	12.804(1024 lines)	12.622 (1024 lines)	14.283 (1024 lines)	17.972 (556 lines)
S (ms)	0.012 (1 line)	0.024(2 lines)	0.028 (2 lines)	0.672 (21 lines)
SYNC. H/V	+/+	- / -	+ / +	- / +
POLARITY				
SEP . SYNC	Y	Y	Y	Y

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3.3.3 Horizontal scanning

Sync polarity : Positive or Negative
Scanning frequency : 30 - 82 KHz

3.3.4 Vertical scanning

Sync polarity : Positive or Negative
Scanning frequency : 56 - 76 Hz

3.4 Power input connection

Power cord length : 1.8 M
Power cord type : 3 leads power cord with
protective earth plug.

3.5 Power management

The power consumption and the status indication of the set
with power management function are as follows,
Note : Disconnect DVI signal cable and audio module

STATUS	Horizontal	Vertical	Power Spec	LED
On	Pulse	Pulse	as normal on	Green
Stand-by	No Pulse	Pulse	< 2 W	Amber
Suspend	Pulse	No Pulse	< 2 W	Amber
off	No Pulse	No Pulse	< 2 W	Amber

According to VESA power saving signalling.
TCO99 power saving requirement
EPA energy star requirement

(Power Switch Off)
for Digital input power consumption is less 2W
(In non-DMPM recoverable off mode)

3.6 Display identification

3.6.1 In accordance with VESA Display Channel Standard V1.0 and
having DDC 2B capability

3.6.2 In accordance with DVI requirement (DDWG digital Visual Interface revision 1.0)
use DDC 2B and EDID 3.0 structure 2.0

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4.0 Visual characteristics

4.1 Test conditions

Unless otherwise specified, this specification is defined under the following conditions.

(1) Input signal : As defined in 3.3, 1280x 1024 non-interlaced mode (80K/75Hz), signal sources must have 75 ohm output impedance.

(2) Luminance setting: controls to be set to 200 nits with full screen 70% duty cycle white signal

(3) Warm up: more than 30 minutes after power on with signal supplied.

(4) Ambient light: 400 -- 600 lux.

(5) Ambient temperature: 20+/- 5°C

4.2 Resolution

Factory preset modes (16 modes)

#	Resolution	Frequency	Pixel rate	Sync	Comment
1	640X350	31.5K/70HZ	25.175	(+/-)	IBM VGA 10h
2	720X400	31.5K/70HZ	28.322	(-/+)	IBM VGA 3h
3	640X480	37.5K/75HZ	31.501	(-/-)	
4	640X480	31.5K/60HZ	25.175	(-/-)	
5	800X600	35.2K/56HZ	36	(+/+)	
6	800X600	46.9K/75HZ	49.498	(+/+)	
7	800X600	37.9K/60HZ	40	(+/+)	
8	832X624	49.7K/75HZ	57.28	(+/+)	MAC
9	1024X768	48.4K/60HZ	65	(-/-)	
10	1024X768	56.5K/70HZ	75	(-/-)	
11	1152X870	68.7K/75HZ	100	(-/-)	MAC
12	1152X900	71.8K/76HZ	108	(+/+)	SUN Mode II
13	1280X1024	64.0K/60HZ	108	(+/+)	
14	1280X1024	80.0K/75HZ	135	(+/+)	
15	1280X1024	81.1K/76HZ	135.008	(-/-)	SUN Mode I
16	688X556	31.25K/50HZ	27	(-/+)	TV-PAL

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- Note : 1. Screen displays perfect picture at 16 factory preset modes
 2. Screen displays visible picture with OSD warning when input modes are other then 32 preset modes

4.3 Brightness : 200 nits (at panel color temperature, 9 points averaged, Fig. 1)

4.4 Image size

4.4.1 Actual display size
 359x287mm

4.5 Brightness uniformity

Set contrast at 70% and turn the brightness to get average above 200 nits at center of the screen.

Apply the Fig 1, it should comply with the following formula:

$$\frac{B_{\max}}{B_{\min}} \times 100\% < 1.3$$

Where B_max = Average of 5 points maximum brightness

B_min = Average of 5 points minimum brightness

4.6 Check Cross talk(S)

Apply Pattern 2. Set contrast and brightness at 100 %.

Measure YA. Then output Pattern 3 and measure YB.

the cross talk value :

$$\frac{ABS(YA - YB)}{YA} \times 100\% < 1.2\%$$

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5.3 Tilt and swivel base

tilt angle : -5 to +35
swivel rotation : 175

5.4 Transportation packages

5.4.1 Shipping dimension/Weight

Carton dimension : 510(W) X 570(H) X 244(D) mm
Gross weight : 8.5 KGS

5.4.2 Block unit / Palletization

<u>layers/block</u>	<u>sets/layer</u>	<u>sets/block unit</u>
8	4	32

<u>blocks/container</u>
<u>20 feet</u> <u>40 feet</u>
10 21

6.0 Environmental characteristics

The following sections define the interference and susceptibility condition limits that might occur between external environment and the display device.

6.1 Susceptibility of display to external environment

Operating

- Temperature : 5 to 35 degree C
- Humidity : 80% max
- Altitude : 0-3658m
- Air pressure : 600-1100 mBAR

Storage

- Temperature : -20 to 60 degree C
 - Humidity : 85% max (<40)
 - Altitude : 0-12192m
 - Air pressure : 300-1100 mBAR
- Note: recommend at 5 to 35°C, Humidity less than 60 %

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6.2 Transportation tests

6.3

Standard		Philips UAN-D1400	NSTA
Drop Test	Height	70 cm	61 cm
	Sequence	1 corner 3 faces	1 corner 3 edge 6 face
	Test Result	electrical function ok mechanical function ok no serious damage on set appearance (room temp./-10 c, humidity 70 %)	
Vibration Test	Sequence	5-200 Hz 0.73 G 30 min. for each axis	10-50-10 Hz 0.35 mm 30 min. for each axis
	Test Result	electrical function ok mechanical function ok no serious damage on set appearance	
Bump Test	For design evaluation only operating 10 G, 11 msec, 1000 cycles temperature : 23 C humidity : 60 % air pressure : 100 kpa (according to DSD draft standard UAN -D636)		

6.3 Display disturbances from external environment
According to IEC801-2 for ESD disturbances

6.4 Display disturbances to external environment

6.4.1 EMI
EMI : FCC,VCCI,CE,C-Tick, MPRIII, BCIQ

7.0 Reliability

7.1 Mean Time Between Failures
System MTBF (Excluding the LCD panel and CCFL) : 50,000 hrs
CCFL MTBF : 50,000 hrs

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8.0 Quality assurance requirements

8.1 Acceptance test according to MIL-STD-105D Control II level

AQL : 0.65 (major)

2.50 (minor)

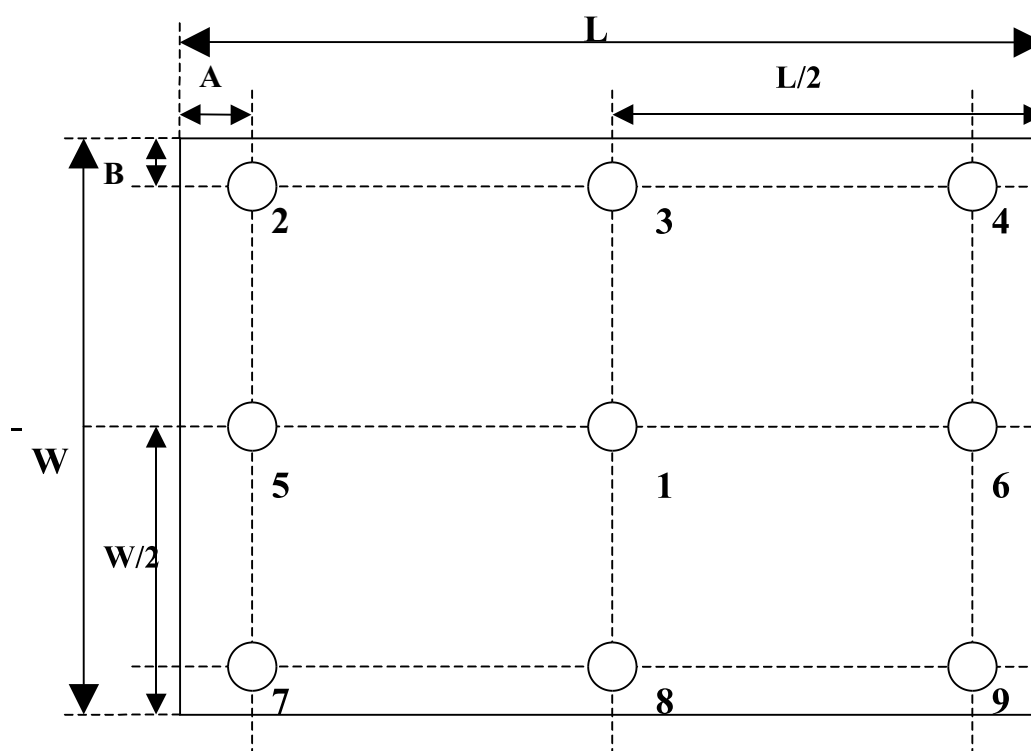
(please also refer to annual quality agreement)

Customer acceptance criteria : UAW0377/00

9.0 Serviceability

The serviceability of this monitor should fulfill the requirements which are prescribed in UAW-0346 and must be checked with the check list UAT-0361.

Fig 1: Brightness and Uniformity



A: L/10 mm

B: W/10 mm

L: 359.040 mm

W: 287.232 mm

Average= 9 points average

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Fig 2:

Cross talk pattern

Gray level 46 (64 Gray level)

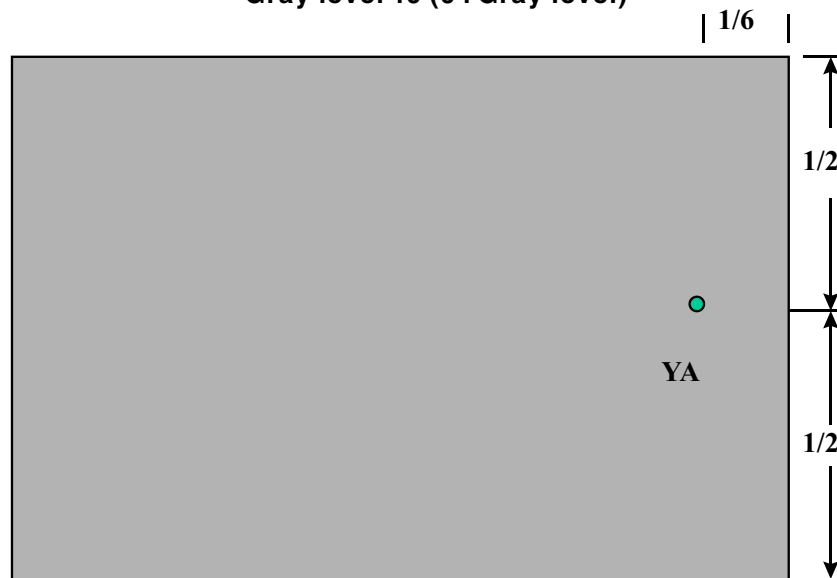
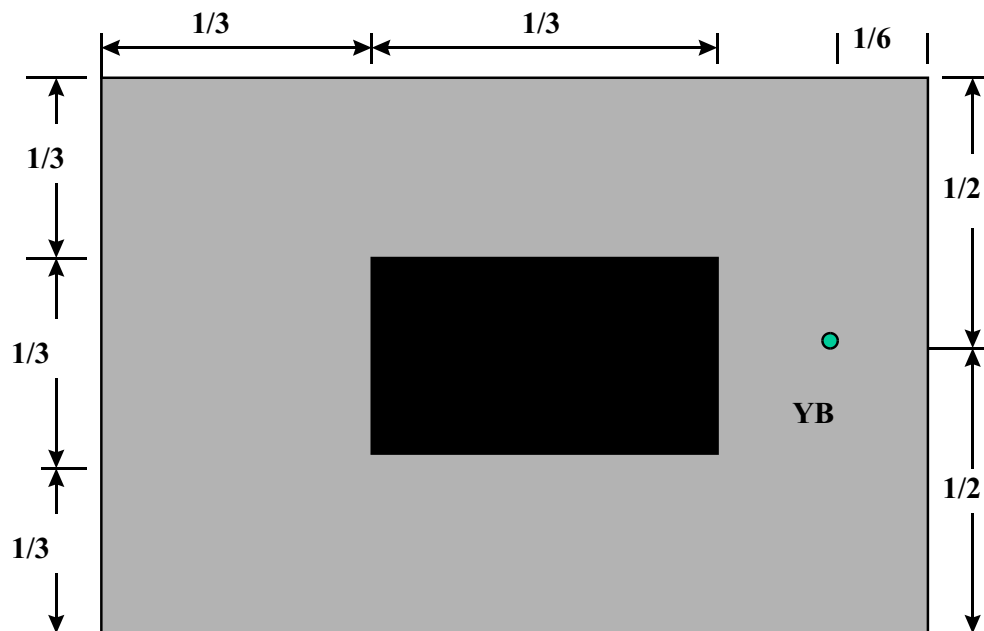


Fig 3:
Cross talk Pattern
Center at Gray level 0 (Black)

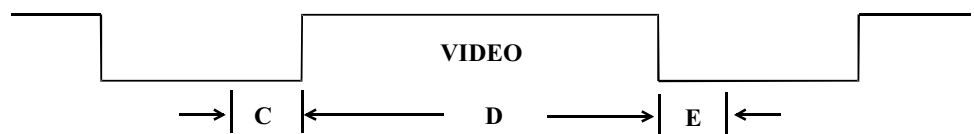


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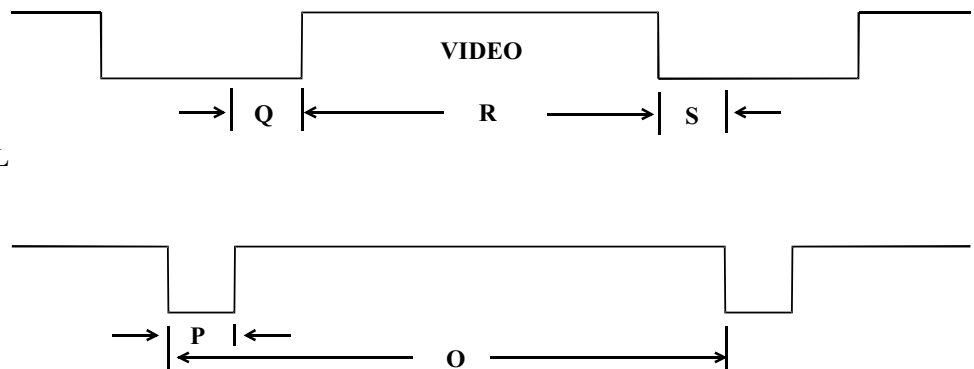
SEPARATE SYNC.



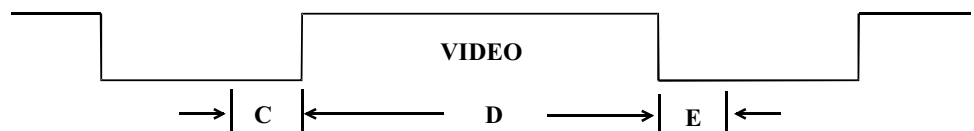
HORIZONTAL



VERTICAL



COMPOSITE SYNC.



HORIZONTAL

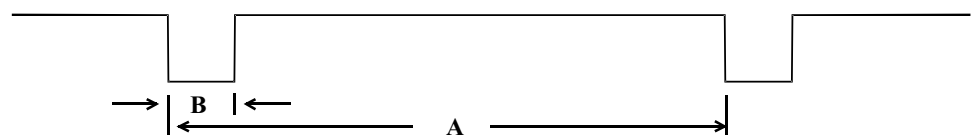


FIG-4 TIMING CHART -1

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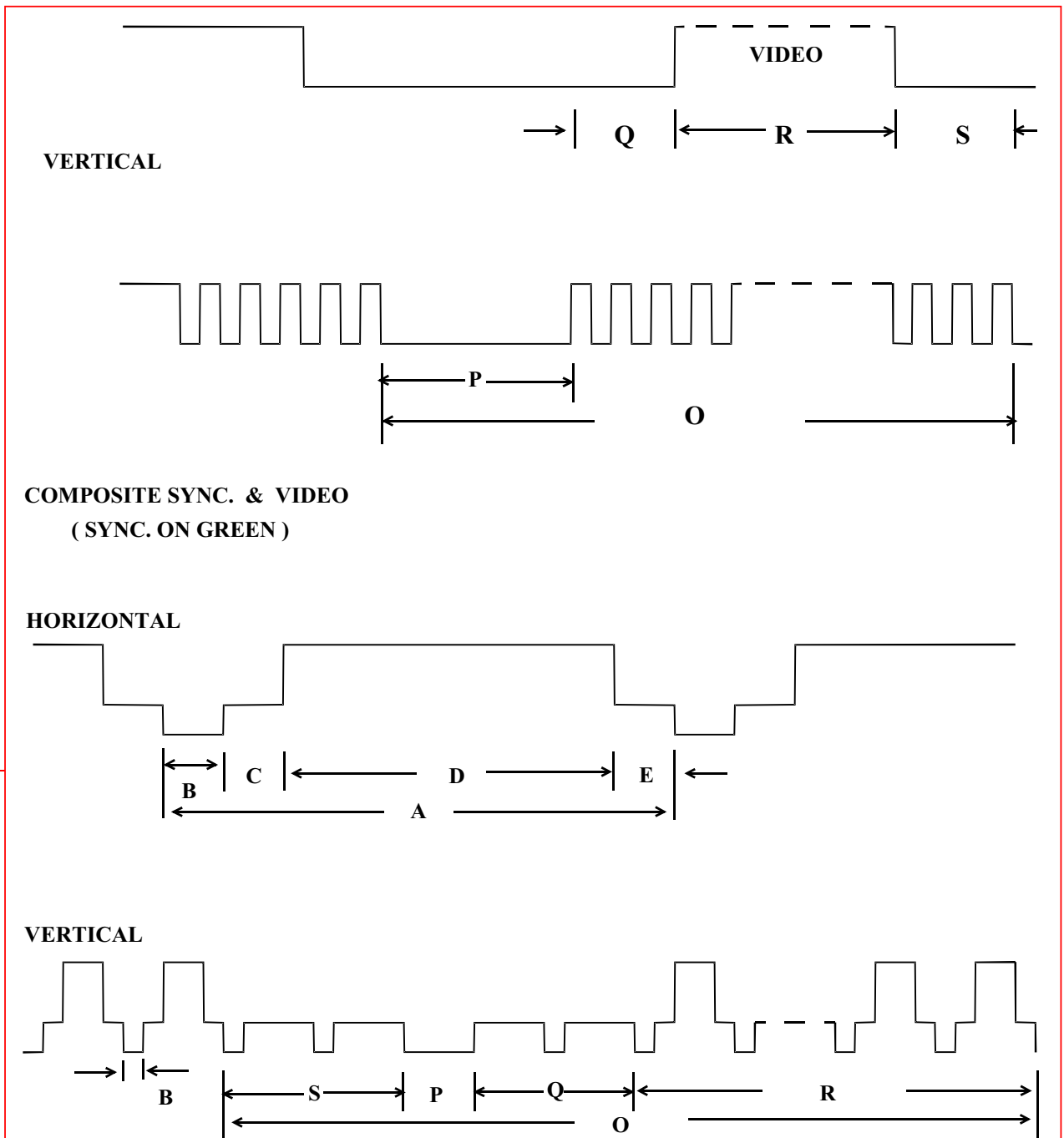


FIG-5 TIMING CHART -2

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Safety Test Requirements

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All units that are returned for service or repair must pass the original manufactures safety tests. Safety testing requires both *Hipot* and *Ground Continuity* testing.

HI-POT TEST INSTRUCTION

1. Application requirements

- 1.1 All mains operated products must pass the Hi-Pot test as described in this instruction.
- 1.2 This test must be performed again after the covers have been refitted following the repair, inspection or modification of the product.

2. Test method

2.1 Connecting conditions

- 2.1.1 The test specified must be applied between the parallel-blade plug of the mains cord and all accessible metal parts of the product.
- 2.1.2 Before carrying out the test, reliable conductive connections must be ensured and thereafter be maintained throughout the test period.
- 2.1.3 The mains switch(es) must be in the "ON" position.

2.2 Test Requirements

All products should be HiPot and Ground Continuity tested as follows:

Condition	HiPot Test for products where the mains input range is Full range(or 220V AC)	HiPot Test for products where the mains input is 110V AC(USA type)	Ground Continuity Test requirement
Test voltage	2820VDC (2000VAC)	1700VDC (1200VAC)	Test current: 25A,AC Test time: 3 seconds(min.) Resistance required: $\leq 0.09 + R$ ohm, R is the resistance of the mains cord.
Test time (min.)	3 seconds	1 second	
Trip current (Tester)	set at 100 uA for Max. limitation; set at 0.1 uA for Min. limitation	5 mA	
Ramp time	set at 2 seconds		

- 2.2.1 The test with AC voltage is only for production purpose, **Service center shall use DC voltage.**
- 2.2.2 The minimum test duration for Quality Control Inspector must be 1 minute. No breakdown during the test.
- 2.2.3 The test voltage must be maintained within the specified voltage + 5%.
- 2.2.4 The grounding blade or pin of mains plug must be conducted with accessible metal parts.

3. Equipments and Connection

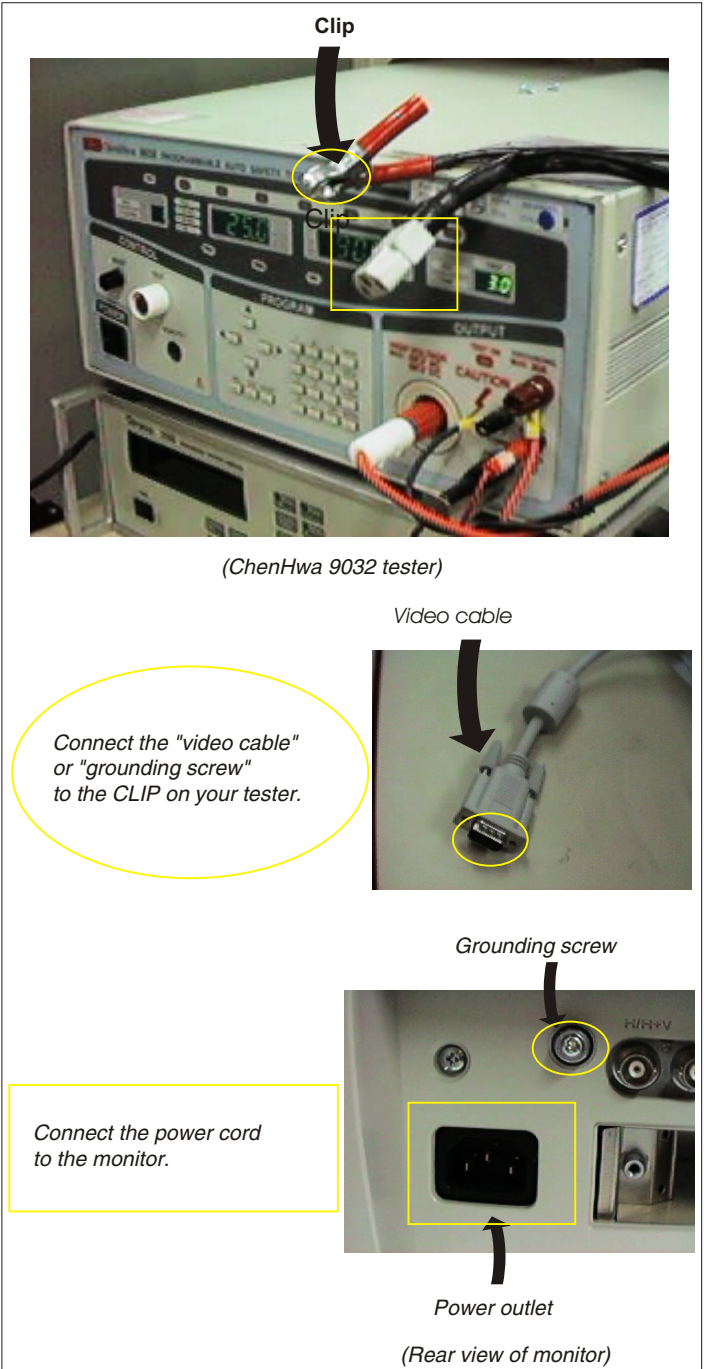
3.1. Equipments

For example :

- ChenHwa 9032 PROGRAMMABLE AUTO SAFETY TESTER
- ChenHwa 510B Digital Grounding Continuity Tester
- ChenHwa 901 (AC Hi-pot test), 902 (AC, DC Hi-pot test) Withstanding Tester

3.2. Connection

- * Turn on the power switch of monitor before Hipot and Ground Continuity testing.



4. Recording

Hipot and Ground Continuity testing records have to be kept for a period of 10 years.