Workshop Service Manual

Make

APRICOT

Model

XJ-52228

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SECTION 1. SPECIFICATION

1.1 Power Input

220-240V AC or 100-125/220-240V AC, 48-63Hz

depending on model.

Power consumption: <85W normal operation. <30W in power saving mode.

1.5 Operating Ranges Temperature: 10-35°C. Humidity: 20-85% (noncondensing).

1.2 Sync Input TTL Levels:

			γ	***	
	Mode	H-Sync	V-Sync	H Freq/kHz	V-Freq/Hz
	1	+	-	31.5	70.1
	2	_	+	31.5	70.1
	3	-	-	31.5	59.9
	4	+	+	37.9	60.3
	5	+	-	37.8	84.1
	6	-	+	37.7	84.0
1	7	-		37.8	72.8
	8	+	+	48.1	72.0
L	9	+/-	+/-	48.4	60.0
L	10	_	-	56.5	70.1
	11	+/-	+/-	57.0	70.7

- (P models only) Presence of sync. signals determines the power saving status.
- 1.3 Signal Cable
 1 metre long with 15-way sub-miniature 'D' connector
- 1.4 Cathode Ray Tube
 14" (13V) Diagonal landscape mode or
 15" (14V) diagonal FST landscape mode.
 Dot pitch 0.28mm, anti-glare screen. V models only: VLMF (Very Low Magnetic Field emissions). VA models only: Anti-static and VLMF

- 1.6 Weight 13kg (14kg for 15" model).
- 1.7 Video Input RGB Analogue video signal 0.71V positive.
- 1.8 User Controls Power On/Off Contrast Brightness Width V-Centre Height H-Phase
- 1.9 Display Colours Infinite array
- 1.10 Display Area 14" 260mm W x 190mm H (approximately) 15" 270mm W x 202mm H (approximately)
- 1.11 Dimensions Width 361mm.
 Depth 382mm.
 Height 380mm with
 swivel base fitted, 345mm without.

SECTION 2. PRECAUTIONS AND SAFETY

- 2.1 Observe all cautionary and safety related notes located on the chassis, cabinet and display tube.
- 2.2 Operation of the monitor with the back cover removed presents a potential shock hazard. Only personnel familiar with the precautions necessary for safe working on high voltage equipment should attempt to
- 2.3 Always wear safety approved shatter-proof goggles when removing, installing or generally handling the picture tube. People not so equipped should be kept at a safe distance when any such handling is being undertaken. Do not handle the picture tube by the neck or deflection coil. Do not carry the picture tube resting against the body.

2.4 The picture tube is designed and constructed to limit X-Radiation to a safe level during normal operation. To maintain the required level of protection and safe operation, replacement tubes must be correctly adjusted and any protective circuits must not be defeated.

2.5 A.C. Leakage Current Test

After servicing and before returning the display to the customer, perform a thorough safety test to ensure there is no potential shock hazard to the operator. The safety test should consist of a high voltage test (1500V rms, 50/60Hz) between live and neutral joined together and earth for one minute; an earth continuity test at 25 amps between the primary safety earth point (marked with a symbol, located near the AC power inlet connector) and the earth terminal of the AC power plug and, if possible, an AC current leakage test on the exposed metallic parts of the cabinet, e.g. signal cable shell and screw heads. Using the test circuit shown in Fig.1, connect the monitor power lead, via an isolating transformer, to the AC supply and switch on.

Measure the AC leakage current between exposed metallic parts of the cabinet and each pole of the isolated supply in turn. The earth leakage current should not exceed 1.0mA rms.

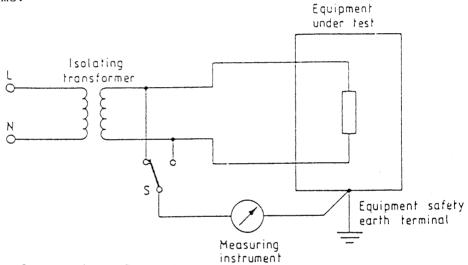


Fig. 1. Measurement of AC Leakage Current.

2.6 Critical Safety Components

A number of electrical components in this monitor contribute to operating safety, and the protection afforded by them cannot necessarily be maintained by using replacement components rated for higher voltage, wattage, etc. They are identified by the A symbol which indicates that only manufacturer's approved replacements are to be used.

2.7 Cabinet Back Removal

- a) Place the monitor on its front, protecting the screen and cabinet with some suitable material, and remove the tilt/swivel base by pressing down its retaining clip at the rear of the swivel ball, and at the same time slide the swivel base towards the back of the cabinet to release the bayonet catches.
- b) Remove the retaining screw just below and to the right of the power inlet socket.
- c) Remove the two retaining screws at the top left-hand and top right-hand corners of the cabinet back (if fitted).
- d) Remove the two retaining screws from the bottom left-hand and bottom right-hand corners of the cabinet back.

- e) Carefully replace the cabinet on its base. Insert a screwdriver blade into the slot next to each screw (item c) pressing downward and forward to release the retaining tabs whilst easing the cabinet away from the front bezel.

Replace the back by reversing the procedure in steps a to f omitting step e. Note: when re-assembling the cabinet the retaining tabs mentioned in step e can be locked by pressing the cabinet back and bezel firmly together.

2.8 Servicing notes

2.8.1 Soldered Connections

Always wrap lead wires around terminals before soldering.

2.8.2 Wire Replacement

Always use the correct wire type. Run connecting wires along their original routes in order to :

- a) Avoid introducing unwanted interference.
- b) Avoid them being too close to high voltage or temperature.
- c) Maintain safety approval standards.

SECTION 3. OPERATING INSTRUCTIONS

3.1 Connections

The Colour Monitor should be connected to a computer incorporating a VDE or FCC approved USVGA graphics display card which supplies analogue video signals.

Connect the captive signal cable to the 15 pin output socket on the card and secure it in place with the locking screws. Connect the monitor and computer to the AC supply.

3.2 Controls

3.2.1 Power ON/OFF

The ON/OFF switch is located on the right by the 'Power On' LED which should illuminate shortly after switching on. The display should become visible within 30 seconds of switching on.

IMPORTANT. Repeatedly switching ON and OFF should be avoided. This action may activate Safety Protection shut-down circuits. Should this occur or the supply be accidentally interrupted, switch OFF and allow 30 seconds for the circuits to reset before switching on again.

3.2.2 Contrast

This control varies the difference in intensity between black and coloured areas of the screen. After setting the brightness control as described below, set the contrast for the most comfortable display.

3.2.3 Brightness

This sets the average intensity of the whole display. Normally this control will be set at the centre detent position.

3.2.4 Width

Allows the user to vary the display width from 255 to 265mm (14" CRT) or 263 to 277mm (15" CRT) approximately.

3.2.5 V-Centre

Allows the picture to be centred vertically, to compensate for the user's viewing angle.

3.2.6 Height

This control allows the height to be adjusted to compensate for

3.2.7 H-Phase

This control may have to be adjusted to suit the particular graphics card in use. Some graphics cards may require adjustment when changing

3.2.8 Plinth

The plinth can be rotated or tilted to improve the viewing angle.

SECTION 4. CIRCUIT DESCRIPTION

4.1 Power Supply

The power supply is of the flyback switch mode type and will operate over the input range 110-125V or 220-240V rms $\pm 10\%$ (depending on model). The circuit is based around IC801 (UC3842AN) which directly drives the switching FET Q801 at a fixed frequency synchronized to the line output stage. The output power is regulated by duty cycle control depending on input voltage and load.

European models are designed for 220-240V operation. In these models, diodes D801 - D804 form a full wave bridge rectifier and CE807 provides smoothing.

North American models are designed for 110-120V or 220-240V, switch selectable. With the switch open (220-240V operation), diodes D801-D804 form a full wave bridge rectifier and capacitors CE807 and CE808 in series provide smoothing. With the switch closed (110-120V operation), diodes D801-D804 and capacitors CE807 and CE808 form a voltage doubler so that the rectified voltage at the transformer (T801) primary is the same as for 220-240V

Immediately after switch-on, current derived from the rectified mains input flows through R807, R808 and D830 charging CE812 to approximately 17V. At this point IC801 makes a start attempt relying on stored charge in CE812 to supply current until the power supply starts. Operating current is then derived from a winding on the transformer via D812/R823 at 12.5V. The circuit operates as

Q801 is switched on, causing a linear rise in current in the primary winding 1-3 of T801. After a period determined by the voltage drop across the current limit resistor R828, Q801 is switched off and the energy stored in the flux of T801 is transferred to the secondary circuits. Diodes D815, D816 and D817 conduct until the transferred is demogratized at which point of the secondary circuits. conduct until the transformer is demagnetized, at which point after a short delay, Q801 is switched on again as before.

The transformer secondary windings produce 155V, 80V, 21V and 7V d.c. when rectified by D818, D820, D821 and D822 respectively. A protective clamp is incorporated in the 155V rectifier circuit formed by D819, C825 and R833 to incorporated in the 155V rectifier circuit formed by D819, C825 and R833 to prevent damage to D818 under certain conditions. The dv/dt on Q801 drain is prevent damage to D818 under certain conditions. limited by D811 which charges C819. In addition, D809/C818 clamps the peak drain voltage to about 500V to prevent damage to Q801.

The main regulation circuit centres around IC803 which conducts heavily when the wall regulation circuit centres around 10000 which conducts neavily when the voltage on the reference pin exceeds 2.5V. The regulation circuit senses the 155V and 21V rails and the control signal is fed back to the error the 155V and 21V rails and the opto-isolator IC802. This forms the voltage amplifier in IC801 via the opto-isolator IC802 amplifier in IC801 via the opto-isolator is also amplified by the primary side to feedback loop. ampilitier in 10001 via the opto-isolator 10002. This forms the voltage feedback loop. A current feedback loop is also employed on the primary side to sense the drain current in Q801. The linear ramp voltage across R828 (1V max) is fed into pin 3 of IC801. This is filtered by R826, C813 to remove transient is fed into pin 3 of IC801. This is filtered by R826, C813 to remove transformer flux spikes. The current feedback circuit actively limits the peak transformer flux divising each availaged to provide food-forward compensation to improve during each cycle and is used to provide feed-forward compensation to improve

The operating frequency is synchronized to the line output stage using a double-insulated single turn of wire around the flyback transformer core. This sync. pulse is fed into IC801 via TR801, but is momentarily shorted by TR802 at switch on. The power supply is synchronized to line frequency in 31 to 38kHz modes and half line frequency at 48 to 60kHz. (i.e. 24 to 30kHz).

Power Management (where fitted).

The Power Management control (PMC) circuit selects mains power saving mode when field OR line sync. pulses are absent for approximately one second. This is achieved by removing the 21V supply from the Deflection board (except 12V regulator input) and reducing tube heater voltage.

Horizontal and vertical syncs. are fed to two re-triggerable monostables IC701A and IC701B. The time constant of these are determined by R702/CE702, and R703/CE703 respectively. Provided that sync. pulses are received at the input to monostable A, the output will remain logic high (5V) and be fed to input to monostable A, the output will remain logic high (5V) and be fed to the CLR input of monostable B. Monostable B remains low if its CLR input is high AND sync. pulses detected at its input. The output is fed via a service link to the base of voltage amplifier TR701. To achieve saturation of link to their gate/source voltage must be in excess of approximately 3 Q701/Q702 their gate/source voltage must be in excess of approximately 3 volts, therefore, biasing is sourced from the 80V supply via R705 and limited volts, therefore, biasing is sourced from the solv supply via R705 and limited volts, therefore, biasing is sourced from the S0V supply via R705 and limited volts, therefore, biasing is sourced from the B0V supply via R705 and limited volts, therefore, biasing is sourced from the B0V supply via R705 and limited volts, therefore, biasing is sourced from the S0V supply via R705 and limited volts, therefore, biasing is sourced from the S0V supply via R705 and limited volts, therefore, biasing is sourced from the S0V supply via R705 and limited volts, therefore, biasing is sourced from the S0V supply via R705 and limited volts, therefore, biasing is sourced from the S0V supply via R705 and limited volts, therefore, biasing is sourced from the S0V supply via R705 and limited volts, therefore, biasing is sourced from the S0V supply via R705 and limited volts, therefore, biasing is sourced from the S0V supply via R705 and limited volts, therefore, biasing is sourced from the S0V supply via R705 and limited volts, therefore, biasing is sourced from the S0V supply via R705 and limited volts, therefore, biasing is sourced from the S0V supply via R705 and limited volts, therefore, biasing is sourced from the S0V supply via R705 and limited volts, therefore, biasing is sourced from the S0V supply via R705 and limited volts, the source volts are visited to the source volts and visited volts. panel disabling the line drive and frame scan circuits etc., reducing overall power consumption to less than 30W.

When the monitor switches to standby the field and line scans rapidly when the monitor switches to standby the free and fine scans rapidly collapse. The spot suppression circuit TR702 switches on pulling the positively charged CE705 +ve to 0V and the -ve side to -155V, cutting off the tube grid which blanks the screen while the EHT collapses. This avoids burning to the tube phosphor which might otherwise occur due to the high intensity of the tube phosphor which might otherwise occur due to the high intensity spot caused by the collapse of the scan currents.

4.3 B+ Regulator

The horizontal scan coil has to be driven with a constant amplitude sawtooth current at all of the possible scan frequencies. This is done by controlling the B+ voltage applied to the scan coil. The B+ regulator is based on a step down switched-mode regulator comprising switching FET Q501, flywheel diode D503 and filter L501/CE504/CE517. When the FET is switched on, its source becomes connected to the +155 volt rail and the current in L501 increases.

After a period determined by control IC501, the FET is switched off and the current in L501 decays, flowing via D503 with the voltage at the FET source now falling to about -1 volt. The switched waveform at the source is smoothed by the filtering action of inductor L501 and the reservoir capacitors CE504 and CE517. The average voltage at the output of the filter, depends on the proportion of the time that the Q501 is 'on'. This is controlled by sampling the peak voltage appearing on pin 5 of the line output transformer T402. A rectified and smoothed sample is compared with the internal reference in IC501 and this controls the 'on' time of Q501. The current feedback signal required by IC501 is simulated by the circuit D505, R515, C510 and D506, which creates an artificial current ramp. The output of the IC501 drives output amplifier (TR502/TR503) which in turn drives a pulse transformer whose secondary winding is connected to Q501. The regulator is synchronized to the line timebase by the circuit around TR501. A pulse derived from pin 3 of T402 is fed to the timing capacitor C507 to trigger the oscillator in IC501. The regulator is inhibited from a full start-up until the line output has started working. TR504 is initially off and TR505 is on holding IC501, pin 1 low, reducing the regulator output to zero. Diode D504 allows the output of the B+ regulator to rise to 21 volts, enabling the line output to operate at low level. When the low level pulse output from pin 3 of T402 is present, it turns on TR504 releasing the over-ride in IC501 enabling the regulator to operate as normal.

4.4 Synchronization

The incoming horizontal and vertical sync. pulses are fed to IC630(74LS86) which has the function of providing positive sync. pulses to the line and field oscillators regardless of the polarity of the incoming pulses.

4.5 Automatic Height Adjustment

The sync. pulses are also applied to two dual monostables, IC631 & IC632 for the mode detection circuits. These monostables are set to $16\mu s$, $19\mu s$, $22\mu s$ and $30\mu s$. The monostables are constantly being re-triggered by the line sync. pulses. If the sync. pulse period is shorter than the monostable, it will never reset and the Q output will not go high. If the sync. pulse is longer than the monostable period, the Q output will go high at the end of the timed period until re-triggered by the next sync. pulse. The action of the monostables is shown in the following table:

Freq. kHz	Period µs	IC631/IC632 pins with +ve pulse
31.5 35-38 48 57 61	31.7 28.5-26.3 20.8 17.5 16.4	IC631 pins 5, 13: IC632 pins 5, 13 IC631 pins 5, 13: IC632 pin 5 IC631 pins 5, 13 IC631 pin 5 None of above

The outputs of the 16, 19 and $22\mu s$ monostables drive the inputs of inverters in IC634. 'High' outputs from the monostables will give a 'low' at the output of the inverter and discharge the capacitor at the output to give a sustained

The sync. pulses and the logic signals from the inverters are decoded in IC633 (quad 2 input NOR), IC635 and IC636 (2 to 4 line decoders). The outputs of the decoders are used to switch one of the bank of resistors R640-R663 across C645. This sets the current flowing in the constant current generator TR635/637/638, controlling the height of the vertical (field) scan to provide automatic height compensation for each mode.

4.6 Vertical Deflection

The incoming sync. signal locks the vertical oscillator to provide a steady, synchronized display. The series combination C311/312 charge from a current of 10201 min of 10201 synchronized display. The series combination C311/312 charge from a current out of IC301 pin 9, mirroring the current at pin 7 from TR637 to establish the correct height, producing a field rate ramp voltage on pin 9. The output current from pin 1 flows through the deflection coil CE309 and R307. Feedback from R307 into pin 12 is compared with this internally generated ramp to produce a linear scan.

Vertical shift is achieved by providing positive or negative offset current into the tube deflection coils using R697 and TR634, with RV684 providing user adjustment to centre the display.

4.7 Horizontal Deflection

The horizontal sync. pulse is used to synchronize a phase-lock loop (PLL), IC401, which controls the line output. A variable monostable (TR416/417/418) between the incoming sync. pulse and the PLL (IC401, pin 14) gives a variable delay to the timing pulse. A fixed monostable (TR419/420) is used to delay feedback from the line time-base to the PLL (IC401, pin 3). Altering the delay has the effect of shifting the display horizontally.

The output of the PLL (IC401, pin 4) drives TR423 switching current into the base of the line drive transistor TR422. IC402 is used to provide a start-up delay inhibiting the line driver stage until the PLL is functioning correctly. The output from TR422 is coupled to the line driver transformer than a coupled to the line driver transformer. output from TR422 is coupled to the line drive transformer whose secondary winding provides the high base current and controlled turn-off required by the line output transistor TR402.

The line output circuit is based on a diode modulator configuration. D403/C407 and D404/C409 form separate tuned flyback circuits with the horizontal deflection coil, and T403/C406 provides fine tuning and the voltage across C410 controls the scan width. In normal operation the voltage across C410 is a parabola with a positive d.c. offset voltage controlled by TR407.

PL406 allows a link selectable offset current to be added to the tube deflection coils to provide horizontal shift, RV414 provides adjustment.

The line output section also includes a beam current limit circuit connected to the contrast control wiper of RV681 so that the screen light output is automatically limited to prove the control wiper of RV681. automatically limited to prevent excessive brightness. The voltage on the wiper is pulled low by TR415 if the beam current exceeds an average of 0.5mA.

4.8 East/West, Keystone and Horizontal Shift

These functions are performed by IC302 and IC303 which control the diode modulator. IC302 generates a parabola from the field current ramp fed into pin 2. This is internally compared with a line rate sawtooth generated at pin 8. The output appears as a pulse-width modulated voltage at pin 5, which is filtered by R327 and C315. This is mixed with a DC control voltage from op-amp IC303 whose output drives the diode modulator controlling transistors TR406, TR426 and TR407.

4.9 Blanking

A pulse derived from the line output stage is applied to C420 giving a sharply defined positive trigger at the start of flyback. This triggers monostable TR/10/411 and gives a line flyback blanking signal. This is combined with a TR410/411 and gives a line flyback blanking signal. This is combined with a field blanking signal derived from the field output (IC301, pin 13) to give a mixed blanking signal which is fed via PL402 to TR210/211/212 on the video panel.

The monitor accepts RGB signals of 0.71V amplitude into 75 ohms. The incoming signals are capacitor coupled to the inputs of IC201. The function of IC201 is to provide gain control and clamping (d.c. restoration). The outputs at pins 16, 20 and 25 of IC201 directly drive emitter followers (e.g. TR205 for the red channel), and the output amplifier IC202 which provides the CRT cathodes with an AC coupled, 40V (maximum) drive. D.C. restoration is provided by D201/TR201 (red channel). The individual cut-off points for the tube are set by RV250/251/252.

4.11 Protection Circuits

4.11.1 Power Supply

The mains input fuse FS801 is of the time-delay type. Replacements must be of the same type and rating.

Immediately after the power supply has started D827 is triggered disabling the start-up circuit, so that in the event of a fault the power supply is prevented from attempting to re-start. For example, the power supply will shut down (after a single start attempt) if any output is

De-magnetization sensing is provided by D814 which feeds a voltage produced by the transformer secondary winding 5-6 into pin 3 of IC801. Diodes D815, D816 and D817 limit the input to 1.8V maximum. This signal is a representation of the transformer flux during the secondary discharge cycle and only when it has fallen to zero can the IC begin another cycle. This circuit is constantly active but normally only limits the flux during the start-up sequence, or in the event of overload, or input undervoltage.

Over-voltage protection has been included for fail-safe operation. The zener diode ZD806 conducts if the IC supply voltage derived from T801 exceeds 16V. This causes D807 to latch on, thus disabling IC801.

4.11.2 B+ Regulator

D504 conducts heavily in the event of the B+ rail being short circuited. This effectively shorts the 21V rail to ground and the power supply shuts down. TR504, by inhibiting the B+ regulator IC501, restricts the B+ voltage to 21V until the line output stage is functional.

4.11.3 Line Drive

FS401 protects against the possible failure of TR422, CE401 or T401.

4.11.4 Line Scan

The EHT is protected from exceeding a safe limit by monitoring the flyback pulse amplitude at pin 3, T402. If it exceeds 35Vpk, ZD420 conducts and triggers D421, simulating high H.T. and resulting in the power supply control IC801 reducing its output until its own supply is too low to function, thereby switching off the PSU. The circuit typically operates at an EHT of 28kV.

The maximum beam current is automatically limited by TR414/TR415 during normal monitor operation. In the event of a fault causing excessive current in the EHT winding, D421 is triggered by current from TR413 which shuts down the PSU as above.

If either the 12V or 5V rail is shorted, TR425 conducts and triggers D421, shutting down the power supply.

4.11.5 East/West

R411 gives protection to the E/W circuit and the diode modulator in the event of a fault.

SECTION 5. ADJUSTMENTS

5.1 Introduction

The monitor incorporates a number of adjustments which are listed below, and will have been correctly aligned before leaving the factory. Any adjustments should be made only if necessary.

5.1.1 Power Supply Panel:

155V	Adjust	RV840
------	--------	-------

5.1.2 Control Panel:

30µs		RV672
19µs		RV680
16µS		RV677
V.Size	Mode 8	RV658
V Size	adjust	RV628

5.1.3 Deflection Panel:

B+ Adjust	RV512
Phase Balance	RV401
H Shift	RV414
V Lin	RV316
V Freq	RV303
Keystone	RV324
Width Adjust	RV422
E/W Amplitude	RV331
A1/Screen voltage	FBT
Focus	FBT

5.1.4 Video Panel:

Red Gain	RV219
Green Gain	RV218
Blue Gain	RV217
Red Cut-off	RV250
Green Cut-off	RV251
Blue Cut-off	RV252

5.2 Adjustments

- N.B. 1. Clockwise (CW) and counter-clockwise (CCW) directions are viewed from component side.
 - 2. Where pots are set to CENTRAL, this need only be approximate.

- Signal source capable of producing the timing modes as described in 5.2.1 Section 1, item 1.2.
- D.V.M. (1000V).
- E.H.T. meter (30kV).
- Light meter (e.g. Minolta). 3.
- 20MHz oscilloscope. 5.

Initial Conditions 5.2.2

- Chassis assembly complete.

 E.H.T lead (poppy) safely terminated onto the CRT.

 Set user CONTRAST control (RV681) to MINIMUM.
- Set user BRIGHTNESS control (RV682) to DETENT. 3. 4.
- Set A1 to MINIMUM, (fully CCW).

 Video gain pots RV217, RV218, RV219 not to be adjusted from the setting made in the board test (i.e. 70% of maximum gain).

 Wideo cut off rate RV250 RV251 RV252 to be fully CW
- Video cut-off pots RV250, RV251, RV252 to be fully CW. 7.
- Connect signal lead to Chroma 2000.

 Locate SK714 in Service Mode (s/c PL704, pins 2/3 [where fitted]).
- 10. Connect A.C. power supply, 240V rms to inlet socket.
- 11. Switch A.C. power switch ON using S801.
- 12. Check green LED illuminates.

Voltage Settings 5.2.3

- Select mode 3, black video.

 Measure the voltage at cathode of D818 and adjust RV840 for a voltage of
- Measure the voltage at cathode of D504 and adjust RV512 (B+ Adj), for a voltage of +65.5V ±0.5V.

E.H.T. Voltage Measurement 5.2.4

Connect E.H.T. meter to poppy and check that the voltage is $24.5 \, \text{kV}$ $\pm 1 \, \text{kV}$. Note: Ground EHT meter to CRT Dag braid.

Focus Settings

- Set CONTRAST to CENTRAL and BRIGHTNESS to DETENT.

 Adjust focus control until thin black lines can be clearly defined. 2. Adjust for optimum focus.

Sync Decoding Logic Presets 5.2.6

- Connect scope probe to pin 13, IC631.

 Adjust RV672 (30µs) for a 1.30µs, ±0.1µs negative going pulse. 2. 3.
- Connect scope probe to pin 4, IC632.

 Adjust RV680 (19µs) for a 19µs, ±0.5µs negative going pulse. 4. 5.
- Connect scope probe to pin 12, IC632. Adjust RV677 (16µs) for a 16µs, ±0.5µs negative going pulse.

Horizontal Raster Centre 5.2.7

- 1.
- Set CONTRAST to CENTRAL and BRIGHTNESS to MAXIMUM. 2.
- With reference to pages 16/17. If necessary, adjust PL406 and RV414 (H.Shift) to centralize the RASTER. a-b <3mm. 3.

5.2.8 Modulator Setting

Select Mode 3, white video, no border.

Set RV683 (width) to DETENT.

Measure the voltage at junction of C410 and L404, adjust RV422 (WIDTH ADJ) for 5.0V, $\pm 0.1V$.

5.2.9 Horizontal Phase

Select Mode 3, 4 x 4 grid.

Set CONTRAST to MAXIMUM and BRIGHTNESS to DETENT.

Adjust RV686 (user H.PHASE) until the display is centralized. 3.

Select Mode 10, 4 x 4 grid. 4.

Adjust RV401 (PHASE BALANCE) fo a centralized display. 5.

Repeat steps 1 to 5 until the phase difference between modes 3 and 10 is less than 2mm, with reference to pages 16/17.

Horizontal Width {}TM5401 []TM5501 5.2.10

Select mode 10, white video, no border. Set CONTRAST to MAXIMUM and BRIGHTNESS to DETENT.

Set RV683 (WIDTH) to DETENT. 3.

Check the width is $\{260\,\mathrm{mm}\}$ [270mm] $\pm 1\,\mathrm{mm}$. Adjust RV512 (B+ ADJ) slightly if necessary.

5.2.11 Geometric Distortion

Select mode 10, 4 x 4 grid. Set CONTRAST to MAXIMUM and BRIGHTNESS to DETENT. 2.

3. Adjust RV331 (E/W AMP) for straight vertical edges.

Adjust RV324 (KEYSTONE) for parallel vertical edges, to give a rectangular display.

5.2.12 Re-check Line Adjustments

Repeat 5.2.9, 5.2.10 and 5.2.11 as necessary. Select Mode 3, black video. Check the B+ voltage is within the range $65.5 \pm 4V$. If not repeat 5.2.10.

5.2.13 User Width Distortion Check

Select Mode 10, white video, no border.

Adjust RV683 (WIDTH) to give minimum width.

Check there are no geometric distortions, with reference to pages 16/17. 3.

Should any be apparent, increase the width slightly using RV422 (WIDTH 4.

5.

Select Mode 3, white video, no border. Adjust RV683 (WIDTH) to give maximum width. 6.

Check there are no geometric distortions, with reference to pages 16/17. 7.

Should any be apparent, reduce the width slightly using RV422 (WIDTH 8. ADJ).

If RV422 has been adjusted repeat 5.2.10 to 5.2.13 to minimize geometric distortions.

5.2.14 Vertical Hold

Select a signal with a 50Hz field rate.

Set CONTRAST to MAXIMUM and BRIGHTNESS to DETENT.
Adjust RV303 (V.FREQ) fully CCW. Then slowly turn CW until the display becomes stable. STOP TURNING RV303 IMMEDIATELY.

5.2.15 Horizontal Linearity

- Select mode 10, crosshatch.
- Set CONTRAST to MAXIMUM and BRIGHTNESS to DETENT.
- Set RV683 (WIDTH) to DETENT.
- 16/17, measure the width of 3. squares above the horizontal centre line. The linearity error defined on reference to pages page 16/17 must be <5%.
- With reference to pages 16/17, measure the width of the squares above the horizontal centre line. The linearity error defined on page 16/17 must be <5%.

Vertical Linearity 5.2.16

- Select mode 10, 4 x 4 grid.
- Set CONTRAST to MAXIMUM and BRIGHTNESS to DETENT.
- With reference to pages 16/17, adjust RV316 (V.LIN) so that the difference in height between the smallest and largest square is <2.0mm. (Adjust for optimum linearity).

Vertical Centre 5.2.17

- Select mode 10, 4 x 4 grid. 1.
- Set CONTRAST to MAXIMUM and BRIGHTNESS to DETENT.
- Adjust RV684 (V.CENTRE). Check that the display can be shifted up and down.
- reference to pages 16/17 adjust RV684 (V.CENTRE) to centralize the display. c-d <4.0mm.

[]TM5501 {}TM5401 Vertical Size (Height) 5.2.18

- Select mode 10, white video, no border.
- Set CONTRAST to MAXIMUM and BRIGHTNESS to DETENT.
- Set RV685 (V.SIZE) to DETENT. 3.
- Adjust RV628 to give a display size of $\{190mm\}$ [202mm] $\pm 1mm$.
- Select Mode 8, white video, no border.
- Adjust RV658 for a display size of $\{190mm\}$ [202mm] $\pm 3mm$.
- Select in turn Modes 1, 2, 3, 4, 5, 6, 7, 9, and 11, white video, no border. In each case check that the display is locked to the vertical 7. sync. and that the height is {190mm} [202mm] ±3mm. If not, readjust RV628 and check height is within specification for all modes (1-
- Select IBM 8514/A interlaced mode, white video, no border. Check that height can be adjusted to nominal $\{190mm\}$ [202mm] $\pm 3mm$ with user 8 control RV685. Then display text (to include circular characters) and check picture quality.
- Repeat 5.2.16, 5.2.17 and 5.2.18 as necessary. 9

5.2.19 White Balance

- Set CONTRAST to MINIMUM and BRIGHTNESS to MAXIMUM.
- 2. Select mode 10, black video.
- 3. Using a Minolta light meter, adjust A1 until a light output of (Y) 3 ± 0.5 Cd/m^2 is obtained.
- 4. Observe the readings of (x) and (y) and adjust a maximum of two CUT-OFF pots from RV250 (RED), RV251 (GREEN) and RV252 (BLUE) to obtain (x) = 0.281 ±0.005 and (y) = 0.311 ±0.005.
- 5. Check (Y) is still 3 ± 0.5 Cd/m². Adjust A1 if necessary.
- 6. Set user BRIGHTNESS to DETENT. Check no raster is visible.
- 7. Set user CONTRAST to MAXIMUM.
- 8. Select Mode 10, green block.
- 9. Adjust RV218 (GREEN) to give a (Y) reading of 97 ± 1 Cd/m².
- 10. Select Mode 10, white block.
- 11. Using Minolta light meter,

Adjust RV217 (BLUE) to give $(y) = 0.311 \pm 0.005$ Adjust RV219 (RED) to give $(x) = 0.281 \pm 0.005$

Note: Record (x) and (y) readings.

- 12. Check (Y) reading is $120 \pm 10 \text{Cd/m}^2$.
- 13. Adjust CONTRAST control to give a (Y) reading of 17 ± 3 Cd/m². Check that (x) and (y) are within ± 0.015 of values noted in 11. If out of spec. adjust only the two CUT-OFF pots adjusted in 4 to correct (x) and (y).
- 14. Repeat 1 to 13 until no further adjustment is required.

5.2.20 Static Convergence

- 1. Select mode 3, crosshatch.
- 2. Set CONTRAST to MAXIMUM and BRIGHTNESS to DETENT.
- 3. With reference to pages 16/17, check that the convergence error of Zone A is <0.30mm and Zone B <0.50mm. If errors are outside these limits, then the convergence rings on the neck of the CRT are to be carefully adjusted.

5.2.21 Purity

- Set CONTRAST to MAXIMUM and BRIGHTNESS to DETENT.
- 2. Select mode 3, RED video.
- 3. Check for any discoloration of the Red raster.
- 4. Select mode 3, GREEN video.
- 5. Check for any discoloration of the Green raster.
- 6. Select mode 3, BLUE video.
- 7. Check for any discoloration of the Blue raster.

5.2.22 Picture Quality Check

- 1. Select mode 8, Text pattern (white text).
- 2. Set CONTRAST to CENTRAL and BRIGHTNESS to DETENT.
- Check that text is clearly legible.
- 4. Check that text is free from smearing/streaking effect.
- 5. Check that text is black and white, especially vertical lines.

5.2.23 Power Management Control Circuit (where fitted)

Check SK714 is in operational mode (s/c PL704 pins 1-2 where fitted).
 Switch off line sync. source.

Connect x10 oscilloscope probe to Q701 source.

Connect power and check that the voltage rises to 21V for approximately one second and falls to $7.5V \pm 1.0V$.

Note: With signal lead disconnected, 'noise' received by the sync. i/p's may occasionally trigger the monitor into operational mode.

- 2. Check that the voltage at TR701 collector is <0.1V.
- 3. Check that the voltage at TR702 collector is <0.1V.
- 4. Check that the voltage at Q702 source is: For Hitachi 36cm picture tube 5.0V ±0.2V. For Toshiba 36cm picture tube 4.0V ±0.15V. For Chungwa 34cm picture tube 4.2V ±0.2V.
- Switch off field sync. pulses. Switch on line sync. pulses. Check that the voltage at TR701 collector is <0.1V.</p>
- Swith on field sync. pulses.
 Observe that the picture is displayed within one second.
- 7. Check that the voltage at TR701 is 25.5V to 29.0V.
- 8. Check that the volyage between Q701 source and drain is <0.15V
- 9. Check that the voltage between Q702 source and drain is <0.1V
- 10. Check that the voltage at Q702 is $6.3V \pm 0.15V$.
- 11. Remove line sync. pulses. Observe that display blanks after approximately one second with no evidence of scan collapse which may result in spot burn damage to the picture tube.

5.2.24 Dual Voltage Version (240/110V)

Note: This section applies to dual voltage models only. For 220-240V models disregard this section.

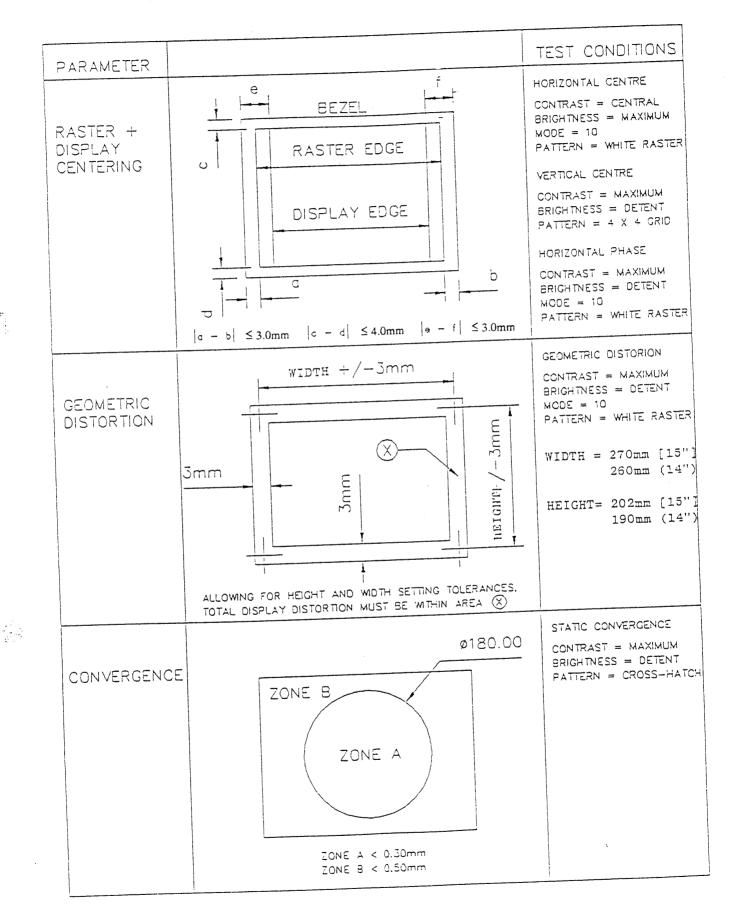
- 1. Disconnect 240V A.C. power supply.
- 2. Switch to 110V operation (SK802 s/c).
- 3. Connect 110V A.C. power supply.
- Switch A.C. power ON using \$801.
- 5. Set CONTRAST to MAXIMUM and BRIGHTNESS to DETENT.
- 6. Select mode 3, white video, no border.
- 7. Check that display is normal.
- 8. Switch off S801 and disconnect the 110V A.C. power supply.
- 9. Switch back to 220-240V operation (SK802 o/c).
- 10. Connect 240V A.C. power supply.
- 11. Switch ON A.C. power using \$801.
- 12. Check that display is normal.

SAFETY TEST (POWER DISCONNECTED, SWITCH S801 ON)

SAFETY TEST MUST BE CARRIED OUT ON ALL MONITORS. A CHASSIS FAILING IN 5.2.25 ANY PART OF THIS SECTION (5.2.23) MUST BE REJECTED. Note:

- CHECK LEAKAGE FROM LIVE AND NEUTRAL TO THE PROTECTIVE EARTH. LEAKAGE CURRENT MUST NOT EXCEED 3.5mA (EN 60950 Paragraph 5.2.3 reference to appendix C). TYPICAL READING 0.5mA
- THE RESISTANCE OF THE SIGNAL CABLE SHELL TO THE PROTECTIVE EARTH MUST NOT 2. EXCEED 0.1 Ohms.
- HAVING CONNECTED TOGETHER LIVE AND NEUTRAL, A BREAKDOWN TEST OF 2120V D.C. APPLIED BETWEEN LIVE/NEUTRAL AND EARTH MUST NOT RESULT IN AN INSULATION FAILURE OR BREAKDOWN.

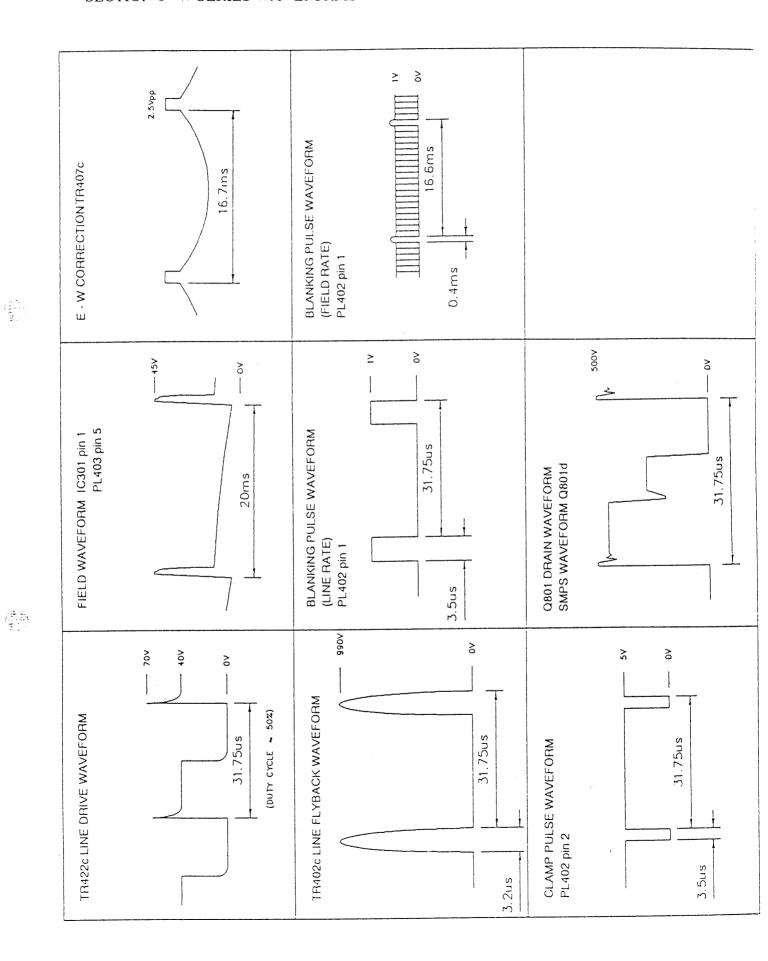
W Series Spec/Limits



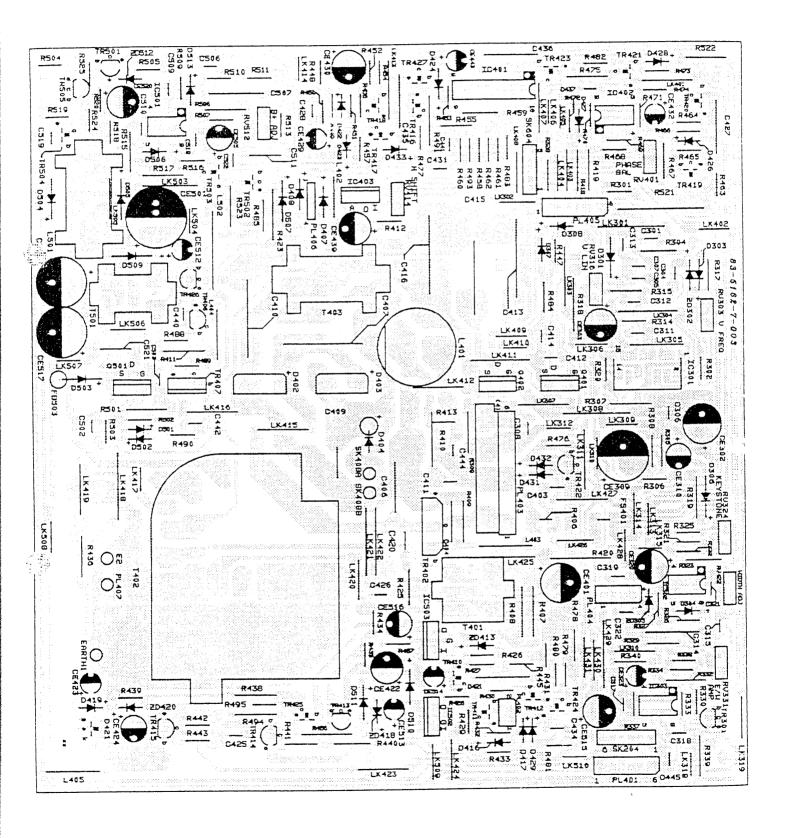
W Series Spec/Limits

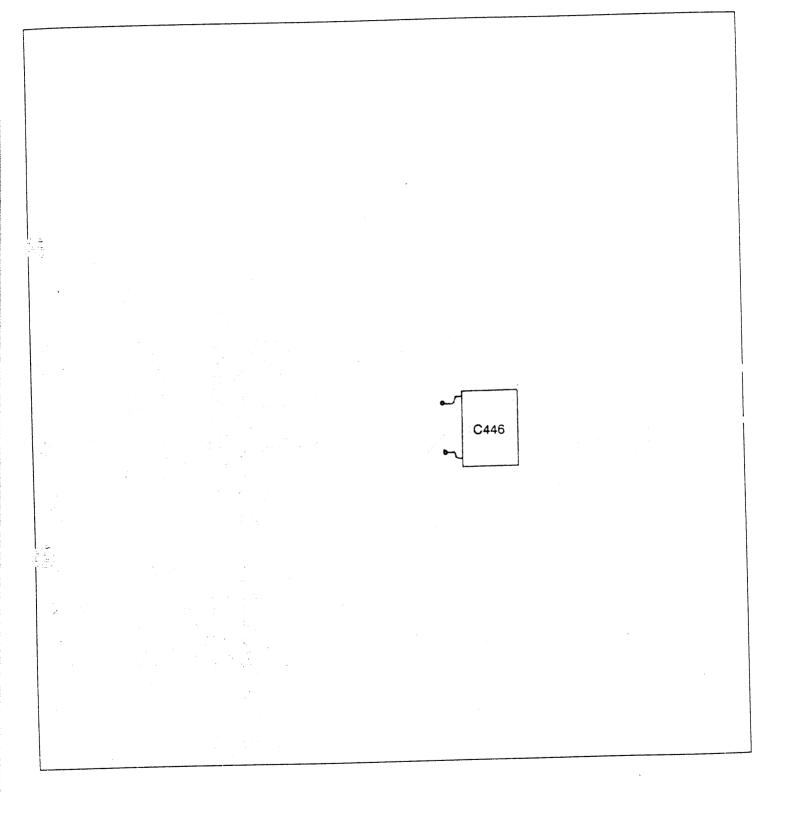
PARAMETER		
TAKAMETER		TEST CONDITIONS
LINEARITY	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	TEST CONDITIONS VERTICAL LINEARITY CONTRAST = MAXIMUM BRIGHTNESS = DETENT MODE = 10 PATTERN = 4 X 4 GRID HORIZONTAL LINEAP' CONTRAST = MAXIMUM BRIGHTNESS = DETENT MODE = 10 PATTERN = CROSS-HATCH
	<pre>(Y - Yi) ≤ 0.05 for i = 1 to 4</pre>	

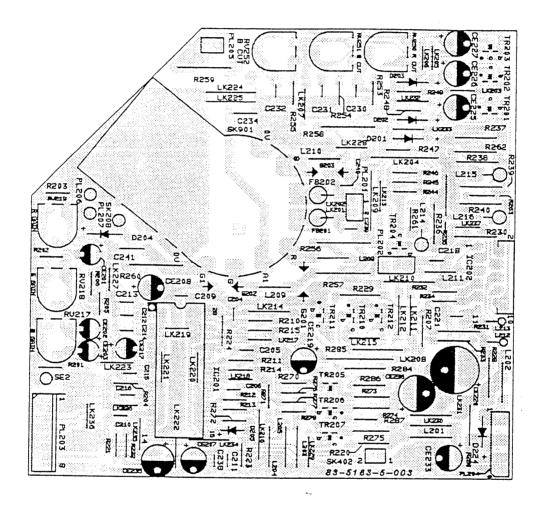
SECTION 6 W SERIES WAVEFORMS



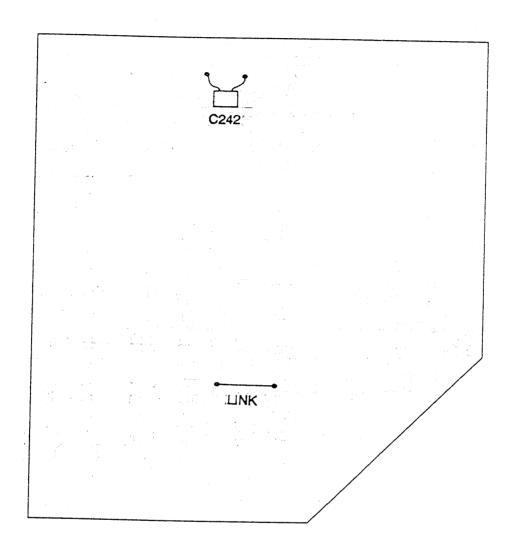
SECTION 7 PCB COMPONENT LAYOUT DEFLECTION PANEL (Component side)



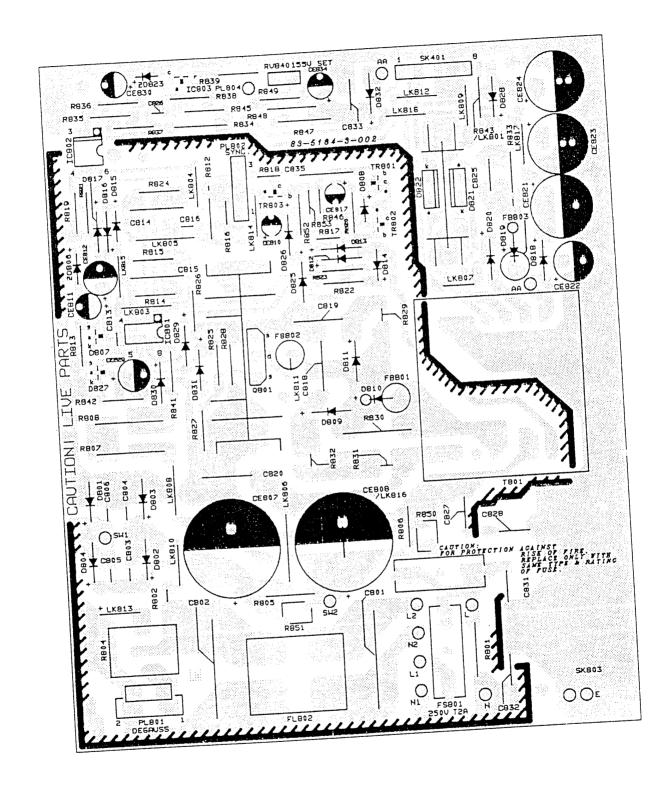




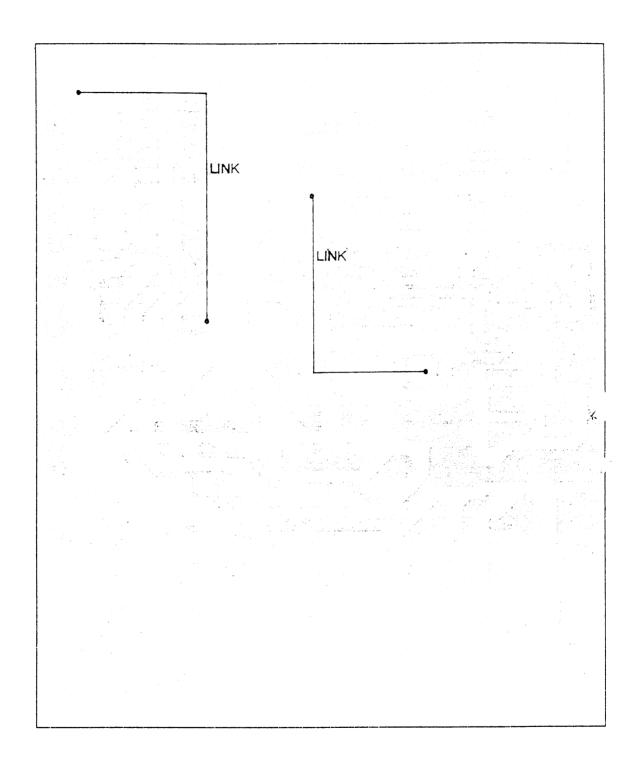
VIDEO PANEL (Track side)



POWER SUPPLY PANEL (Component side)

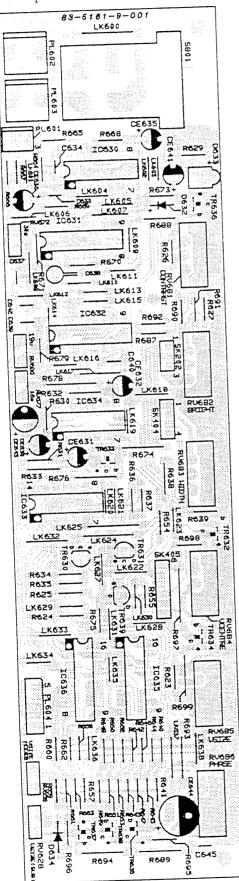


1: 11 4: 45

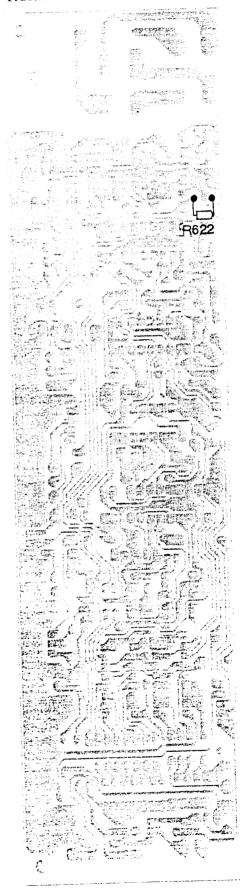


CONTROL PANEL

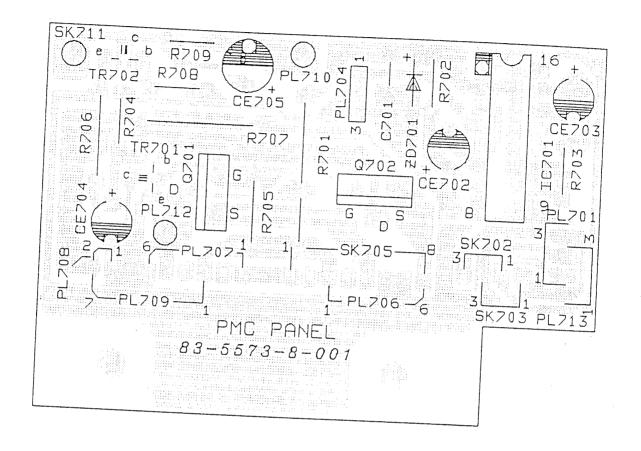
Component side



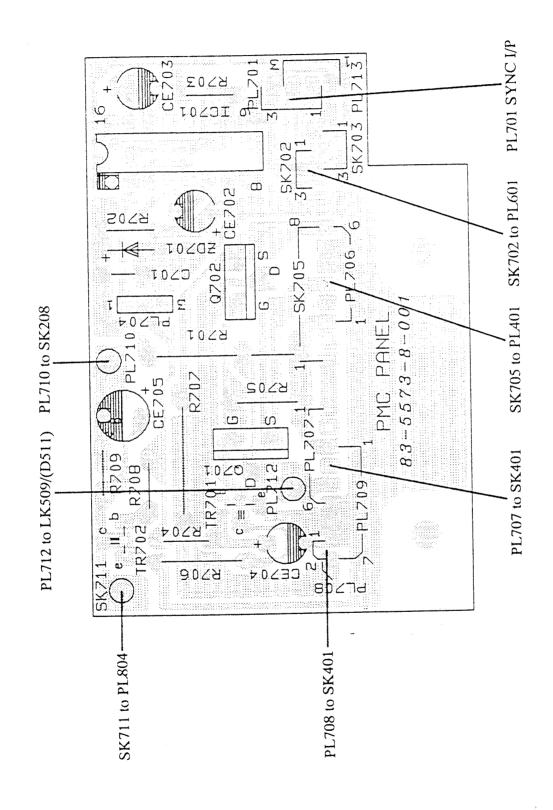
Track side



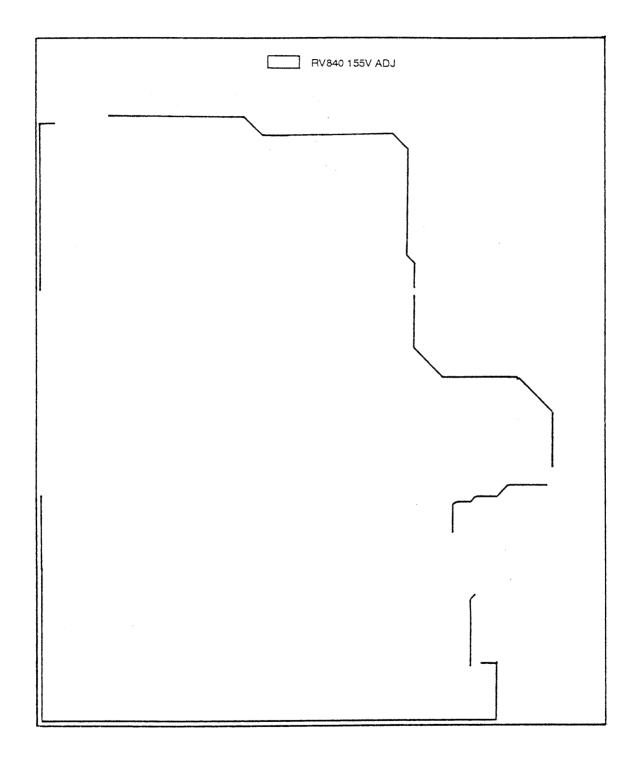
POWER MANAGEMENT PANEL (Component side)

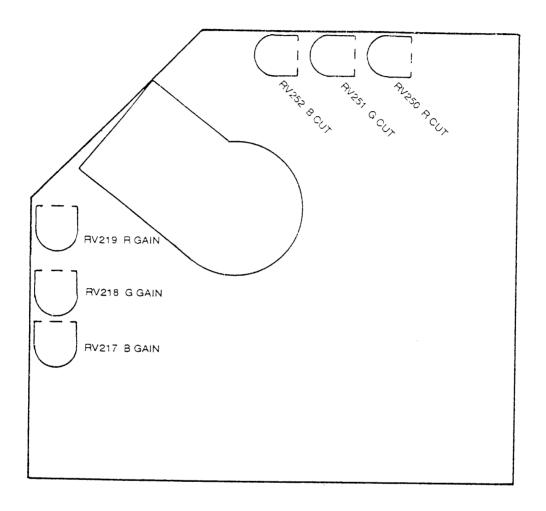


POWER MANAGEMENT PANEL INTERCONNECTIONS

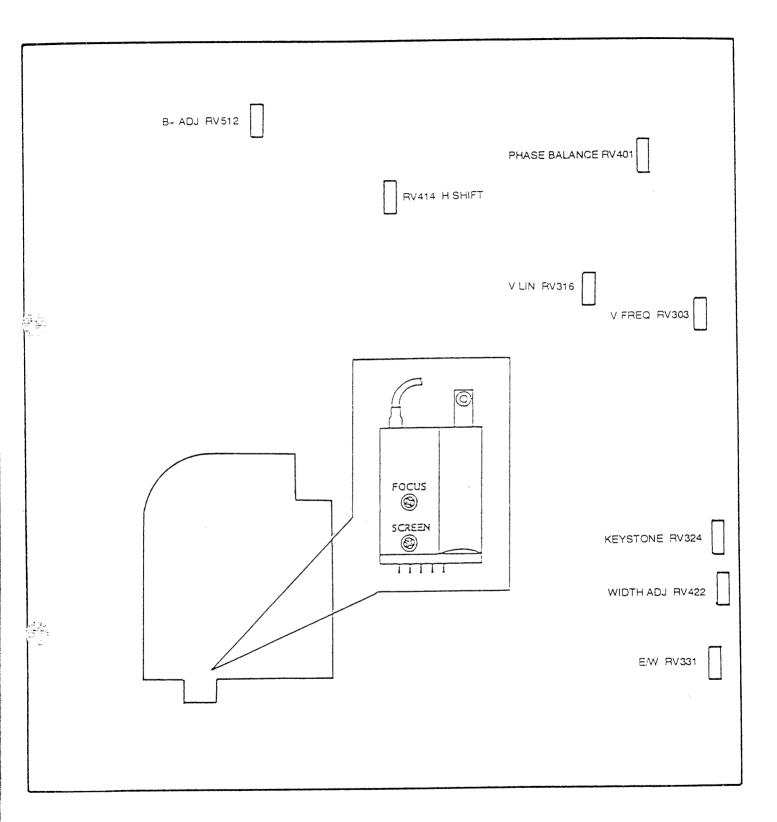


SECTION 8 ADJUSTMENTS POWER SUPPLY PANEL





DEFLECTION PANEL



RV672 30µs	RV681 CONTRAST
RV 680 19µs RV 677 16µs	RV682 BRIGHTNESS
	RV683 WIDTH
	RV684 V.CENTRE
	RV685 V.SIZE
RV658 V.SIZE MODE 8 RV628 V.SIZE ADJ.	RV686 PHASE

SECTION 9 LIST OF COMPONENTS

9.1 RESISTORS

Components which are marked \triangle in the Parts List and on the circuit diagram are Safety Approved types and should be replaced only with components supplied or approved by the manufacturer's Service Department. It is also recommended that components not marked with the safety symbol should be replaced with parts of the type originally fitted. This applies particularly to those resistors which are stood off the printed circuit board.

The manufacturer reserves the right to modify the design of the product, and to use or supply such alternative components as may be deemed necessary.

Not all components are fitted to every chassis variant. Always replace with the same type as originally fitted or an alternative approved by the manufacturer's Service Department.

The majority of resistors are either 0.125W, 0.25W, 0.4W, 0.5W or 0.6W, 5% or 10% standard carbon or metal film types which are readily available from most component stockists. Replacements should be of the same rating and tolerance as the originals. Refer to the appropriate circuit diagram for values.

The resistors listed below should only be replaced with components approved or supplied by TATUNG Service Department.

Legend of the symbols preceding some components in this parts listing:

110/240V switchable version - N3 for America.

 Σ 240V only version - N1 for Europe.

 θ Fitted on 300mA heater current Toshiba tubes.

	U	Titted on soons			
Cct Ref. R223 R224,	Value 2K2 12K 4R7 100R 1K 220R 220R 470R 1K 150R 11K 39R 82R 270K 100K 220K 33K 2K5 1K 18K 0R68 24K 39K 62K 39K 62K 31K 18K 100K 11K 11K 11K 11K 11K 11K 11K 11K 1	Tol% 1 15 10 10 55 51 10 55 51 11 55 55 55 55 51 11 11 11 11 11	R DUAL PC R DUAL PT /L140LA2 PST MIN PST MIN PST MIN	C 18R N N	Part Number 11-5657-8 11-5657-8 11-5291-2 11-5452-4 11-2621-0 11-5424-9 11-5542-3 11-2715-2 11-5213-0 11-5223-8 11-5659-4 11-5659-4 11-5658-6 11-5319-6 11-5266-1 11-3377-2 11-5661-6 11-5580-6 11-5576-8 11-5576-8 11-5623-3 11-5655-1 11-5623-3 11-5609-8 11-5609-8 11-5609-8 11-5609-8 11-5609-9 11-5613-6 11-4292-5 11-5688-8 11-5529-6 11-5688-8 11-5529-6 11-5633-8 11-5643-8 11-5531-8 11-5643-8 11-5531-8 11-5643-8 12-3170-7 12-3183-9 12-3199-5 12-3202-9
RV316	100K	POT LIN 25%	B PST MI	N	12-3202-7

θ

Cct Ref. RV324,401,672,	Value	Tol%	Wattage Type	Part Number
RV677,680	4K7	POT LIN 25%	PST MIN	12-3204-5
RV414	100R	POT LIN 25%	PST MIN 0.1W	12-4585-6
RV422	22K	POT LIN 25%	PST MIN	12-4588-0
RV512	1K	POT LIN 25%	PST MIN	12-4582-1
RV840	470R	POT LIN 25%	PST MIN	12-3211-8
RV628	47K	POT LIN 25%	PST MIN	12-3205-3
RV681,684,686	10K	POT LIN 20% F	R121V0AF15B103	12-3215-0
RV682,683,685	100K	POT LIN 20% F	R121V1AF15B104	12-3206-1

9.2 Capacitors

Many of the capacitors are common usage components and can be obtained from most electronic component stockists. Refer to the circuit diagram for values. Replacements must be of the same tolerance and rating as the originals.

Capacitor types are identified according to the following codes:

C - Ceramic; CD - Ceramic Disc; CP - Ceramic Plate; E - Electrolytic; MP - Metallised Polyester; P - Polystyrene; PP - Polypropylene; TC - Tubular Ceramic.

Cct Ref. C210 C221,230-232,440,	Value 10n	Tol% -20+80	Voltage 63	Type CP	Part Number 14-6892-8
C308 C234,241 C242,445 C315 C317,816,301,306 C318	10n 10n 100n 10n 4n7 220p	20 -20+50 5 10 10 2	400 2kV 100 400 100 50	MP CD CP MP CP CP N470	14-6838-3 14-6954-1 14-7221-6 14-6994-0 14-6894-4 14-7092-2
C321,428,522,634 C322,519 C403 C411 C420	1n 10n 15n 1n 56p	10 -20+80 20 20 5	100 63 400 400 2kV	CP CP MP CD CP	14-6934-7 14-6892-8 14-6933-9 14-4320-8 14-7215-1
C425 C441 C442,826 C506 C510,637,639,640	10n 470p 22n 3n3 10n	-20+50 10 10 10 5	100 100 250 100 400	CP CP MP CP MP	14-6903-7 14-6940-1 14-6877-4 14-6998-3
C521,833 C314 C406 C407	100n 180p 470p 5n6	5 20 1 5 5 5	250 630 1k5 2kV	MP P PP PP	14-6842-1 14-4386-0 14-6014-5 14-6985-1 14-7222-4
C409 C413 C415 C416 C421,427	15n 390n 1µ 680n 470p	5 5 5 5	630 250 250 250 250 630	PP PP PP PP P	14-7237-2 14-7241-0 14-7225-9 14-7236-4 14-6999-1
C435 C436,835 C444 C446 C507,814,815,638	120p 1n0 220p 2n2 3n3	1 1 20 5 1	630 250 400 1600 160	P P CD P	14-4917-6 14-7003-5 14-4537-5 14-7248-8 14-4849-8
C511 C813 A C801 A C802 A C806 C818 C819 C820	100n 470p 1 μ 470n 1n 33n 680p 100n	20 2 20 20 20 10 5	100 100 250VAC 250VAC 250 1kV 1K6	MP CP N1500 MAINS CLASS X2 C PP PP MP	14-6864-2 14-7045-0 14-6919-3 14-7029-9 14-6937-1 14-6991-6 14-7077-9
C820 C825 △C827,828,831,832 CE225-227,834,705 CE220 CE236 CE501 CE504,517	100n 22n 4n7 1 μ 47 μ 10 μ 47 μ 33 μ	20 20 20 20 20 20 20 20 20 20	400 400 250VAC 160 160 250 DC L 160 105C	MP CLASS Y ELECT ELECT ELECT OW ESR	14-5003-4 14-6981-9 14-7209-7 14-7212-7 14-7120-1 14-7044-2 14-6992-4 14-7205-4

Cct Ref.	Value Tol%	Voltage Type	Part Number
CE807	100μ 20	400 105C ELECT	14-7079-5 Σ
CE807.808	220 <i>u</i> 20	200 105C ELECT	14-7142-2 *
CE821	100μ 20	160 10CC ELECT	14-7144-9
CE822	47μ 20	100 105C ELECT	14-7201-1
CE823	$2200\mu - 20$	25 105C ELECT	14-7204-6
CF824	$1000\mu - 20$	25 105C ELECT	14-7210-0

9.3 Integrated Circuits

Cct Ref.	Description	Part Number
IC201	LM1203N NAT SEMI	19-8589-2
IC202	LM2416T NAT SEMI	19-8587-6
IC301	TDA1675A SGS THOMSON	19-8480-2
IC302	TDA4950 SGS THOMSON (14" Tubes)	19-8660-0
IC302	TDA8145 SGS THOMSON (15" Tubes)	19-8698-8
IC303,402	LM358N NAT SEMI	19-8356-3
IC401	4046B MOTOROLA	19-8597-3
IC403	LM317T NAT SEMI	19-8264-8
IC501	UC3842AN UNITRODE	19-8696-1
IC502	REGULATOR +12V, 0.5A, TO220 MOTOROLA,	
	SGS, NAT SEMI	19-8398-9
IC503	REGULATOR +5V, 0.5A, TO220 MOTOROLA	19-8333-4
IC630	74LS86 MOTOROLA	19-8008-4
IC631,632,701	74LS123 MOTOROLA	19-8039-4
IC633	74LS02 MOTOROLA	19-8006-8
IC634	74LS05N NAT SEMI	19-8648-1
IC635,636	74LS156 MOTOROLA	19-8535-3
IC801	UC3842AN UNITRODE	19-8696-1
<u> </u>	CNY17-2ZW QUAL TECH	19-8644-9
<u> </u>	Alternative TLP731(D4)-LF2	19-8690-2
IC803	REGULATOR TL431CLPR TEXAS	19-8584-1

9.4 Transistors

Cct Ref. TR201-203 TR204,702 TR205-207, TR406,410,411, TR413,414,416, TR417,419-421, TR423,425,426, TR503,801-803,	Description BF423 PHILIPS BF422 PHILIPS	Part Number 19-8293-1 19-8150-1
TR636 TR210-212,412 TR301, TR630-635,	2SA1015Y PNP TOSHIBA PH2369 PHILIPS	19-8591-4 19-8324-5
TR637-639 TR415,418,427, TR501,502,504,	2SC1815-BL NPN TOSHIBA	19-8646-5
TR505,701	2SC1815Y NPN TOSHIBA	19-8590-6
TR422	2SD667C HITACHI	19-8601-5 19-7857-8
TR407 TR402	TIP31C TEXAS,SGS,HARRIS 2SC3884A TOSHIBA	19-7657 - 6 19-8467-5
O501	FET 2SK526	19-8705-4
Q401,402	FET 2SK1035 12A150V MATSUSHITA ELECTRIC FET 2SK525 10A 150V TOSHIBA	19-8602-3 or 19-8704-6
Q701,702	BUK452-60B	19-8710-0
Q801	FET 2SK1649 TOSHIBA	19-8647-3

9.5 Coils and Chokes

Cct Ref.	Description	Part Number
L201,202,402	CHOKE 33µH 10% LALO3 TAIYO YUDEN	15-7818 - 9
L203-205	CHOKE 242H 10%	15-7800-6
L208-210	CHOKE 150nH 10%	15-7692-5
L211-213	CHOKE 680nH 10%	15-7786-7
L214-216	CHOKE 1µH 10% LAL04 TAIYO YUDEN	15-7842-1
L403	CHOKE 1'µH 10%	15-7726-3

Cct Ref. L401	Description HORIZONTAL LINEARITY COIL, DELTA	Part Number 15-7830-8 or
L401 L404	HORIZONTAL LINEARITY COIL, JET SIGNAL	15-7804-9 15-7723-9 87-0229-2-001
L501 L502	CHOKE 100MH 10% CHOKE 3mH 10% 1A, KV ELECTRONICS CHOKE 10MH (HIGH CURRENT)	85-1753-3 87-0235-7-001
<u>1.901</u>	DEGAUSSING COIL	87-0233-7-001
9.6 Diodes		Part Number
Cct Ref. D201-203,505	Description 1SS83 HITACHI	19-8595-7
D204,224,301,	10000	
D504,509-511 D825,826,828	1N4003	19-8346-6
D205,303,304, D306-308,416,		
D417,419,422, D423,426-428,		
D424,431,432 D433,501,		
D502,506,808, D814-817,830,		19-3992-0
D632,634,513 D407,408,507,	1N4148	
D812,813	RGP10D GEN INST THYRISTOR TICP106D TEXAS	19-8603-1 19-8544-2
D421,807,827 D404	UF5404 3A 400V 50ns GEN INST	19-8606-6 19-8331-8
D503 D402,403	RPG10J GEN INST 5A 1600V 5THZ52 TOSHIBA	19-8650-3 19-8144-7
D801-804 D809,811,818,	BY133GP GEN INST	19-8340-7
D810 D820,832,819	RGP15M GEN INST RGP10M GEN INST	19-5135-1 19-8310-5
D821,822 D633	BY229-200 PHILIPS L.E.D GREEN 90 5mm LEG	19-8505-1 19-8598-1
ZD302,413 ZD305	ZENER 2V7 500mW ZENER C9V1 5% 345mW V/REG	19-4033-3 19-4528-9
ZD418 ZD420	ZENER C6V8 5% 345mW V/REG 7FNER C33V 5% 345mW V/REG	19-8531-0
ZD512	ZENER C4V7 5% 350mW V/REG ZENER C5V1 5% 345mW V/REG	19-8032-7 19-6295-7
ZD701 ZD806	ZENER C16V 5% 345mW V/REG ZENER C12V 5% 350mW V/REG	19-4517-3 19-5070-3
ZD823		
9.7 Transforme		Part Number
Cct Ref. T401	Description LINE DRIVE	87-0234-9-001 15-7794-8-004 or
⚠ T402	FLYBACK MURATA FLYBACK SANYO	15-7792-1-004 87-0223-3-003
T403 T501	DIODE MODULATOR GATE DRIVE	87-0233-0-001 87-0217-9-002
<u>↑</u> T801	SWITCHED MODE	8720217-7-002
9.8 Crystals, Fi	ilters, Delay Lines	Part Number
Cct Ref. ⚠ FL802	Description FILTER TLF-011 SHINING YUAN	15-7688-7
9.9 Wiring Ass	semblies	Part Number
Cct Ref. ∧ SK901	Description CRT BASE SKT CVT3240-1251 SMK	25-2070-2
SK402	WIRING HARNESS BKG/CMP WIRE G1 BIAS	83-5207-0-002 83-5320-4-002
SK208 SK406,714	SHUNT TYPE 9610-102-10 METHODE WIRING HARNESS, POWER VIDEO	22-8203-8 83-5206-2-002
SK204 SK604	WIRING HARNESS, SYNC DECODER	83-5201-1-003 83-5205-4-002
SK401 SK602,603	WIRING HARNESS, POWER DEFL. WIRING HARNESS, AC POWER	83-5209-7-002
,		

Cct Ref. SK202 SK404 SK405 SK409 SK702 SK707 SK711 SK712	Description WIRING HARNESS, BRI/CONT WIRING HARNESS, WIDTH WIRING HARNESS, S-CAP WIRE OVP DRIVE WIRING HARNESS, SYNC. FEED WIRING HARNESS, POWER DEFLECTION WIRING HARNESS, POWER SOURCE WIRING HARNESS, SPOT SUPPRESSION WIRING HARNESS, 12V REGULATOR	Part Number 83-5208-9-003 83-5204-6-003 83-5203-8-002 83-5711-0-001 83-5588-6-001 83-5589-4-001 83-5591-6-001 83-5590-8-001
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9.10 Connectors

,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		TO A DY House
Cct Ref. PL201,402,708 PL202 PL203	Description 2 WAY PLUG 3 WAY PLUG 8 WAY PLUG JAE	Part Number 22-8386-7 22-8372-7 22-8390-5
PL204,401, PL405,707 PL205	6 WAY PLUG 5267-06NA MOLEX 1 WAY PLUG B1P-LV-TN-JST	22-8352-2 22-8280-1
PL206,207,407, PL409,804,710,712 PL403 PL404 PL406 PL408 PL601,701 PL602,603 PL604 PL704 PL801 PL802	PCB PIN 2.36mm MOLEX 16-06-0005 BASE OF PIN 6P. HBR-206C 4 WAY PLUG 5267-04 4 WAY PLUG 22-03-2041 MOLEX WIRE LINK 3 WAY PLUG JAE 2 WAY PLUG 5289 4 WAY PLUG 5267-05 3 WAY PLUG 2 WAY PLUS BASE 3 WAY PLUG RTB-1.5-3 JST	22-8421-9 22-8255-0 22-8363-8 22-8210-0 83-5517-7-001 22-8391-3 22-8402-2 22-8364-6 22-8076-0 22-8423-5 22-8279-8

9.11 CRT

Description 14" M34AFA60X33 CHUNGHWA STANDARD 14" M34AFA80X33 CHUNGHWA ANTI-STATIC 15" M36KLH80X17(DH) HITACHI NORMAL 15" M36KLH80X17(YH) HITACHI NORMAL 15" M36KLH680X18 HITACHI VLMF/ANTI-STATIC 15" M36KPN26XX04 TOSHIBA NORMAL	Part Number 18-1040-5 18-1049-9 18-1058-8 18-1063-4 18-1056-1 18-1055-3
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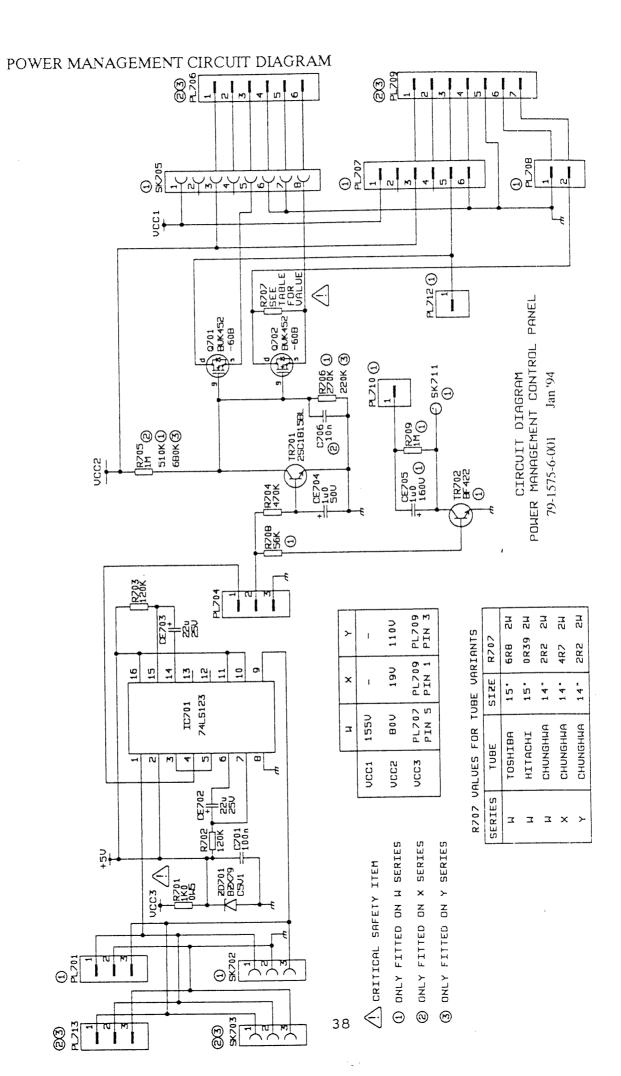
9.12 Miscellaneous

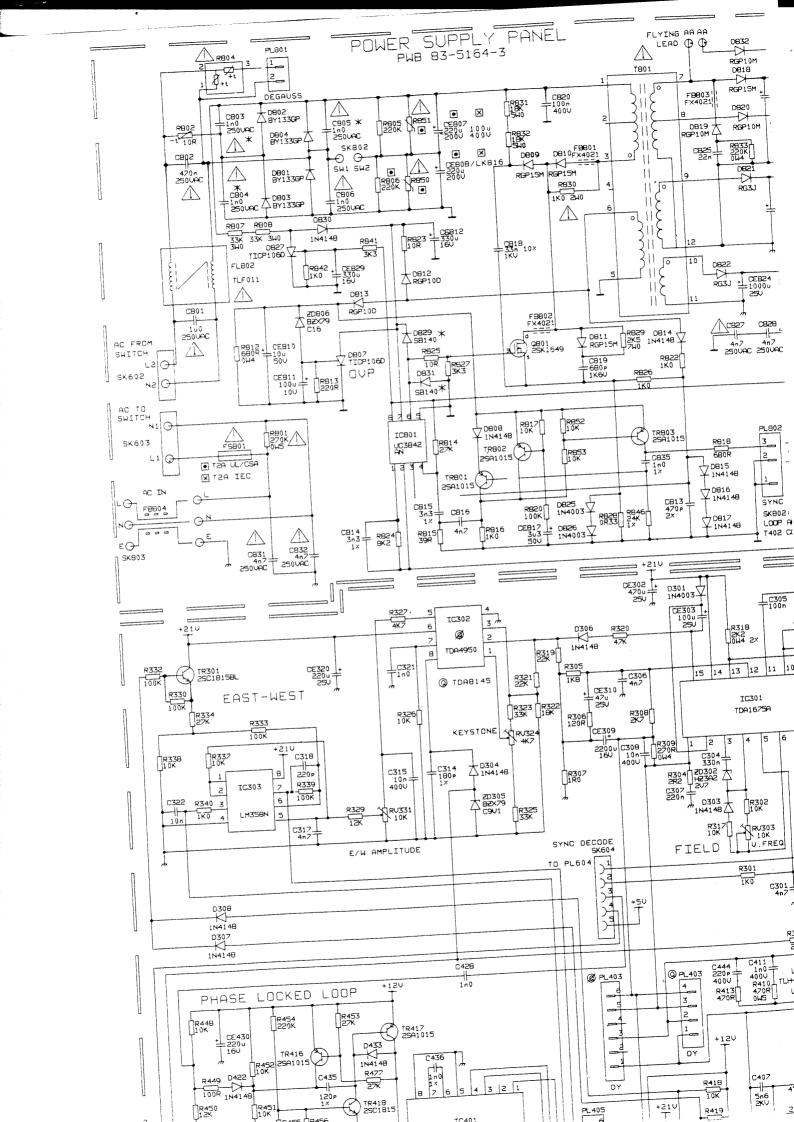
Cct Ref. △ SE2	Description LEAD PRIMARY EARTH (MOLEX) LEAD EARTH W SERIES FUSE, T500mA TR5-T 19372/K LEAD, E2 (MOLEX) FUSE HOLDER DE611/01 FUSE, TIMELAG 2A CERAMIC LEAD, PRIMARY EARTH SWITCH, 2 POLE PUSH-PUSH POWER ALTERNATIVE, 2 POLE MAINS DPST SPARK GAP DSP-201M-A11F MITSUBISHI ★ KNOB, V/R ABS SIGNAL LEAD COVER ESCUTCHEON BRAID EMC SWIVEL WASHER RUBBER FOOT (WHITE) ★ MAINS CORDSET IEC320, 10A, 1 METRE ★ POWER CORDSET UL SWITCH SLIDE SPDT ★ CHASSIS RUNNER L.H MX3 ABS ★ CHASSIS RUNNER L.H ABS	Part Number 83-5273-9-001 83-5651-3-001 21-3713-5 83-5168-6-001 21-3712-7 21-3685-6 83-5686-6-001 20-4091-3 20-4089-1 21-3727-5-26 83-4008-0-002 83-5260-7-001 83-4306-3/130100 83-5514-2-001 83-4148-6-003 83-5020-5-002 22-8366-2 22-8355-7 20-4090-5 83-4054-4-001 83-4005-6-003 83-4916-9-001
	⚠ PCB SUPPORT BRACKET MX3 ABS	83-4916-9-001

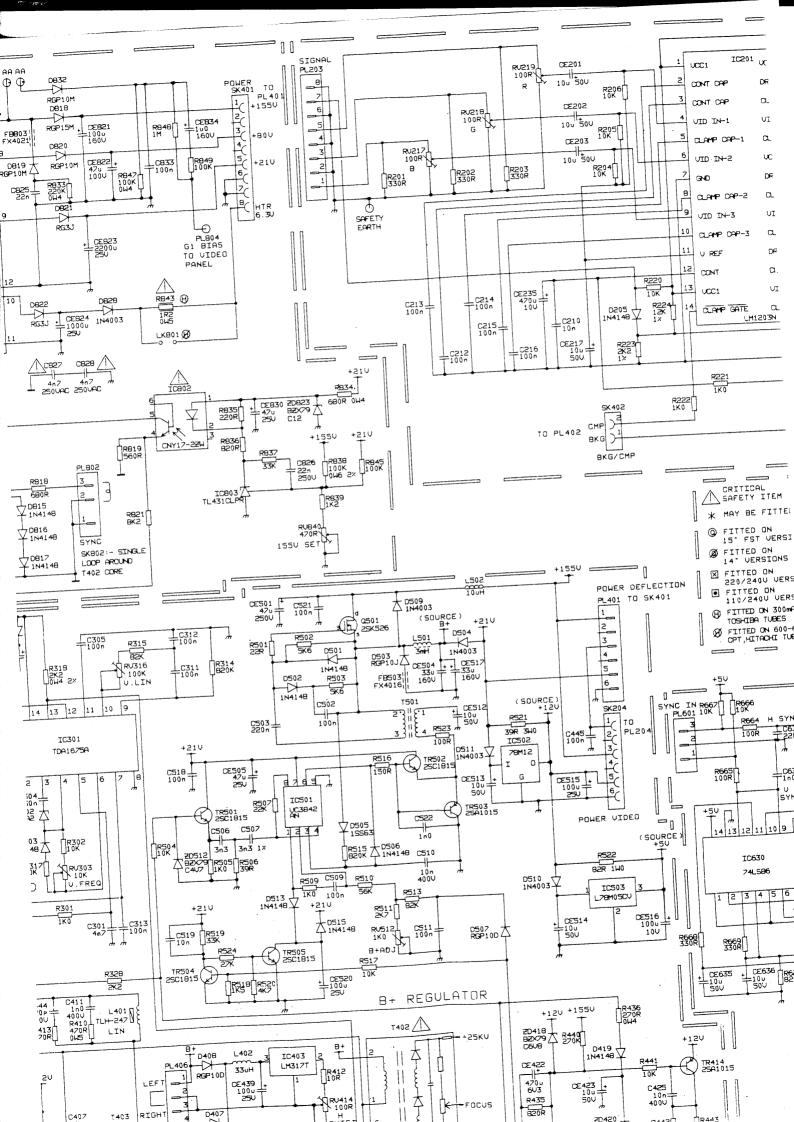
		70 - A NY - 1
Cct Ref.	Description	Part Number
(V/VA MODELS)	COIL, MAGNETIC FIELD CANCELLING	83-5323-9-002
(V/VA MODELS)	LEAD DAG COAT EARTH	83-5185-6-003
(V/VA MODELS)	SPRING CONNECTOR DAG COATING	83-5215-1-002
,	FOAM PAD	83-4623-2
	HEATSINK/SCREEN VIDEO	83-5013-2-003
	HEATSINK DEFLECTOR	83-5151-1-003
	EARTHING BRAID (14")	83-5303-4-001
	EARTHING BRAID (15")	83-5363-8-001
	△ LEAD, P-BAND SAFETY EARTH 310mm	83-5286-0-002
	ACONNECTOR, AC POWER, IEC320, 10A	22-8322-0
	PRISM	83-4315-2-002
	BRACKET MAINS FILTER	83-4060-9-006
	SCREEN DST MX3	83-4921-5-002
	SCREEN PSU INNER	83-5012-4-001
	CLAMP PLATE	83-5016-7-001
	BRACKET CLAMP DST	83-5150-3-002
	OPERATING INSTRUCTIONS W SERIES	79-1481-4-005A
*	OP' INST'S W SERIES USA/CANADA	79-1527-6-002

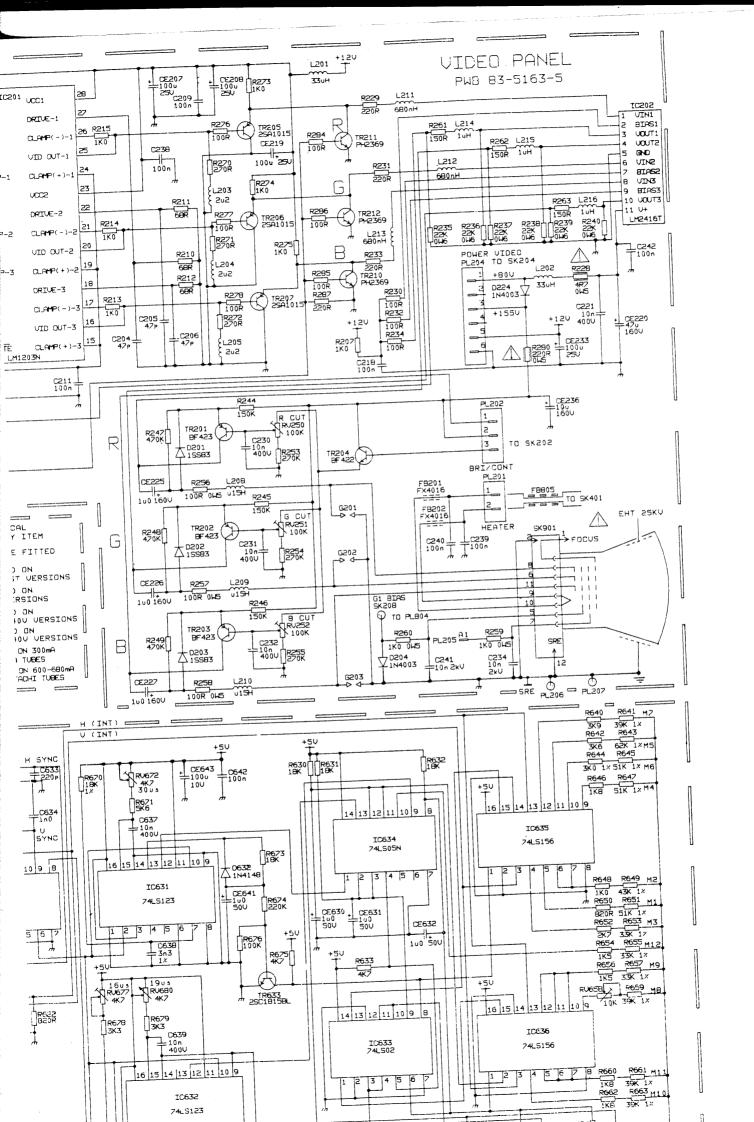
N.B. When ordering the following parts you will need the serial and model numbers of the monitor to ensure you receive the correct part. Because of different batch numbers there may be slight variations in colour when ordering the following designated replacement parts.

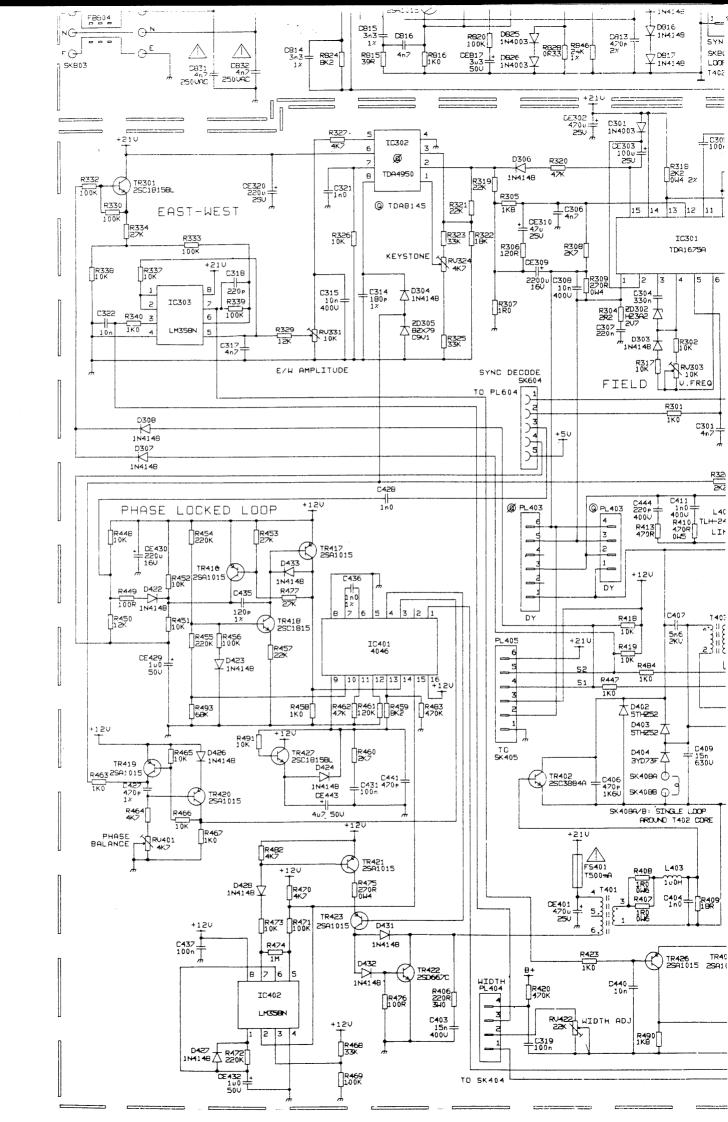
⚠ FRONT COVER MX3 (14")	83-4052-8
\triangle Front Cover MX3 (15")	83-4050-1
\triangle BACK COVER MX3 (14"/15")	83-4053-6
SWIVEL BASE MX2/MX3	83-5024-8
SWIVEL TOP MX2/MX3	83-4057-9
∆ KNOB ON/OFF MX3	83-4055-2

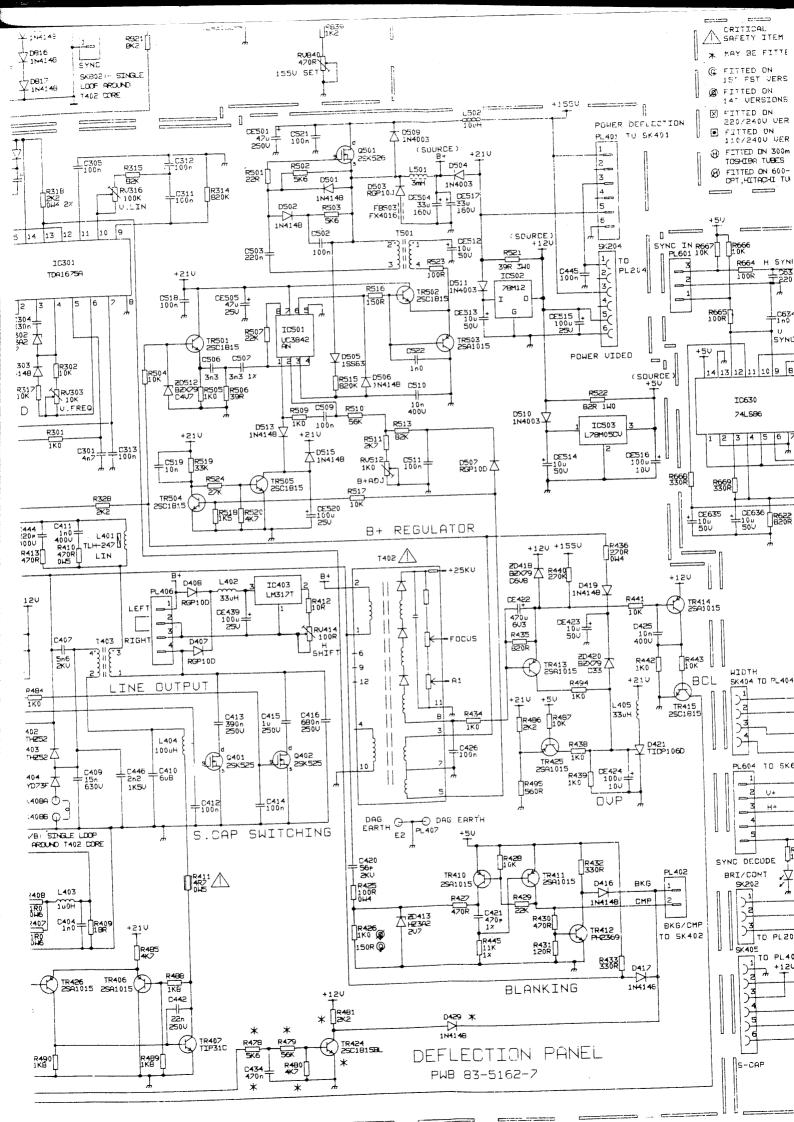


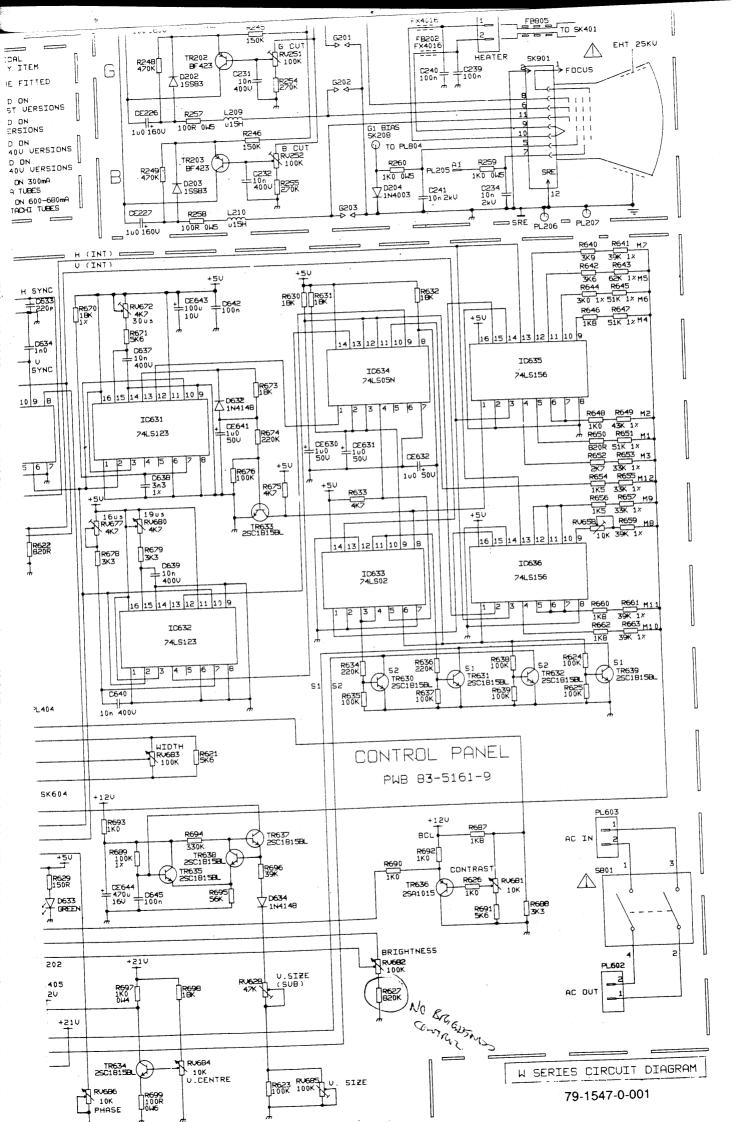












SERVICE MANUAL AMENDMENT RECORD Series: W Date of Issue:

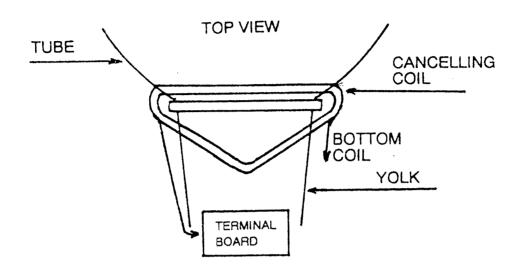
Document number: 79-1612-4-004 Models: PMS models 18/01/95

This information refers to: Service Manual No. 79-1499-7-001 Circuit Diagram No. 79-1547-0-001

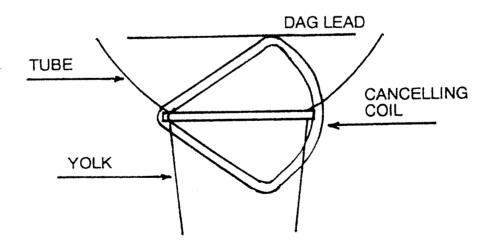
Circuit Diagram No. 79-1547-0-001			
Seq No	Details of changes	Reason	Ref. No. (ECN)
01	Power Management Control Panel 83-5573-8 issue 2 incorporated. See attached circuit diagram 79-1575-6-002 Appendix 1	Improved protection against static discharge to signal lead. Power Management System models only (PMS).	88/0934
02	Component differences R705,47K,5%,0.25W, 11-2798-5 R706,15K,5%,0.125W,11-5249-1 R710,12K,5%,0.125W,11-5285-8 R711,12K,5%,0.125W,11-5285-8 R712,47K,5%,0.125W,11-5233-5 ZD702,12V,5%,345mW,19-5070-3 ZD703,12V,5%,345mW,19-5070-3		
03	Delete page 38 of W Series Service Manual 79-1499-7-001 Power Management Control Cct. Diagram 79-1575-6-001 Replace with Power Management Control Circuit Diagram 79-1575-6-002. See Appendix 1		

Apendix A

Amended Fitting of Magnetic Field Cancelling Coils



BOTTOM VIEW



Date of Issue: W Series: SERVICE MANUAL AMENDMENT RECORD 06/07/95 Models: All VLMF Document number: 79-1612-4-006 This information refers to: Service Manual No. 79-1499-7-001 Circuit Diagram No. 79-1547-0-001 Ref. No. Reason & Category (ECN) Seq No Details of changes To further improve magnetic field 88/1008 A change in the method for fitting magnetic field cancel-ling coils as illustrated in cancellation. 01 Category C appendix A.

Essential for all. Α.

Recommended if problems are experienced. Category Category B.

Discretionary. Category C.

Date of Issue: Series: SERVICE MANUAL AMENDMENT RECORD 23/05/94 Models: 5401/5501 Document number 79-1612-4-001 79-1499-7-001 This information refers to: Service Manual No. 79-1547-0-001 Circuit Diagram No. Ref. No. Reason Seq No Details of changes (ECN) To increase Vert-88/0807 R697 changed to 180R, 5%, 2W ical Shift range. 11-5696-9/P01-37 01 M. Oxide R698 changed to 3K3, 5%, 0.125W 02 11-5229-7-26 Std. Film R699 changed to 68R, 5%, 1W 11-5697-7/P18-31 03 M. Oxide RV684 changed to 2K, 20% 04 12-4587-2 TR634 changed to 2SD667AC 05 19-8533-7 88/0817 Avoid monitor be-IC701 changed to 74HC123, part ing switched 'on' 06 when connected to number 19-8763-1 PC which has sync signals at 1v D.C. when first switched 'off'. 88/0820 R223 changed to 1K5, 1%, 0.125W To improve bias 07 IC202 and remove 11-5700-0 'smear' on Windows type displays.

Date of Issue:

Models: 5401/5501 27/06/94 Document number 79-1612-4-002 This information refers to: Service Manual No. 79-1499-7-001 Circuit Diagram No. 79-1547-0-001 Reason Ref. No. Seq No Details of changes (ECN) C212 & C213 changed to 22n, 63v To correct instab- 88/0841 80 ility in IC201 -20+80% Cer. Plt 14-6911-8 when Texas part is Add C243 22n, -20+80%, 63v, C.P used. 09 14-6911-8 to back of Video PWB between pins 2 & 3 of IC201. C243 must be connected directly to IC201 pins with minimum sized legs R483 changed to 1MO, 5%, 0.125W To improve Phase 88/0846 10 balance adjustment 11-5216-5-26 Add R496, 470K, 5%, 0.125W, 11 11-5355-2-26 Add R497, 15K, 5%, 0.125W 12 11-5249-1-26 Note 1: Fit R496 between pins 9 & 10 of IC401, solder side of Deflection PWB 83-5162-7-003 Note 2: Fit R497 between pin 4, SK604 and +12v supply, solder side of Deflection PWB 83-5162-7-003 Alternative CDT 88/0870 13 Delete assembly 03-0752-4/M has integral VLMF (18-1040-5 M34AFA60X33 CDT) coil for improved performance. (83-5323-9-002 coil) (29-5975-5 sleeving) cable tie) (57-2900-9 (60-6656-9 adhesive) Add 18-1067-7 14" CDT M34AFA60X48

Series: W

SERVICE MANUAL AMENDMENT RECORD

Deflection Panel (Track side)

