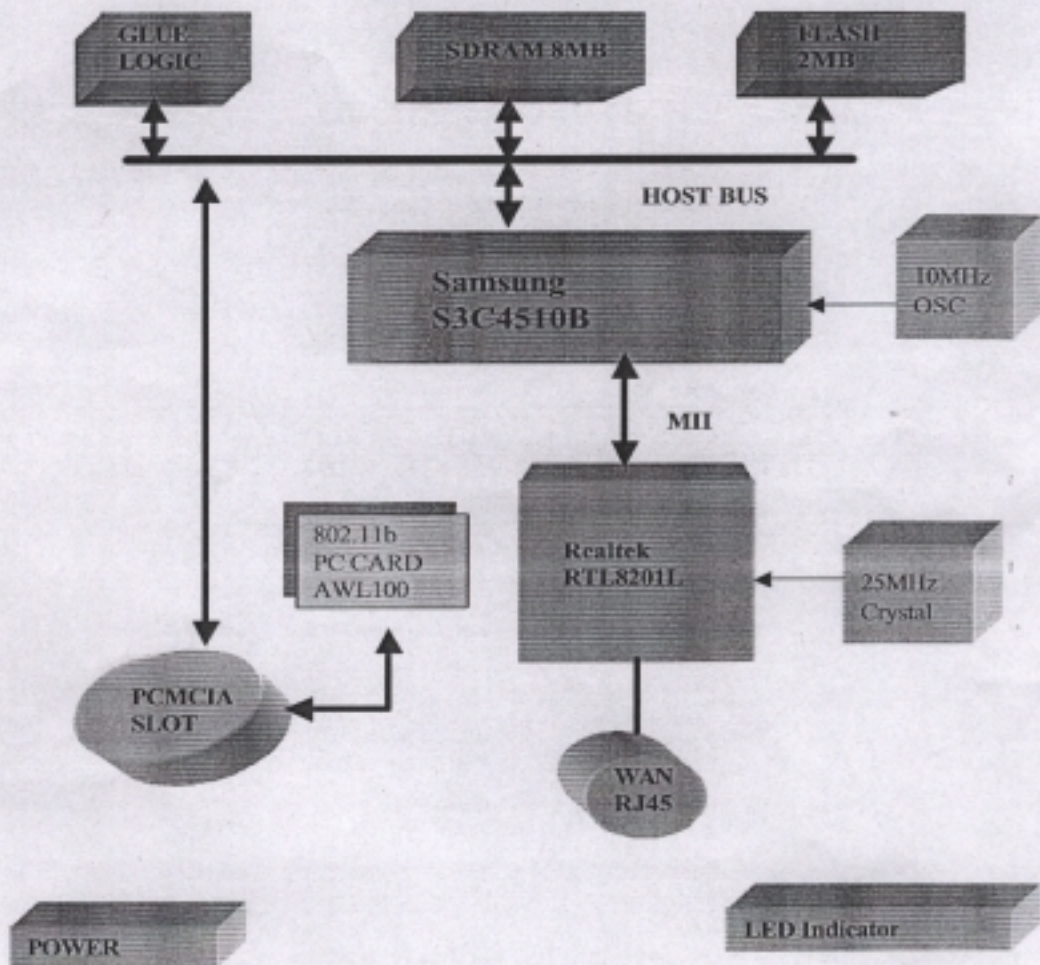
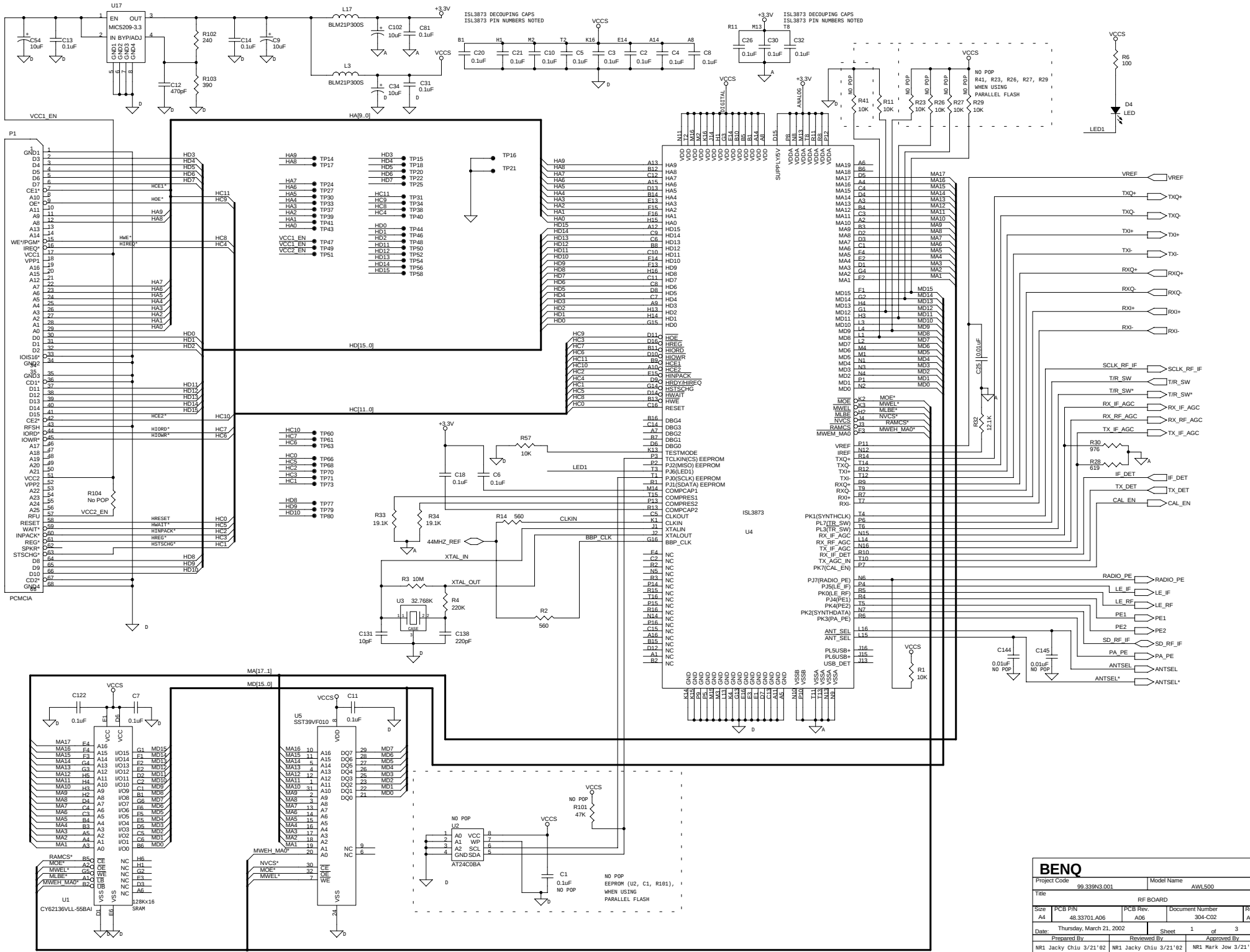


Functional Block – AWL500

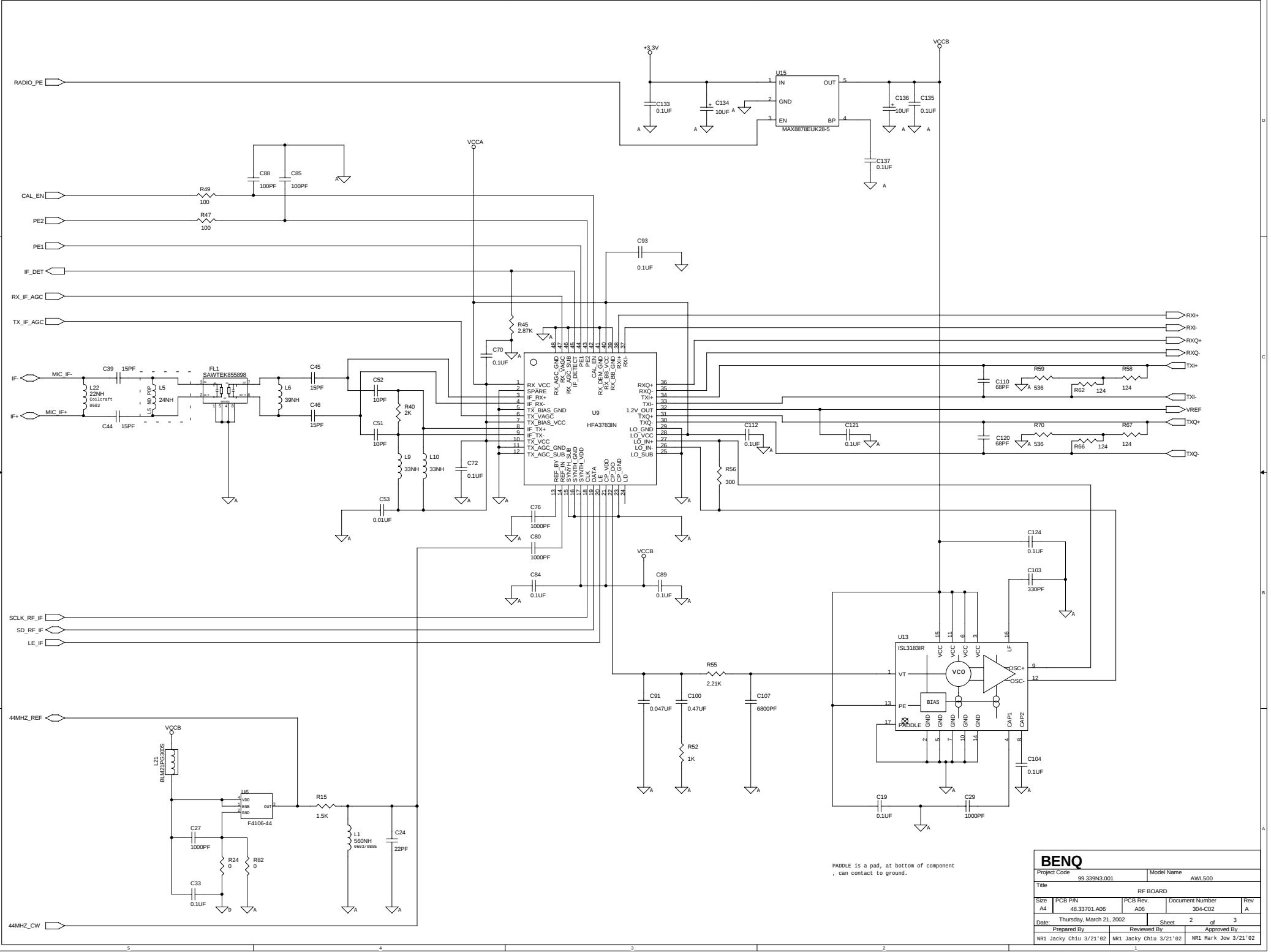


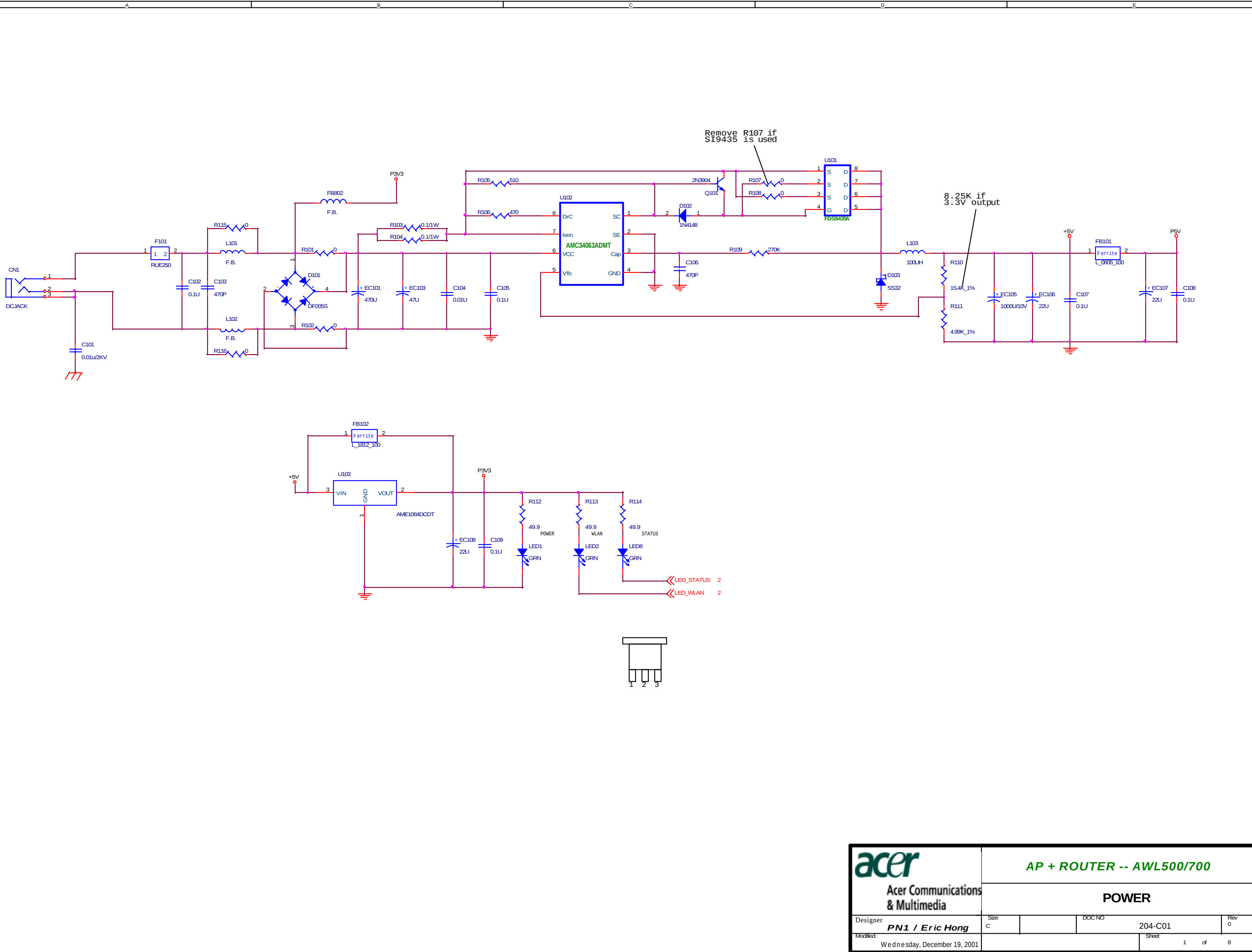
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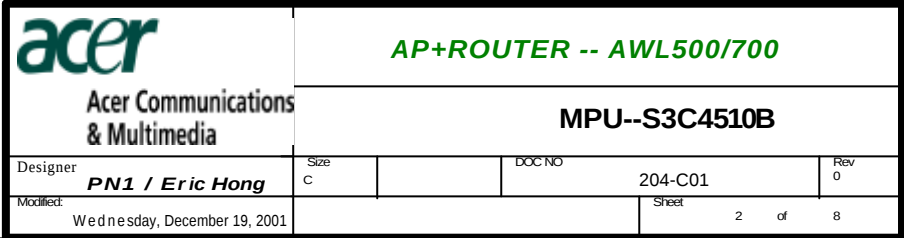
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RF BOARD							
Size A4		PCB P/N 48.33701.A06		PCB Rev. A06		Document Number 304-C02	
Rev A							
Date: Thursday, March 21, 2002				Sheet 3 of 3			
Prepared By			Reviewed By			Approved By	
NR1 Jacky Chiu 3/21/02			NR1 Jacky Chiu 3/21/02			NR1 Mark Jow 3/21/02	

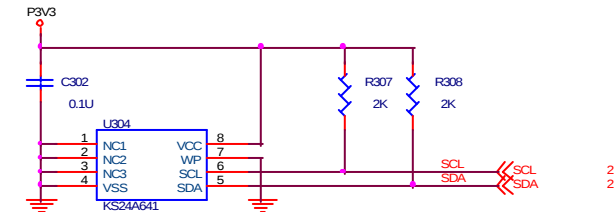
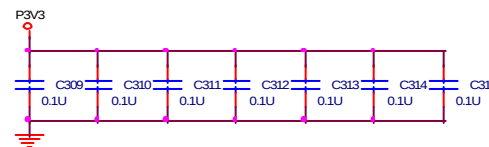
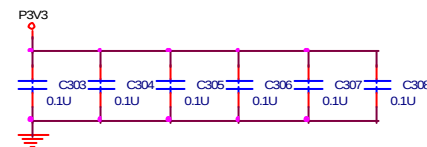
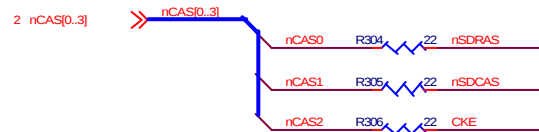
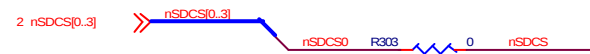
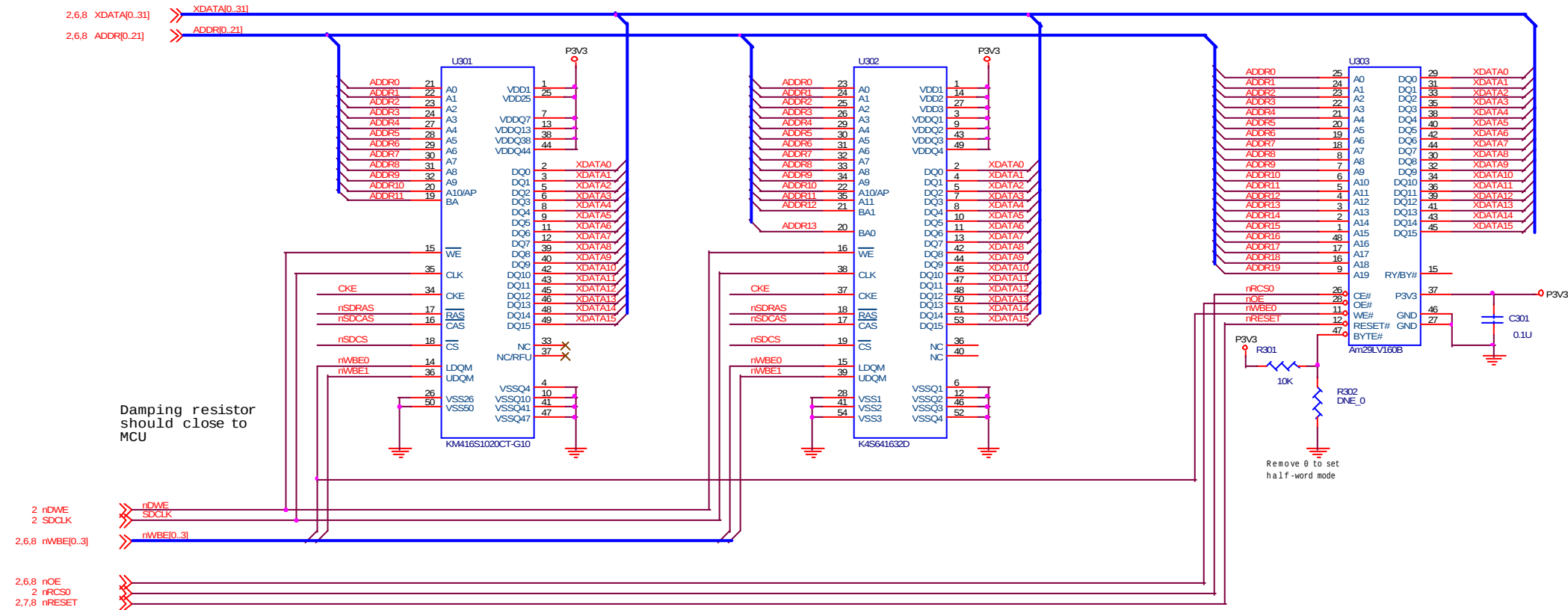


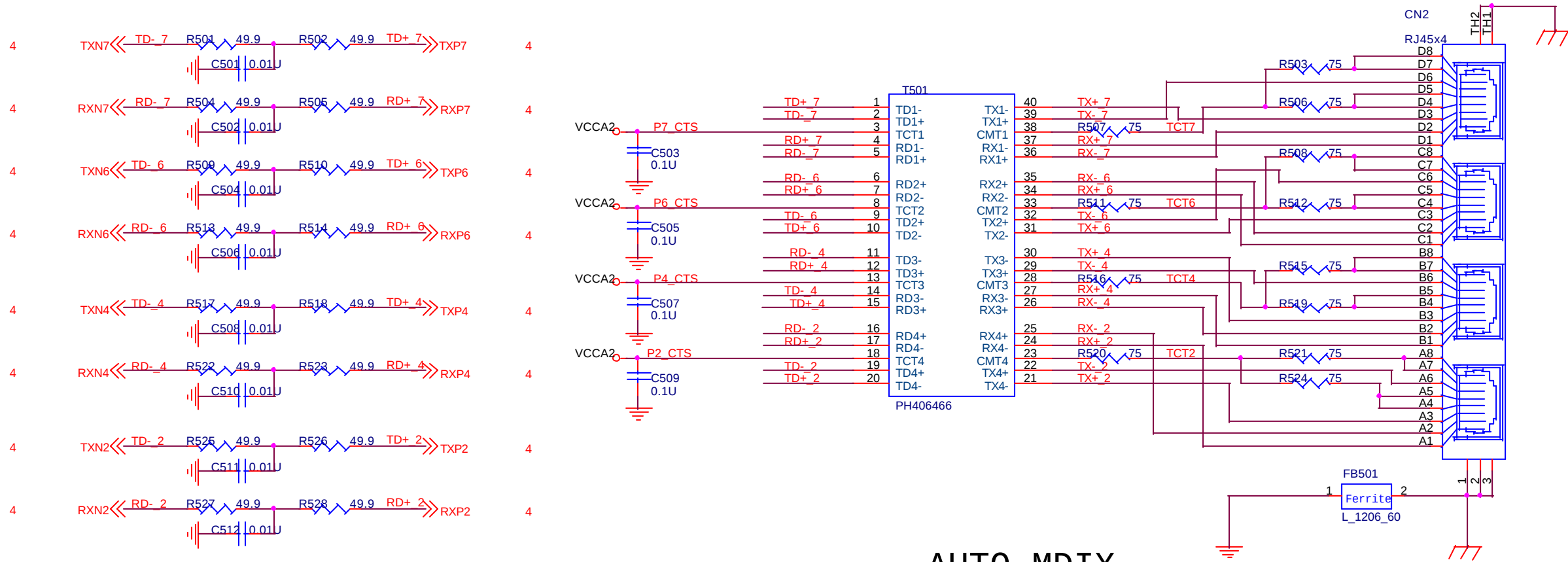
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Size	PCB PIN	PCB Rev.	Document Number
A4	48.3701.A06	A06	304-C02
Date:	Thursday, March 21, 2002		Rev A
		Sheet	1 of 3
Prepared By		Reviewed By	
NR1 Jacky Chiu 3/21/02		NR1 Jacky Chiu 3/21/02	
Approved By		Approved By	
NR1 Jacky Chiu 3/21/02		NR1 Mark Jow 3/21/02	



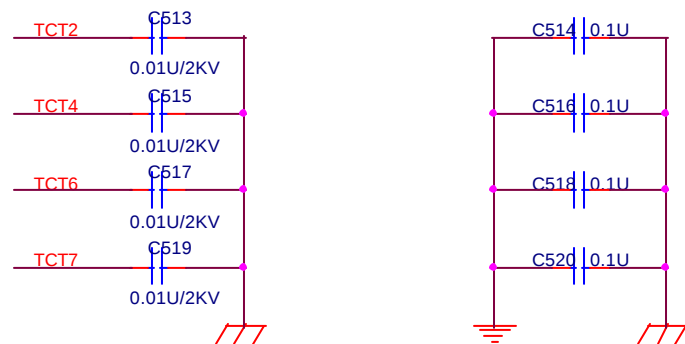








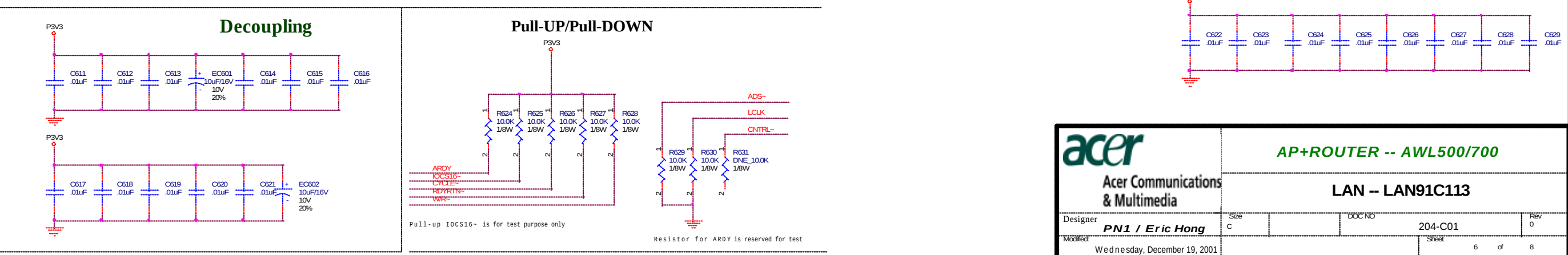
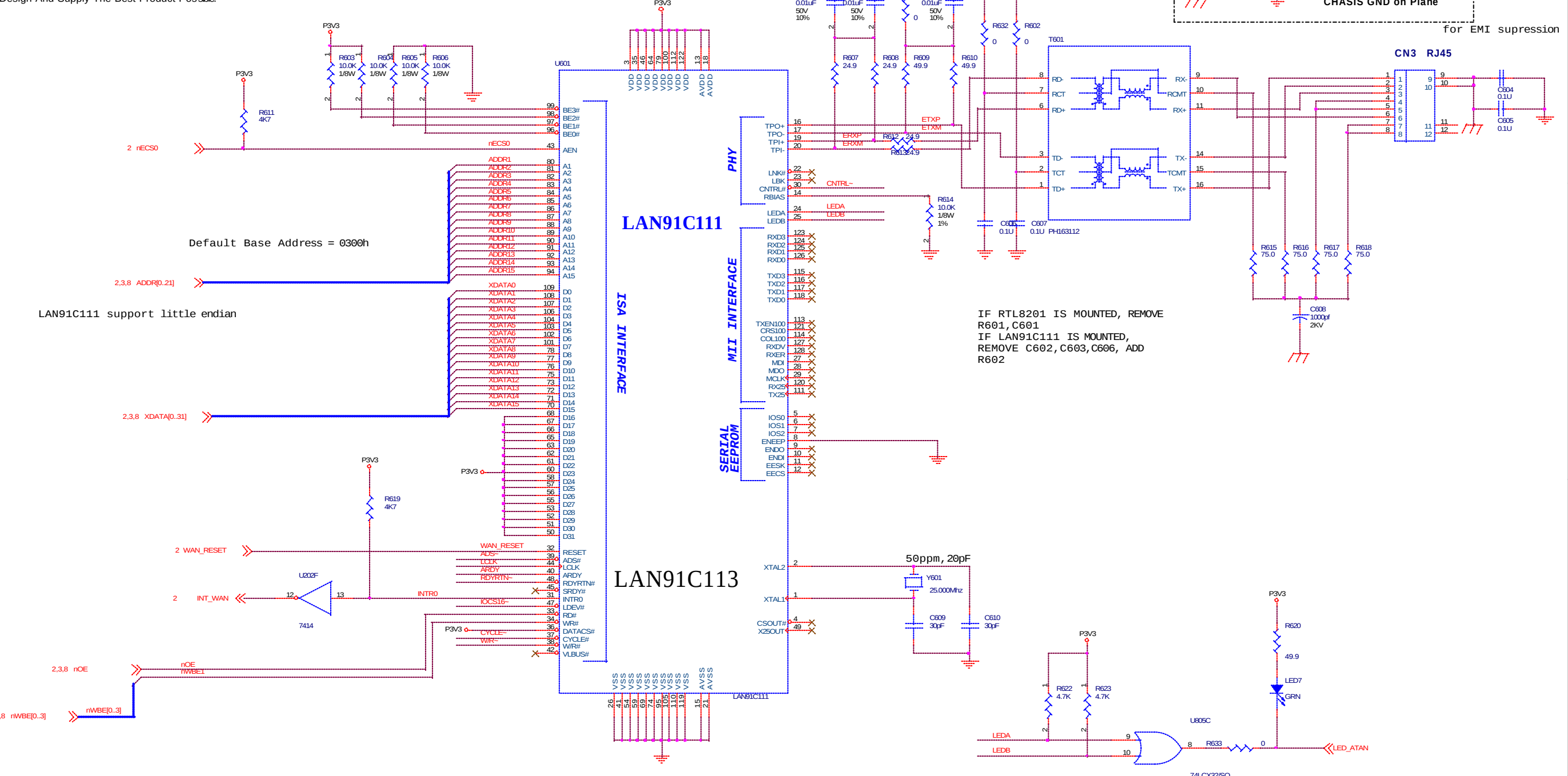
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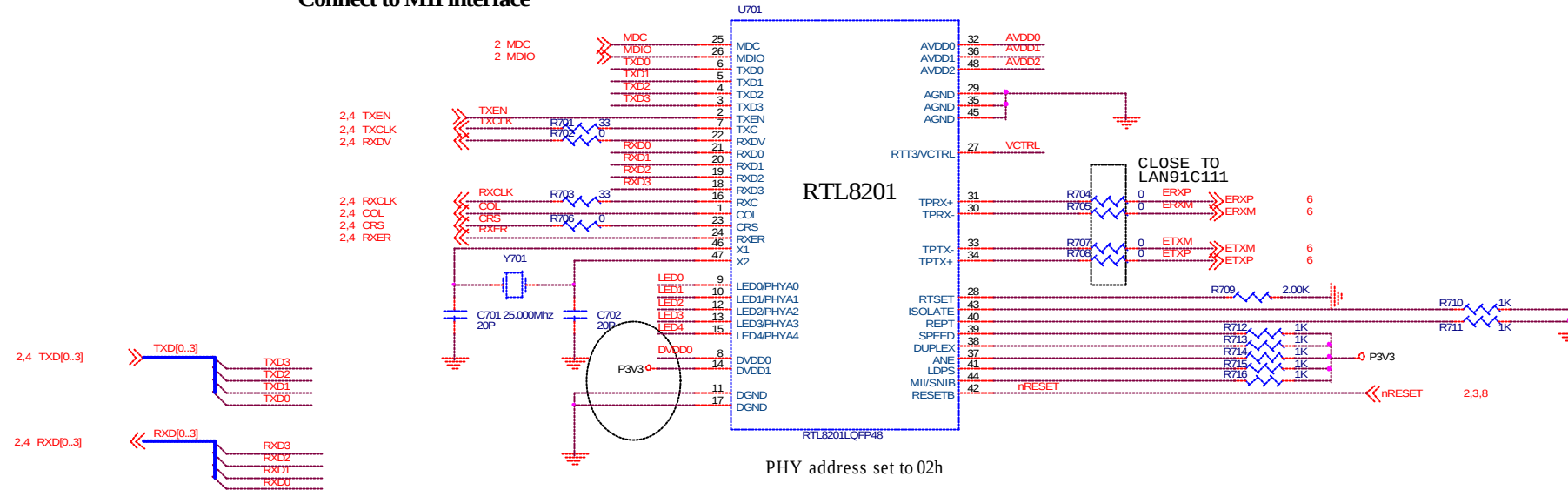
 Acer Communications & Multimedia		AP+ROUTER -- AWL500/700					
		SWITCH TRANSFORMER					
Designer	PN1 / Eric Hong	Size	Custom	DOC NO	204-C01	Rev	0
Modified:	Wednesday, December 19, 2001			Sheet	5	of	8

LAN91C111 Evaluation Design

Circuit Diagrams utilizing SMSC Products Are Included As A Means Of Illustrating Typical Semiconductor Applications: Consequently Complete Information Sufficient For Construction Purposes Is Not Necessarily Given. The Information Has Been Carefully Checked And Is Believed To Be Entirely Reliable. However, No Responsibility Is Assumed For Inaccuracies. Furthermore, Such Information Does Not Convey To The Purchaser Of The Semiconductor Devices Described Any License Under The Patent Rights Of SMSC Or Others. SMSC Reserves The Right To Make Changes At Any Time In Order To Improve Design And Supply The Best Product Possible.

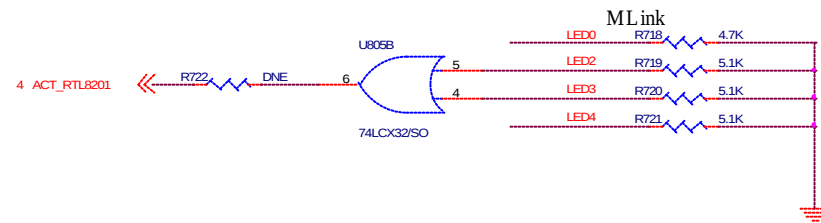


Connect to MII interface



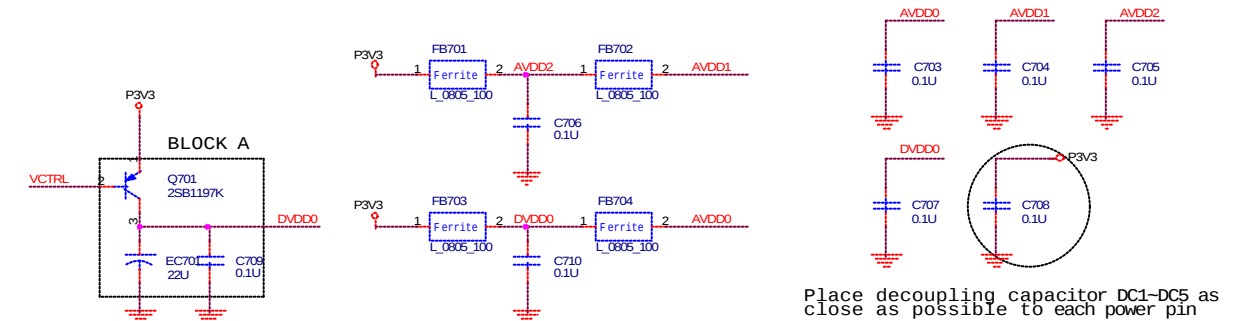
MDux
LED1 4.7K P3V3

RTL8139C+ should configure to MDux=0, MLink=1
MDupActiveState=0
MLinkActiveState=1



Config:

Enable: N-way, Full duplex, 100Mbps, Link Down Power Saving
Disable: Isolate, Repeater Mode



BLOCK A : The components inside BLOCK A are reserved for RTL8201B(.25u version). In RTL8201B, DVDD0 and AVDD0 will be fed 2.5V produced by BLOCK A which is functional as linear regulator and L3, R6 will be removed. In order to be layout-compatible between RTL8201 and RTL8201B, please reserve the component space and pads in your layout for future extension.

