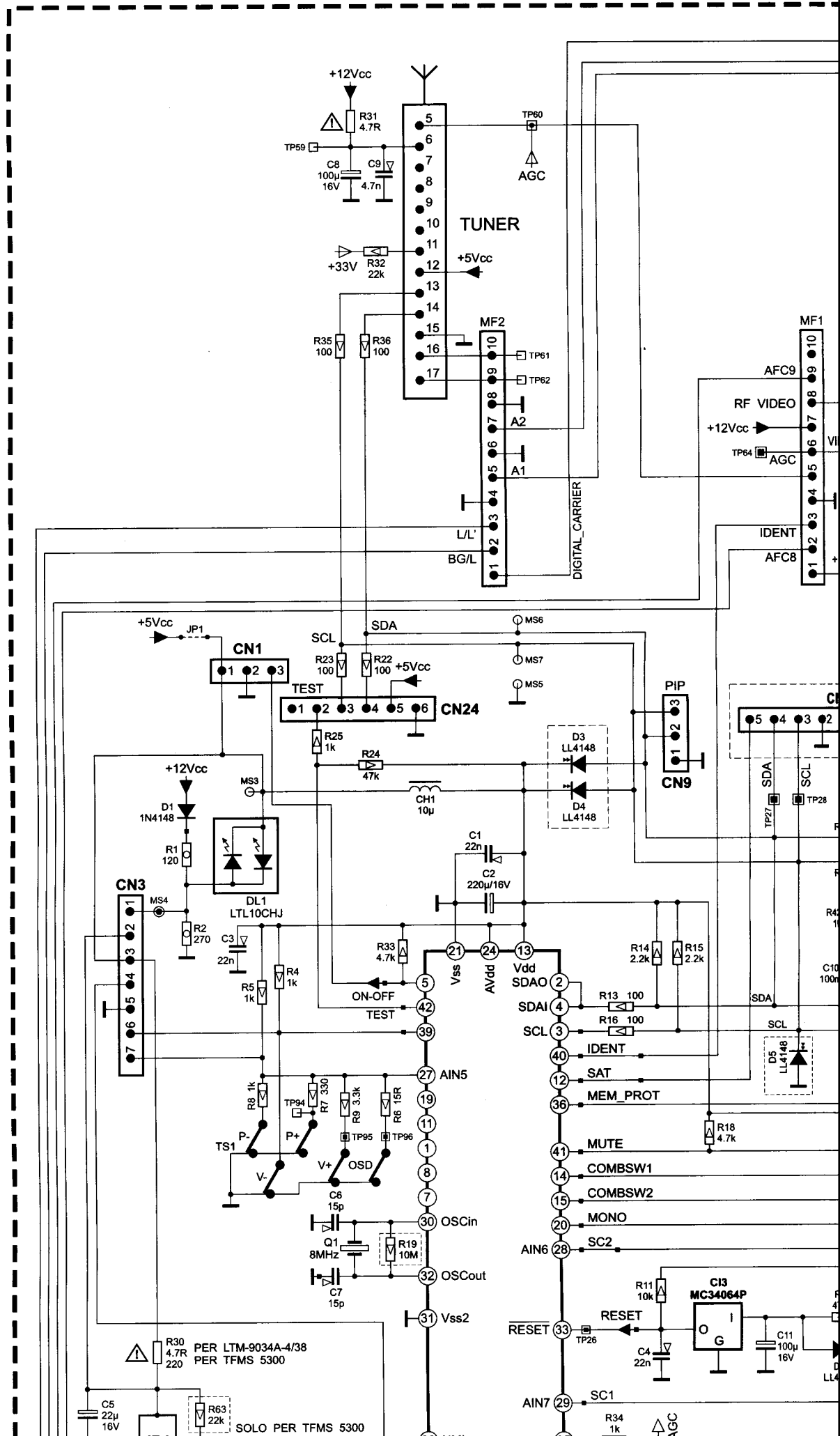
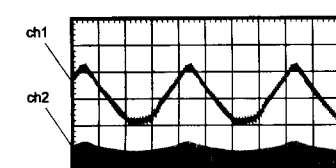
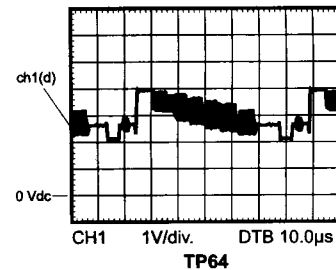
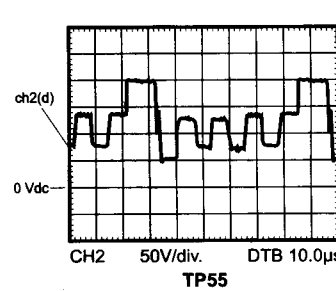
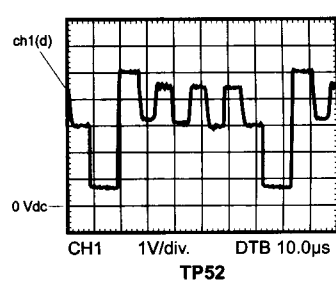
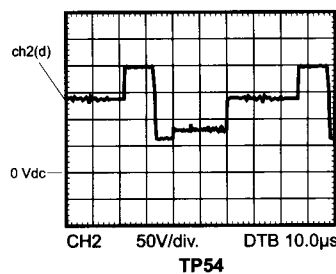
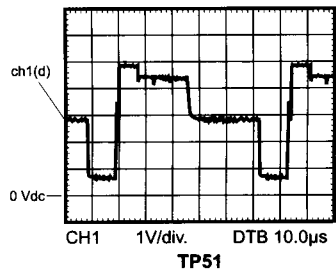
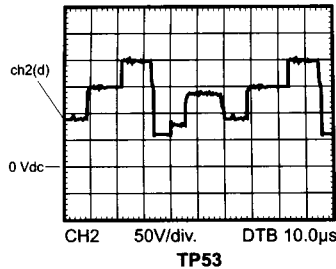
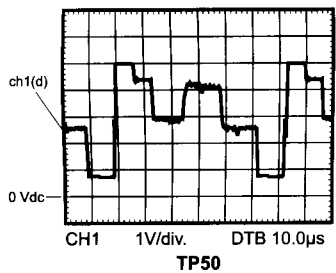
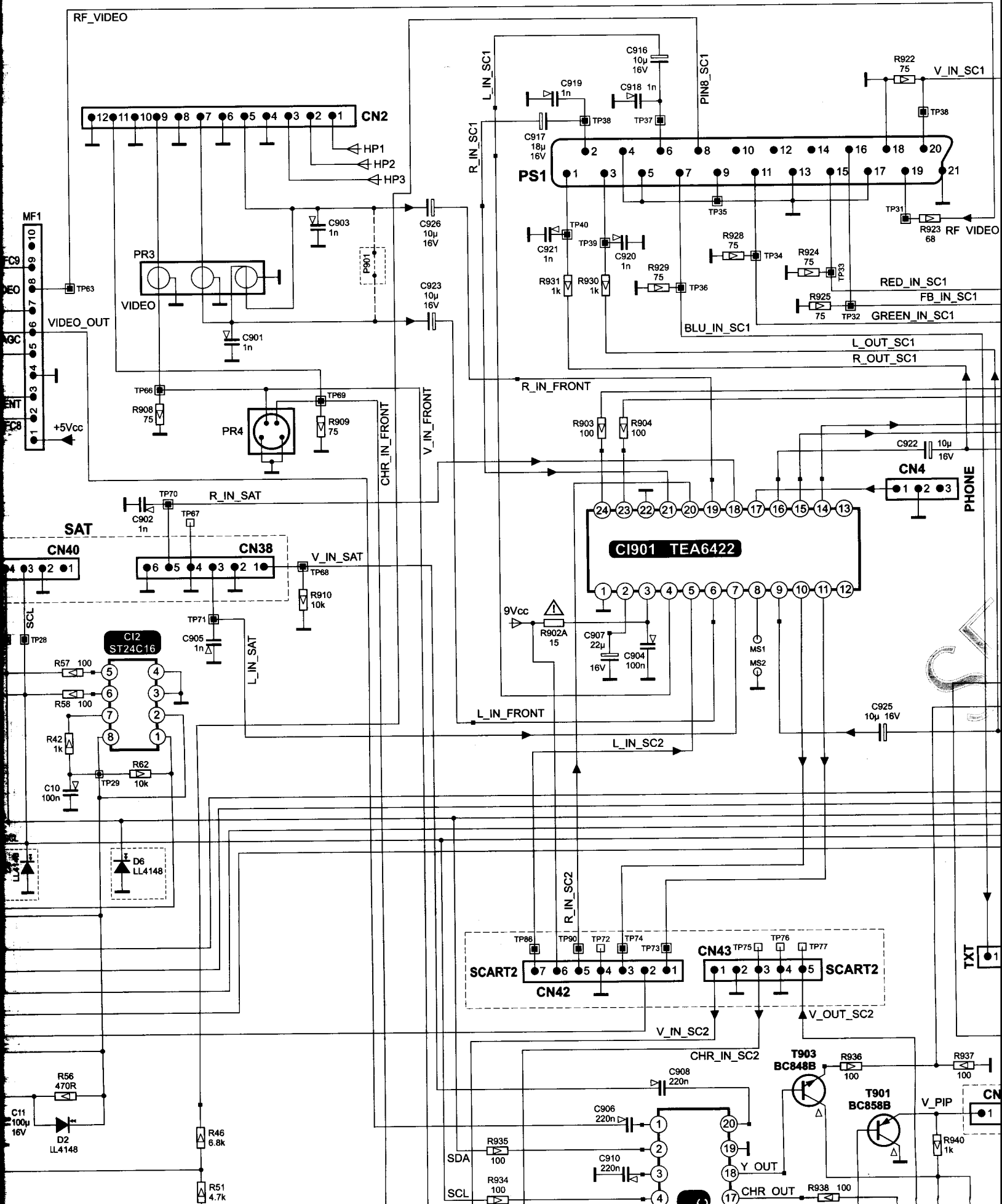




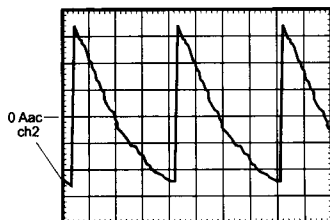




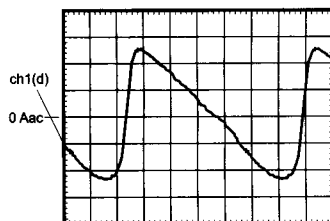
CH1 5V/div.
CH2 100V/div. MTB 5.00ms
E/W: CH1 Pin 11 CI601
CH2 wave form on D305



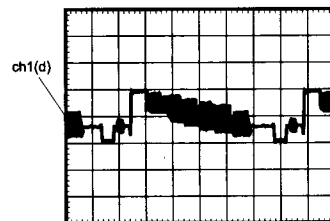
CH1 5V/div.
CH2 500V/div. DTB 10.0µs
Horizontal: CH1 Base T301
CH2 Collector T301



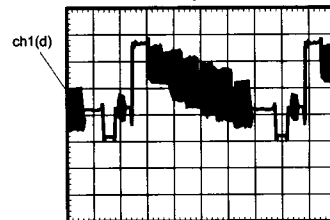
CH2 200mA/div. MTB 5.00ms
Horizontal yoke current



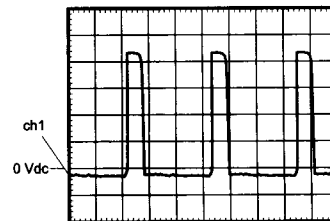
CH2 1A/div. MTB 10.0µs
Vertical yoke current



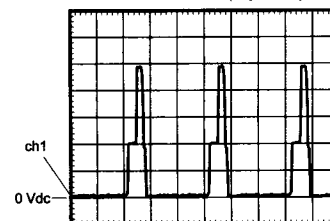
CH1 0.5V/div. DTB 10.0µs
Pin 7 IC903 with signal video from Tuner



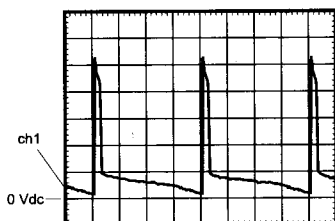
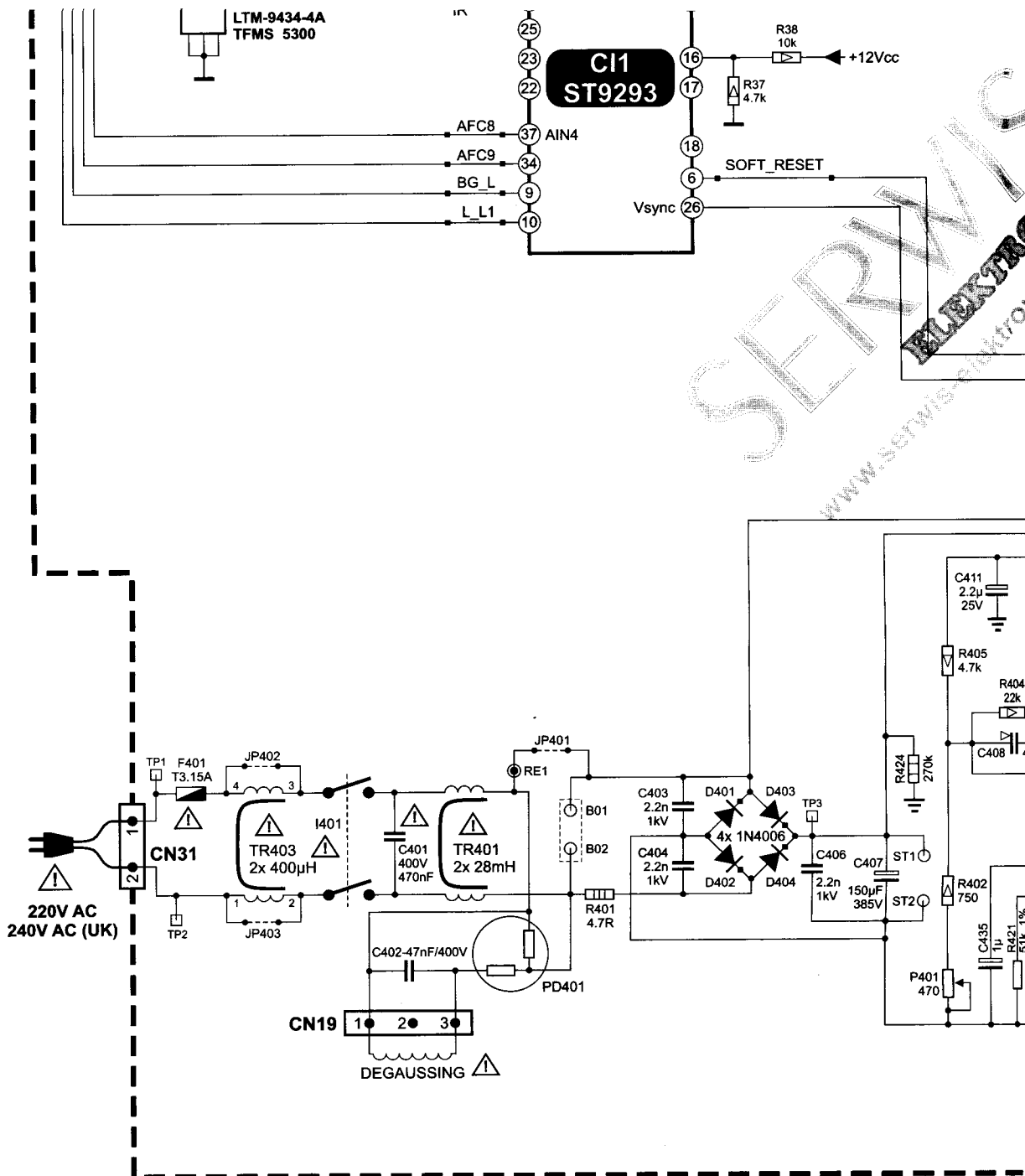
CH1 0.5V/div. BWL DTB 10.0µs
TP64(AC)



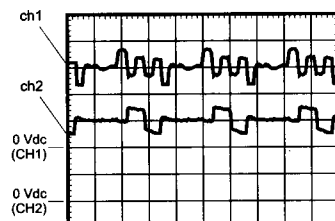
CH1 2V/div. MTB 20.0µs
Pin 41 IC903 (Flyback)



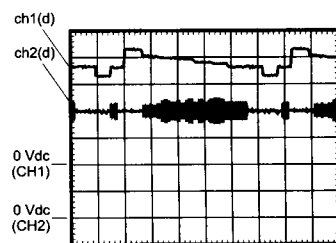
CH1 1V/div. MTB 20.0µs
Pin 39 IC903 (Super-sandcastle)



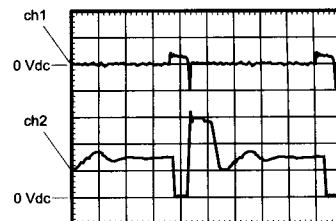
CH1 10V/div. MTB 5.00ms
Pin 9 IC601



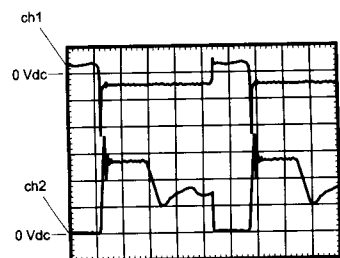
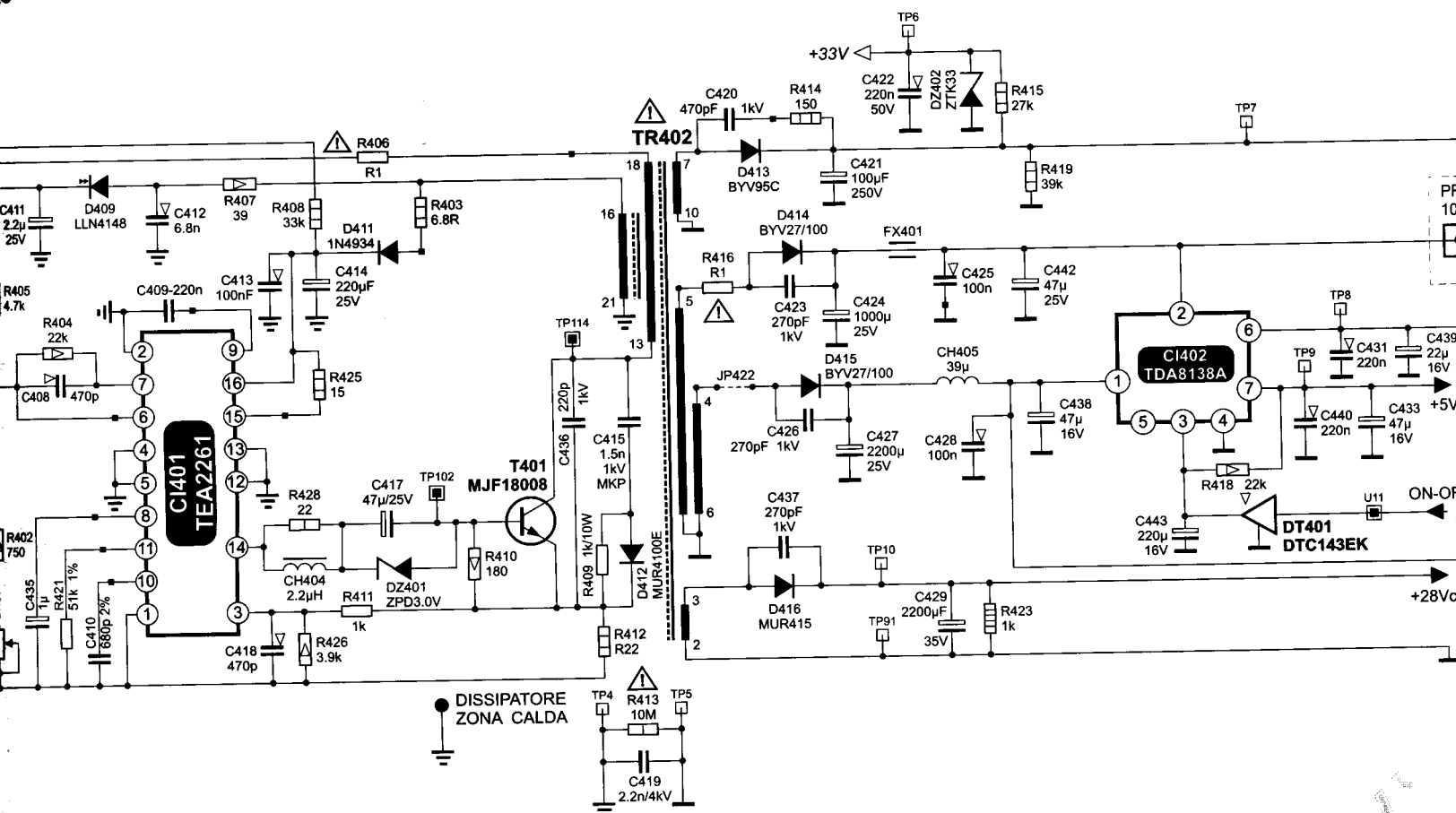
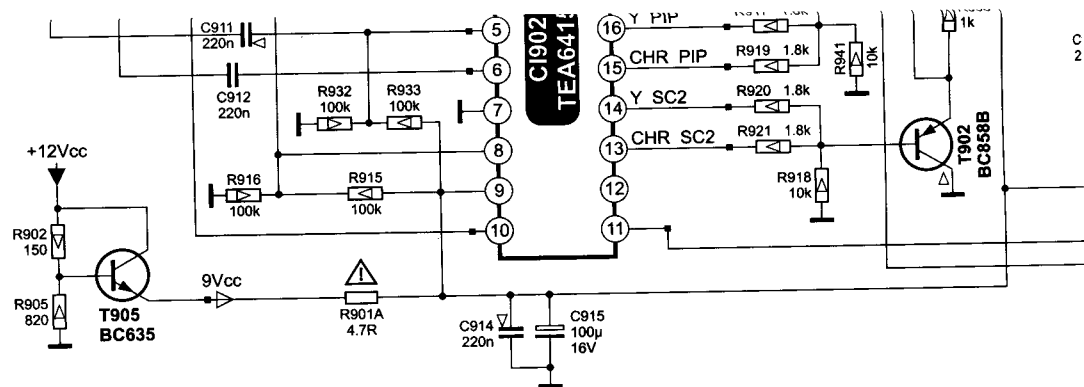
CH1 500mV/div.
CH2 500mV/div. MTB 20.0µs
CH1 Pin 29 CI903 (B-Y)
CH2 Pin 30 CI903 (R-Y)



CH1 1V/div. DTB 10.0µs
CH2 1V/div.
CH1 Pin 7 CI903
CH2 Pin 6 CI903 (with signal input SVHS)

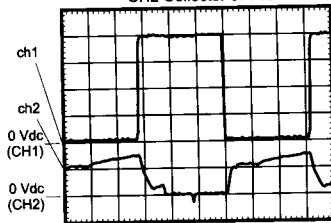


CH1 5V/div. MTB 5.00µs
CH2 200V/div.
TVC: STAND-BY
CH1 Base of T401
CH2 Collector of T401.

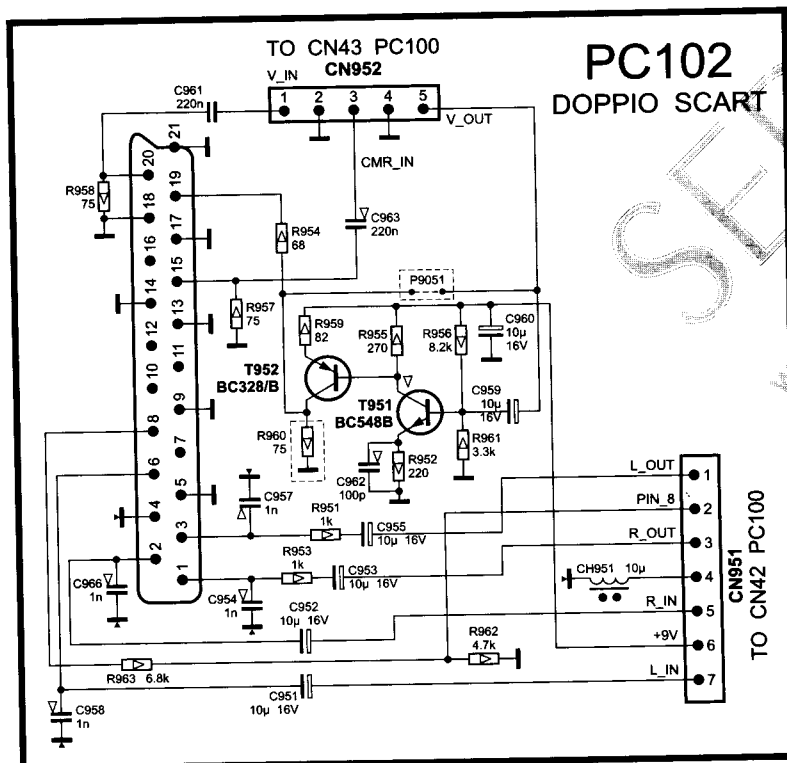


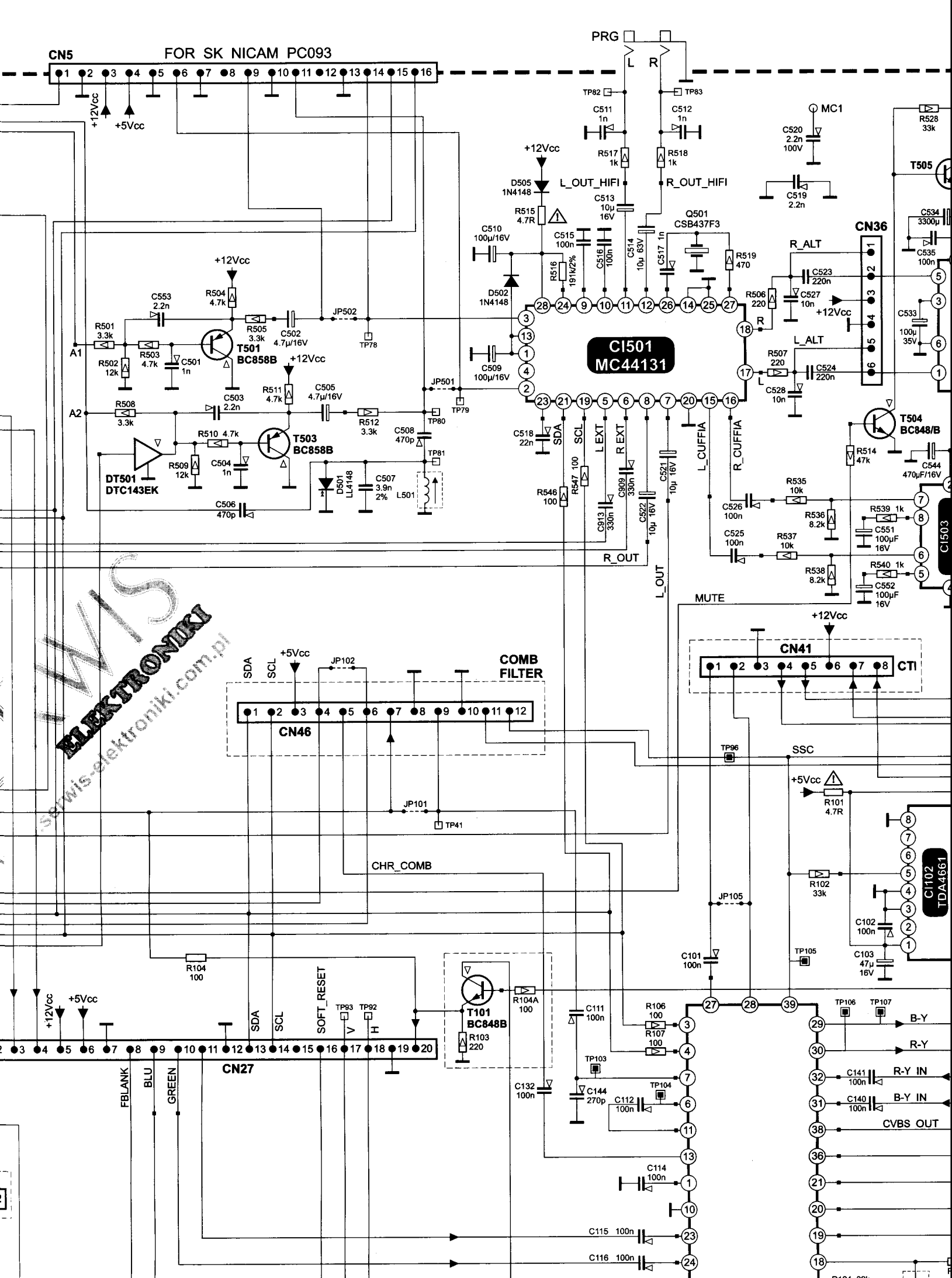
CH1 5V/div. MTB 5.00μs
CH2 200V/div.

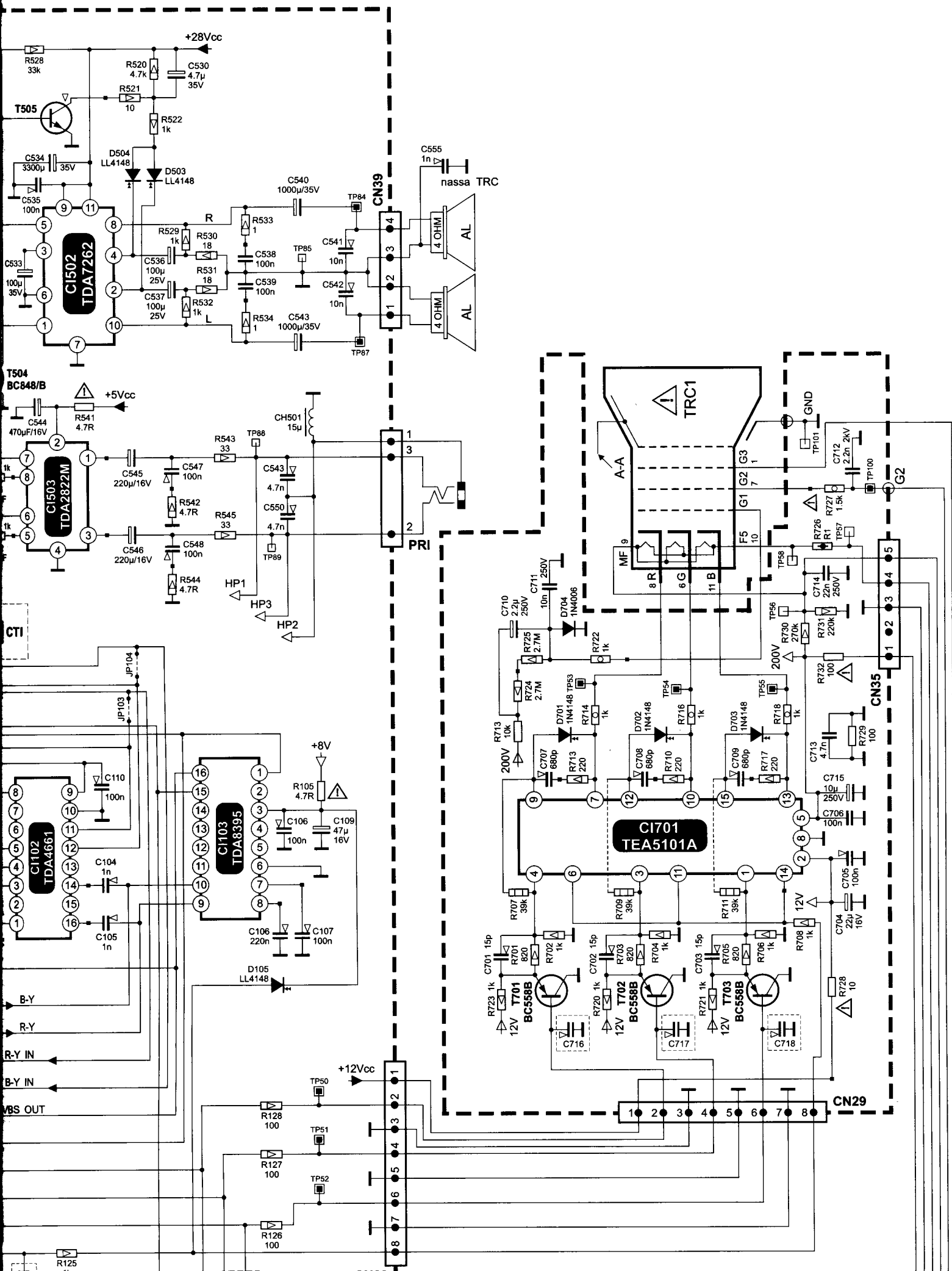
TVC: POWER ON:
CH1 Base of T401
CH2 Collector of T401.



CH1 2V/div. MTB 10.0μs
CH2 5V/div.
Horizontal: CH1 Pin 40 CI903
CH2 TP19



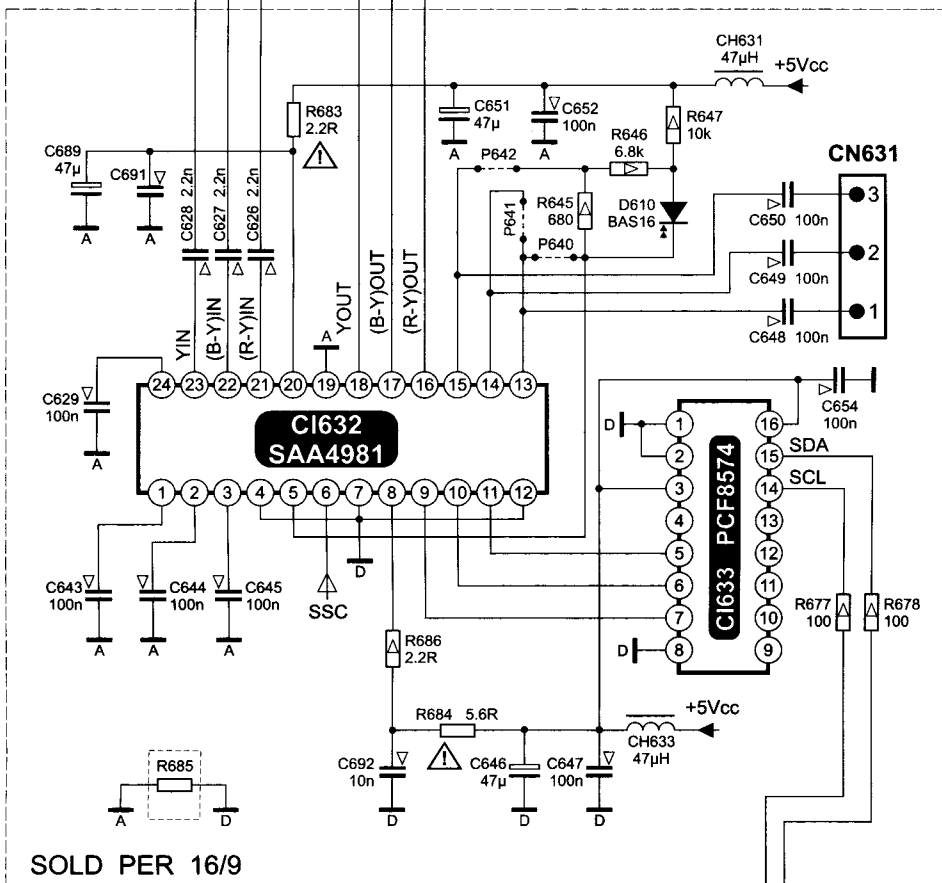
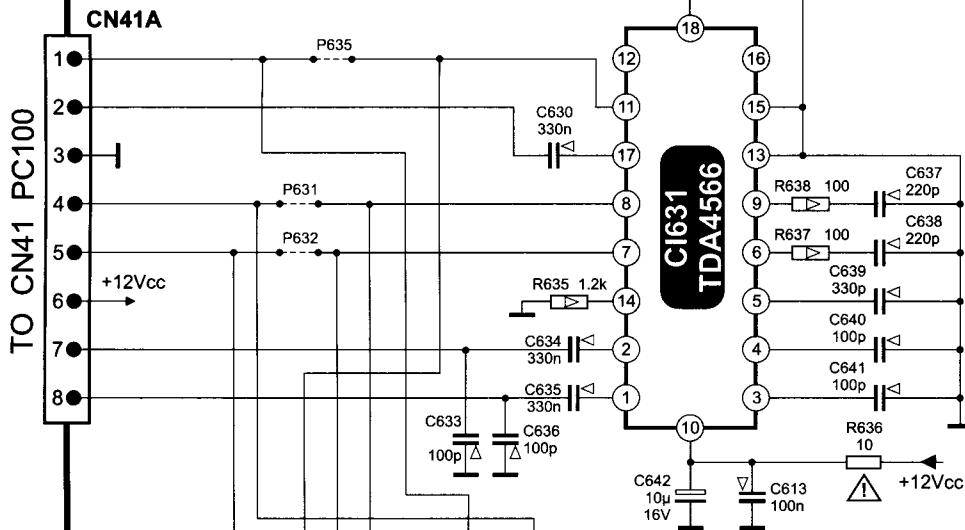




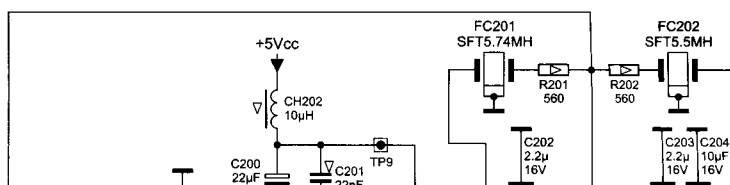
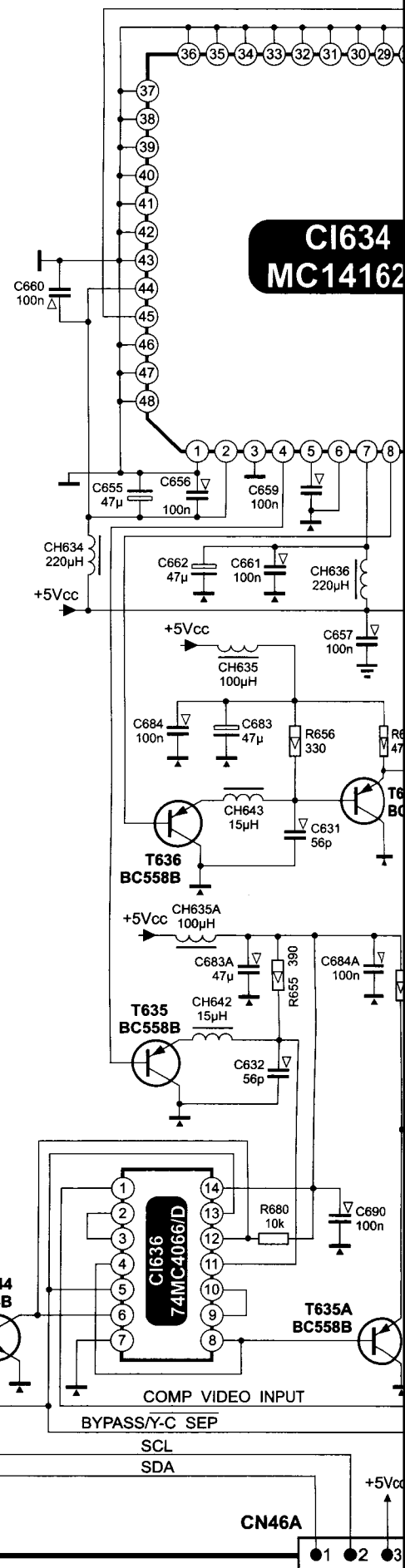


TESI
PC100

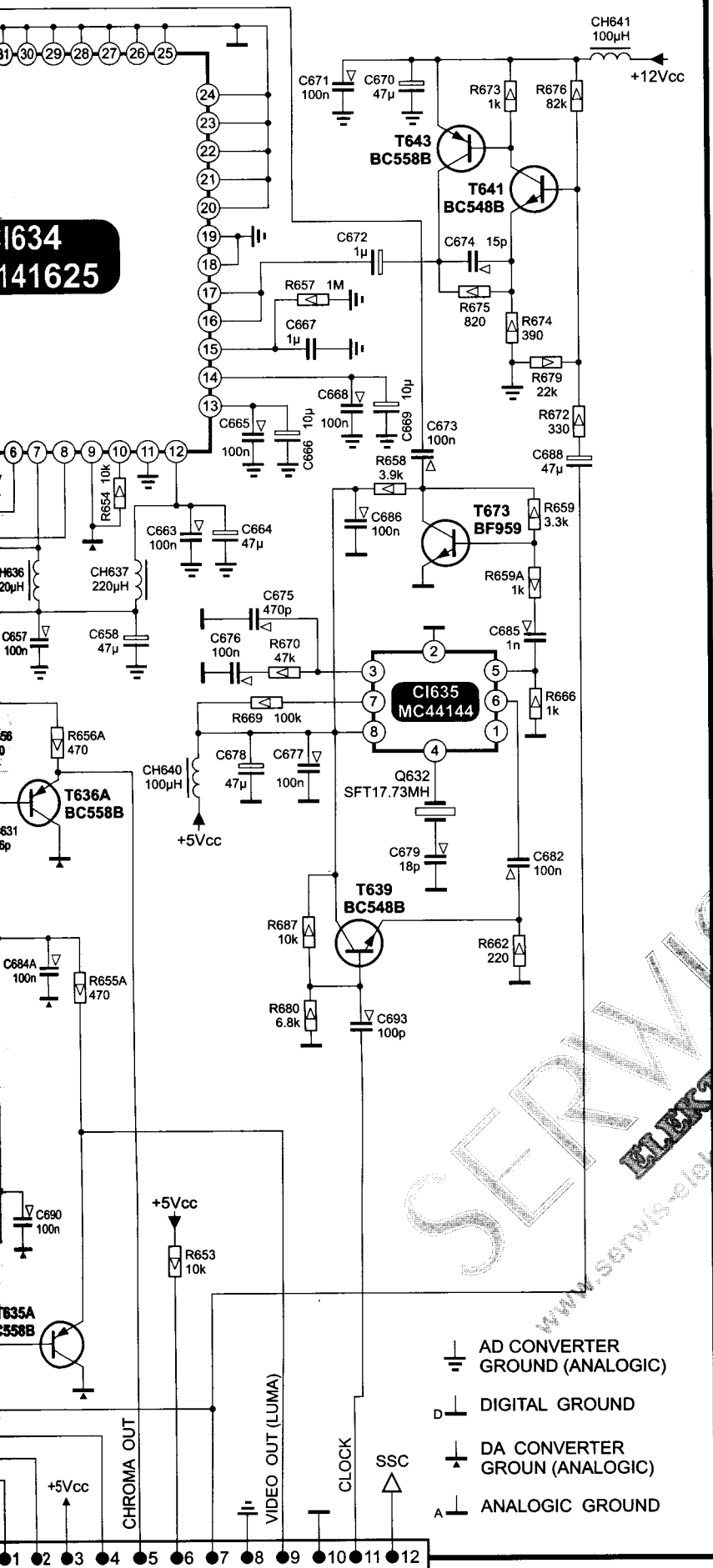
le: 765/325, 765/368, 765/384 (strona 1/2)



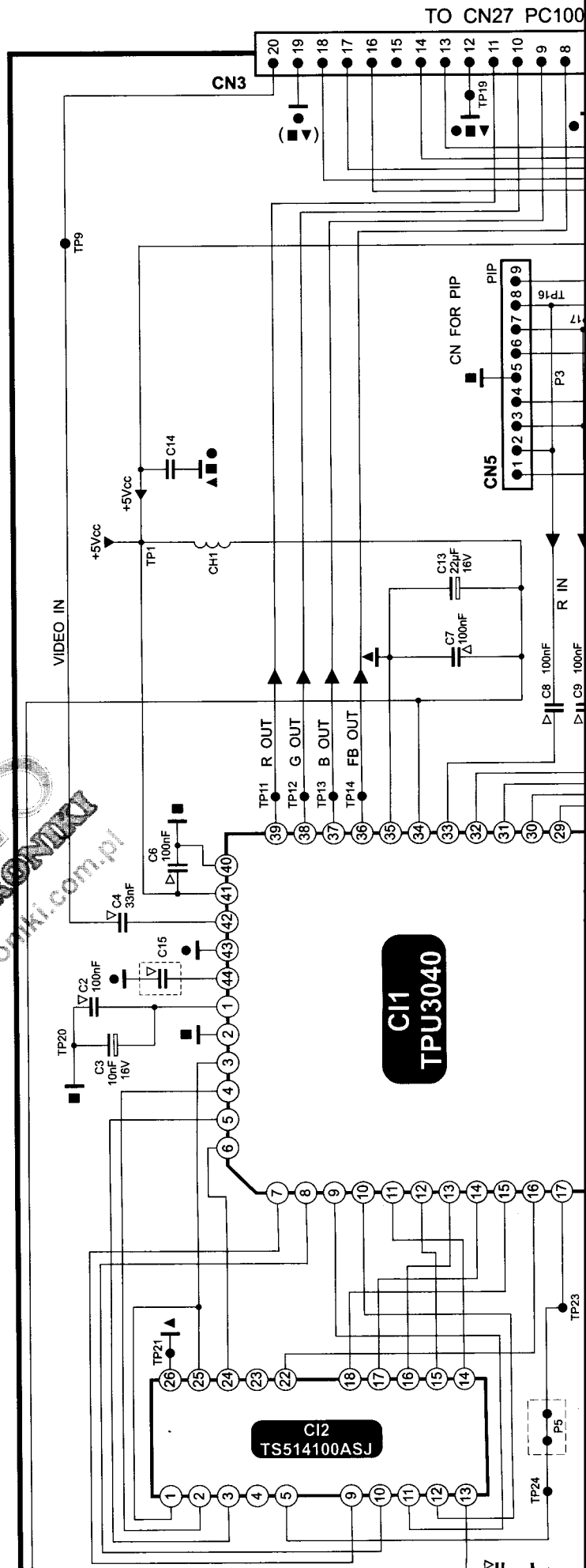
PC105, 0
COMB FILTER + CTI

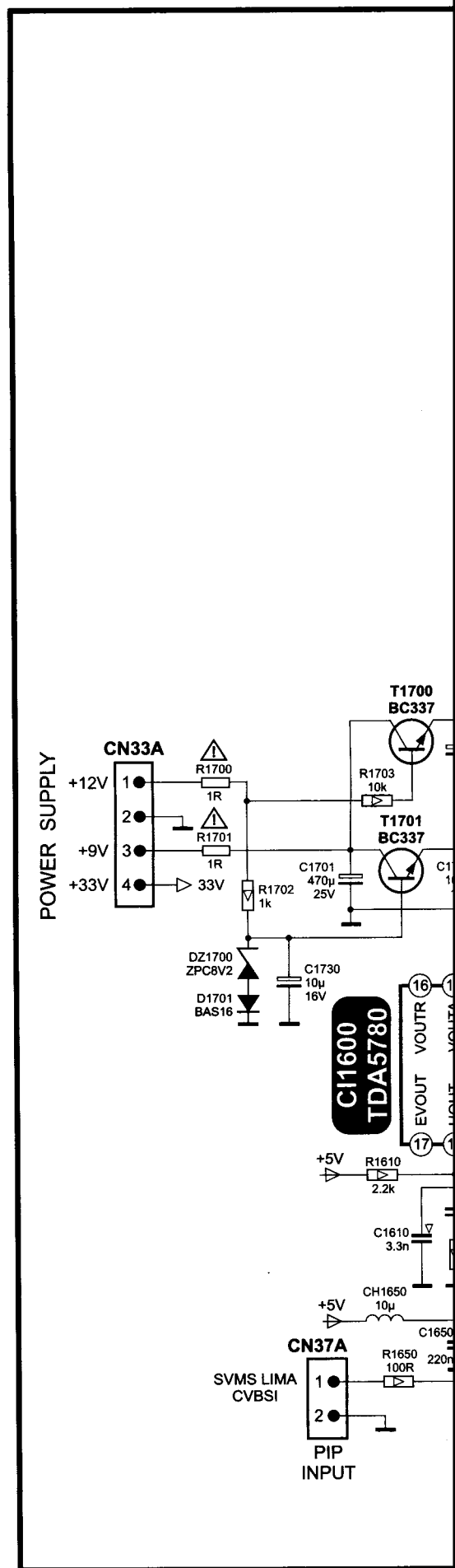
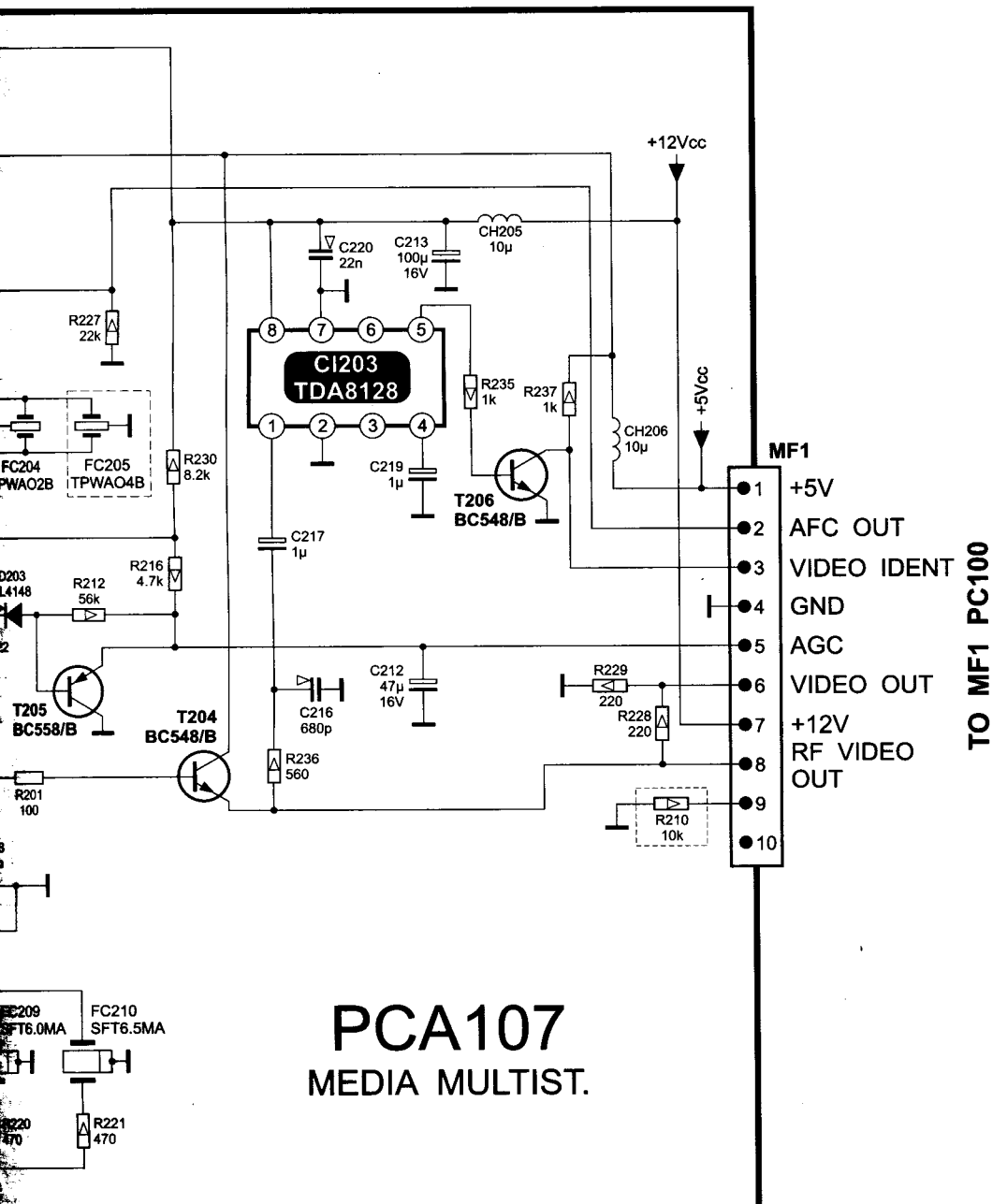
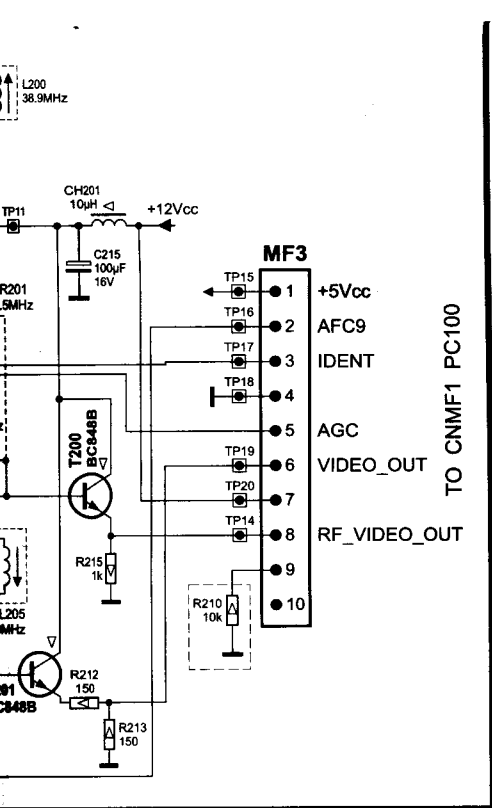


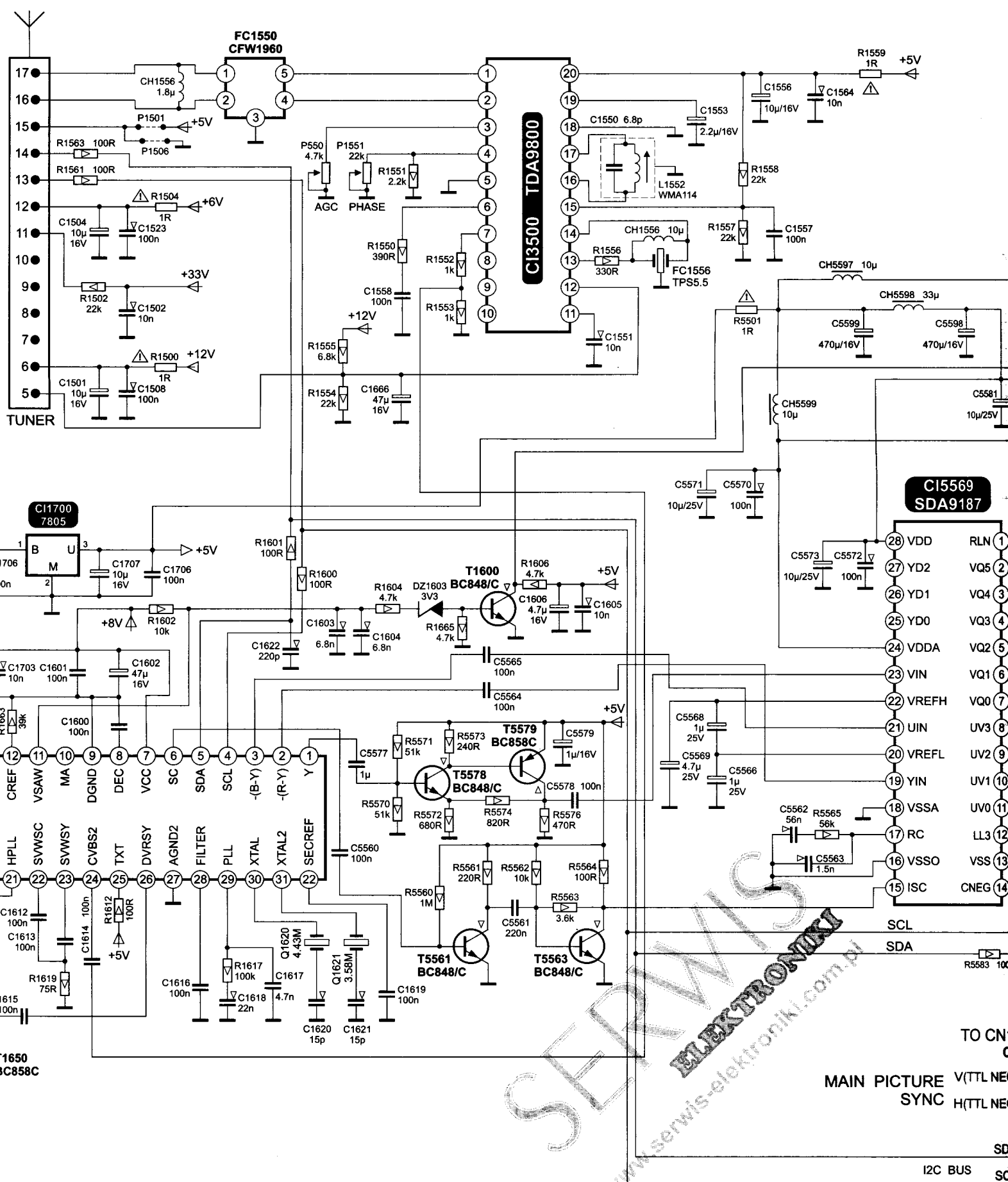
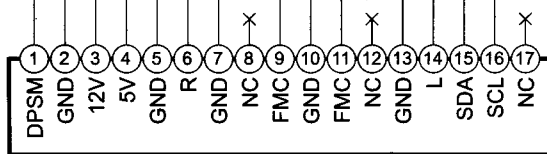
1634
141625



TO CN27 PC100







TO CN1
MAIN PICTURE V(TTL NEG)
SYNC H(TTL NEG)

SDA
SCL

I2C BUS

PC176

PIP * TEST

