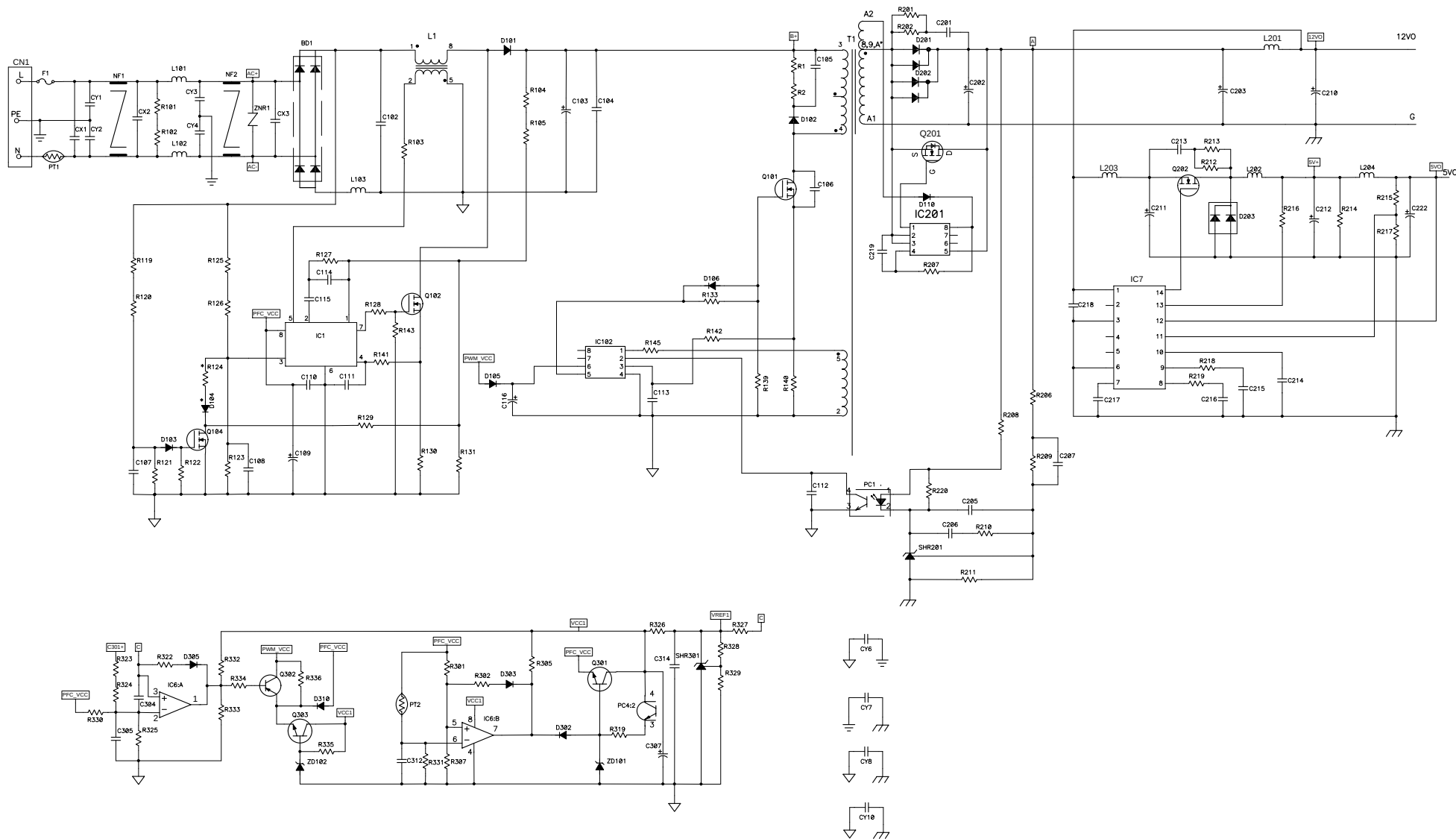
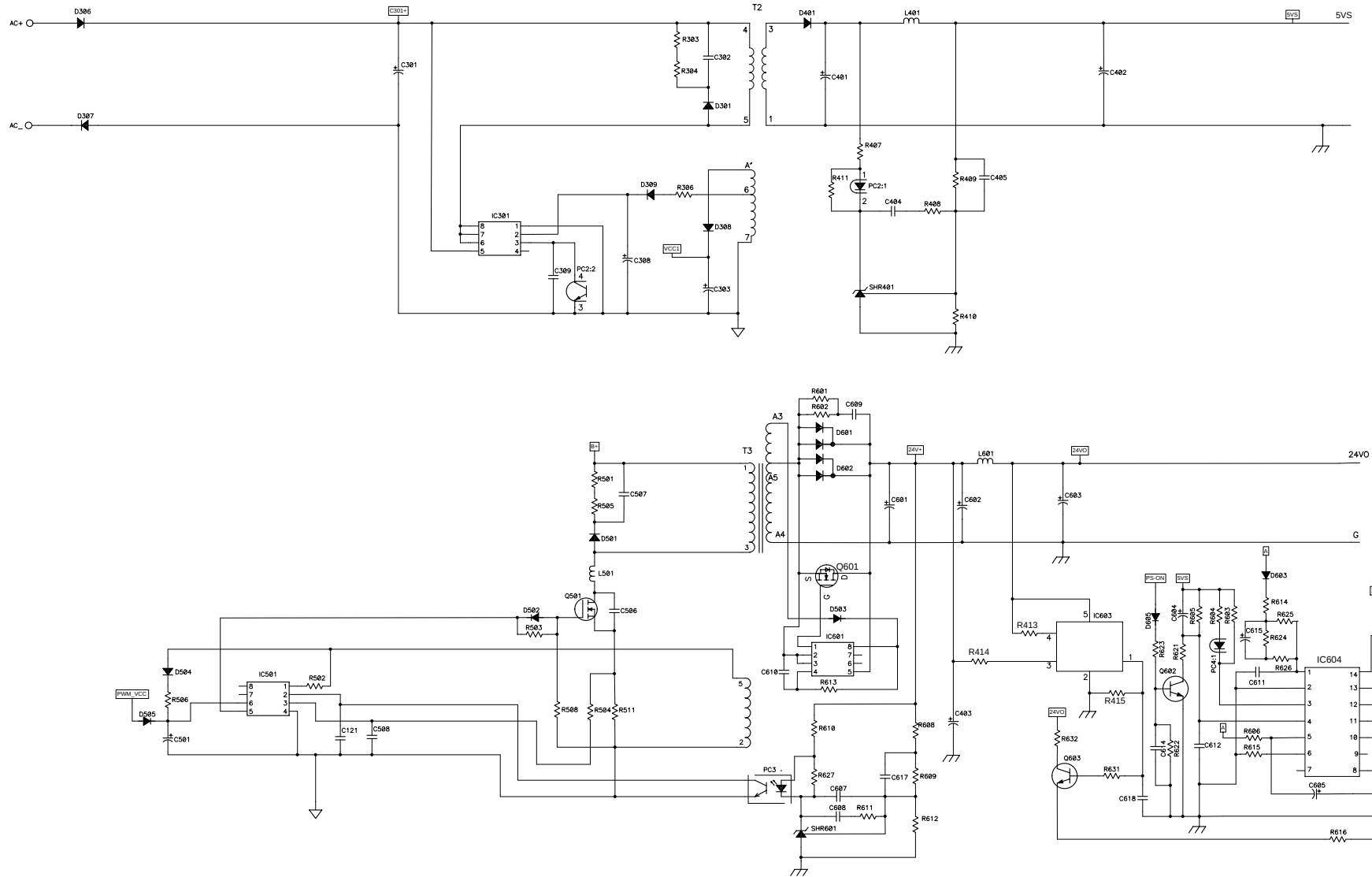
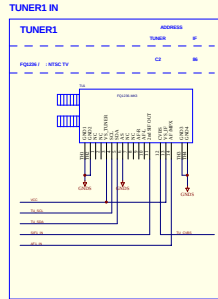


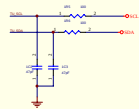
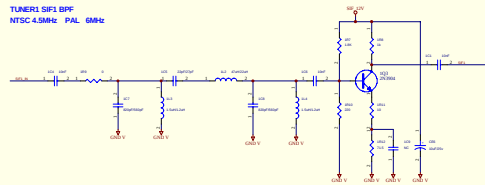




Title		
PHIHONG TECHNOLOGY CO.,LTD.		
Size	Number	Rev
A2	PSA200-412	01
Date	LI PING '05/07/12	
Filename	Sheet 2 of 2	



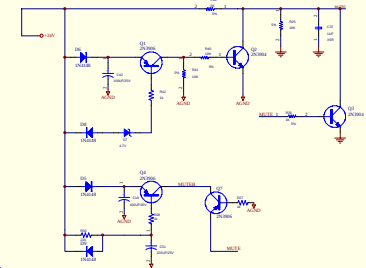
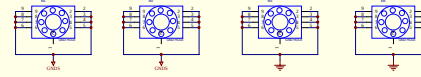
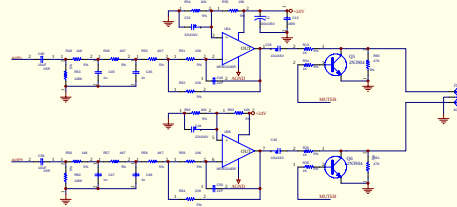
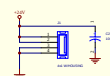
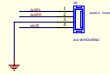
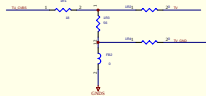
TUNER1 SPI BPF
NTSC 4.5MHz PAL 6MHz



AUDIO GROUND

VIDEO GROUND

AV TUNER ID



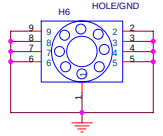
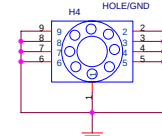
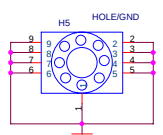
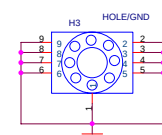
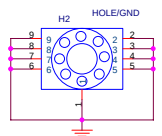
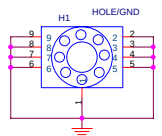
REV	1.0	DATE	2010-01-01
BY	DESIGN	DATE	2010-01-01
CHKD	DESIGN	DATE	2010-01-01
APPD	DESIGN	DATE	2010-01-01

MT8205P1V3.2

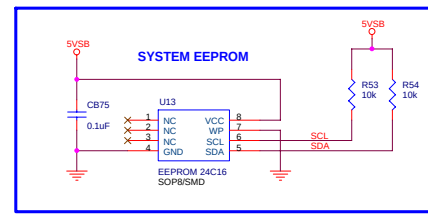
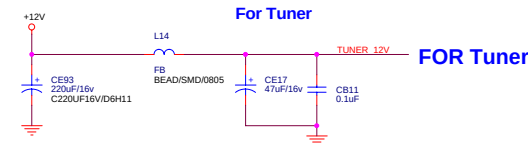
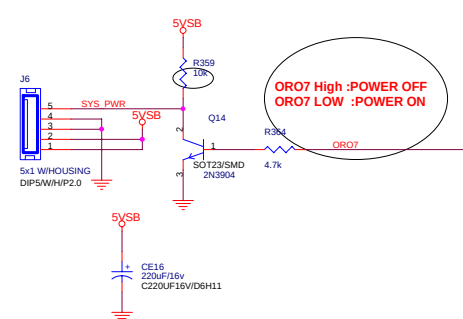
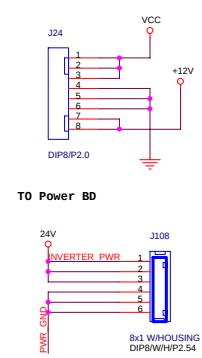
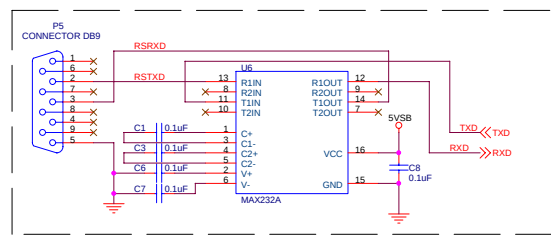
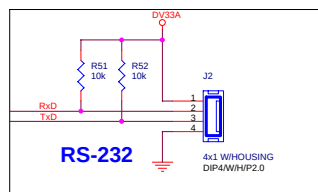
MT8205E (PBGA388) LCDTV BOARD 4 LAYERS

1. INDEX
2. LDO
3. MT8205E PBGA388
4. MT8205 ANALOG DECOUPLING
5. MT8205 DIGITAL DECOUPLING
6. ICE I/F
7. DDR MEMORY & FLASH
8. DVI INPUT
9. AD9883
10. VGA IN & PC AUDIO IN
11. VIDEO IN & TUNER I/O
12. AUDIO / VIDEO IN CIRCUIT
13. WM8776 AUDIO CODEC
14. LVDS / CRT / TTL OUT
15. BACK LIGHT / KEYPAD
16. READER CARD I/F

UP3_0 BLACKLIGHT ON/OFF
MUTE<-->UP1_5

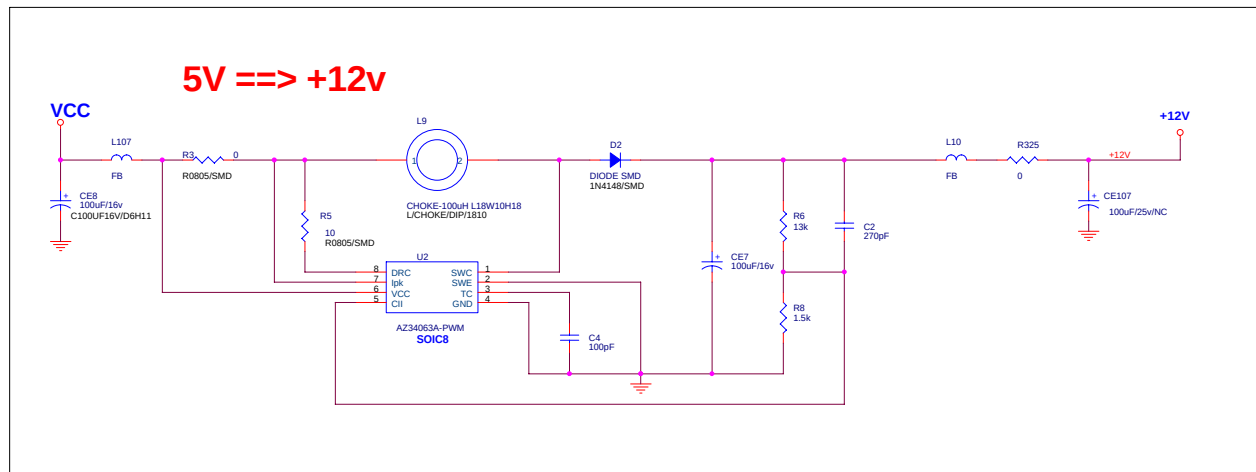
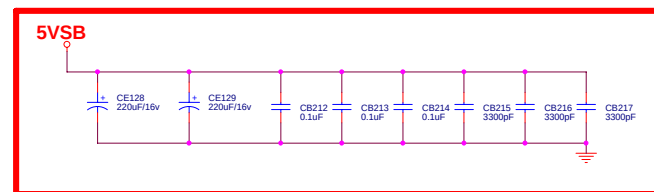
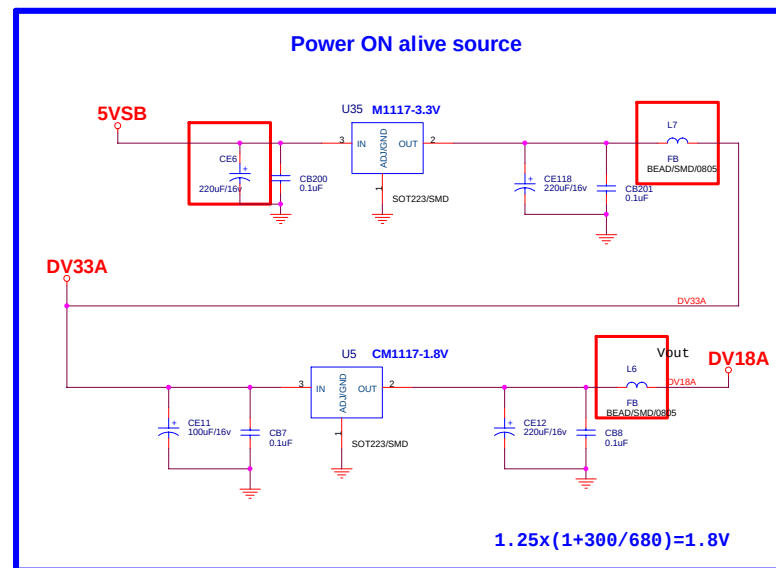
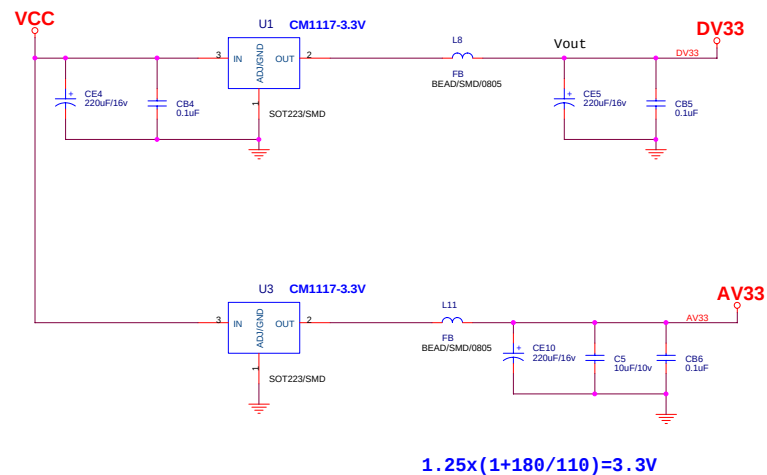


AUDIO INOUT GND ANALOG INPUT GND DIGITAL GND



Rev	History	P#	Date
V0.1	1.FIRST VERSION		2004/11/29
V0.2	1. 2.5V use 5VSB conversion. All 0402 change to 0603 2. LED_RED/GRN CIRCUIT CHANGE 3. Change Power on/off circuit(ORO7 CTRL PIN) 4. WM8776 DVDD Needed 5. Del the Si9936 LVDS Parts 6. Del DVI&AD9883 PARTS		2005/03/09

TXD	TXD	3
RXD	RXD	3
SCL	SCL	6.9.11.13
SDA	SDA	6.9.11.13
+12V	+12V	2
TUNER_12V	TUNER_12V	11
ORO7	ORO7	3
MUTE	UP1_5	6

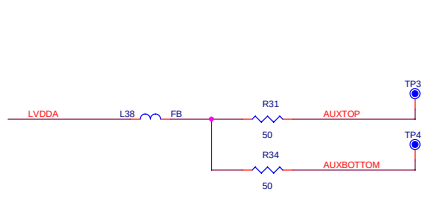
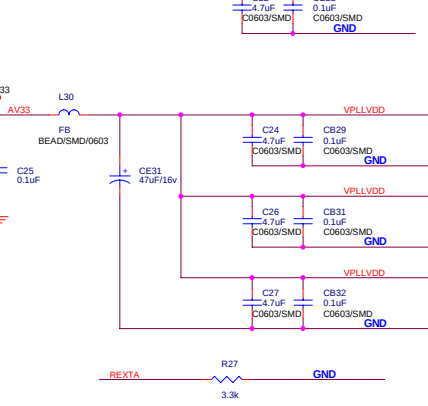
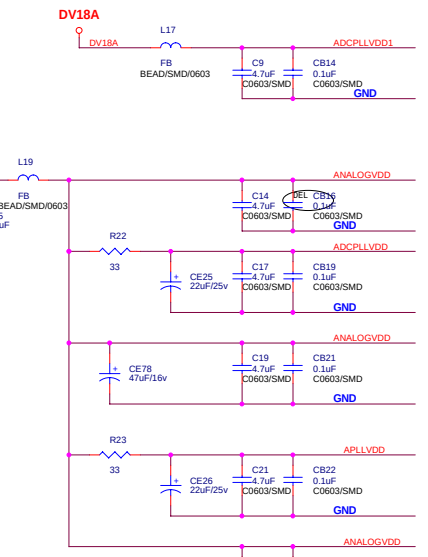
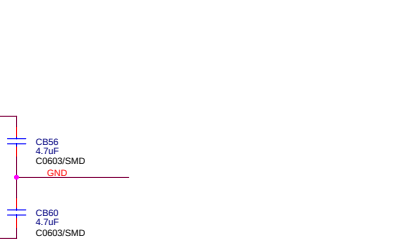
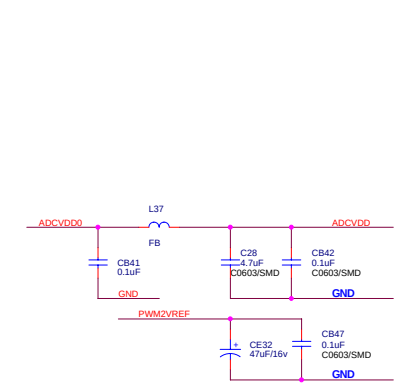
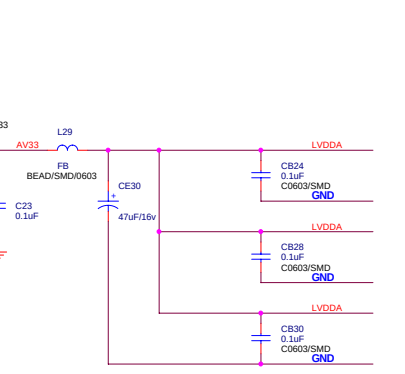
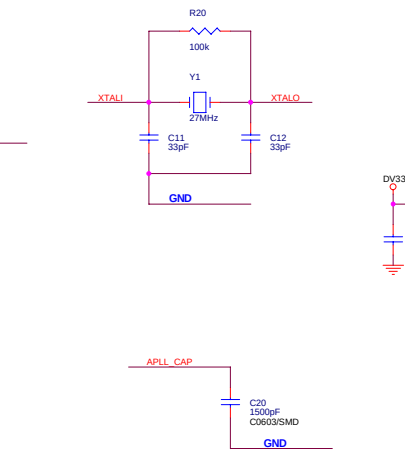
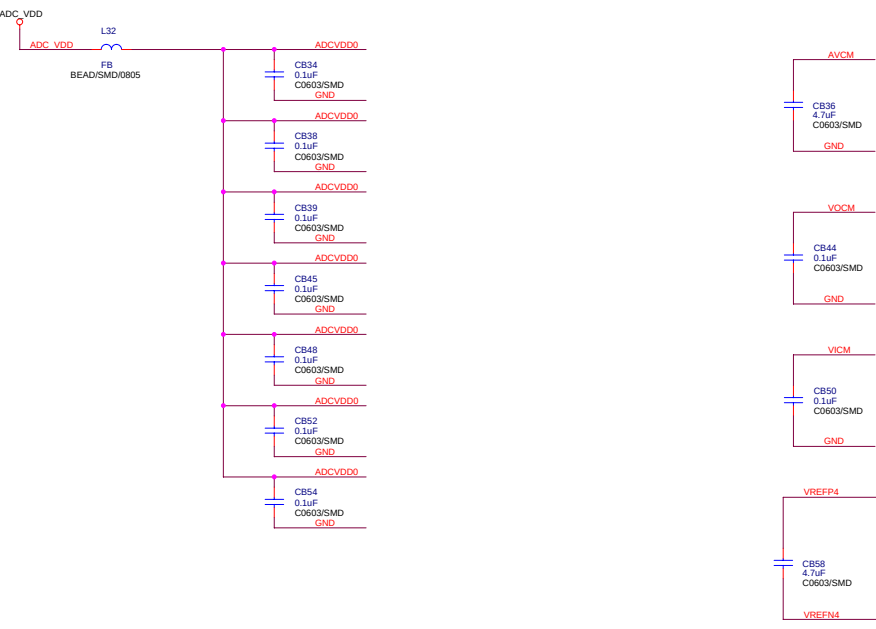
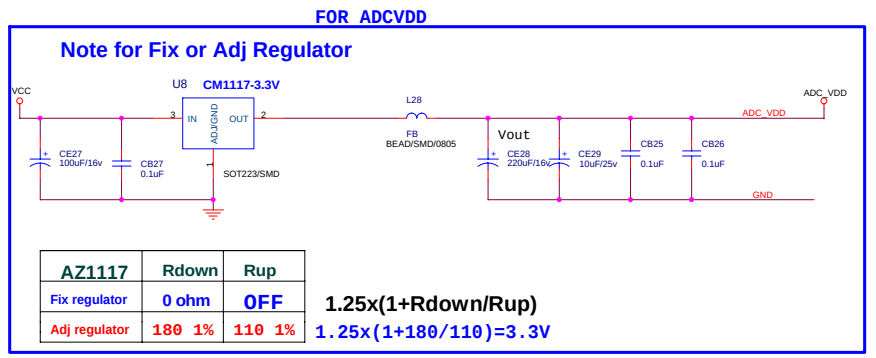
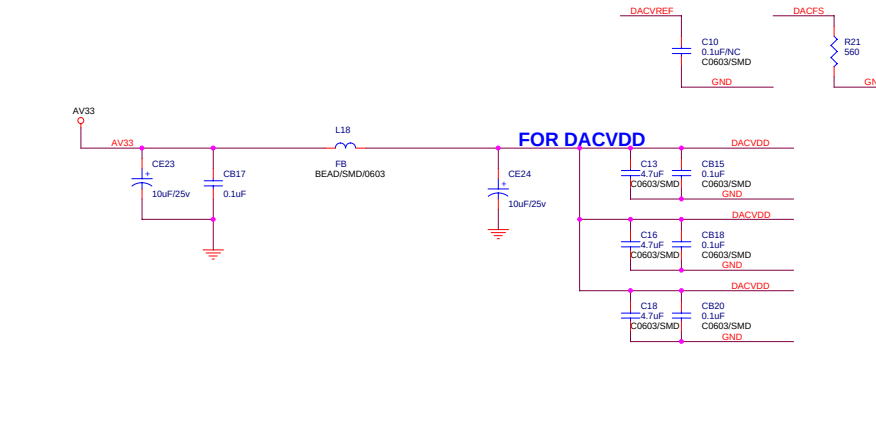


MT8205 ANALOG DECOUPLING

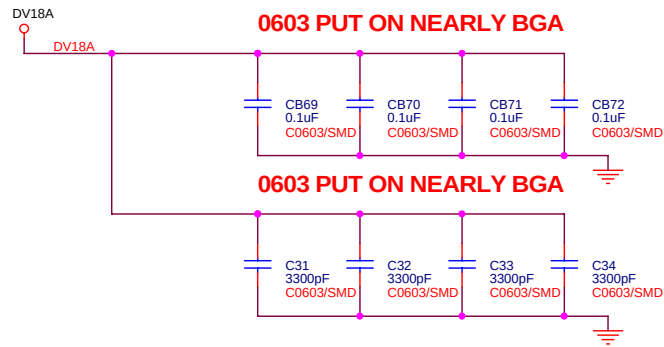
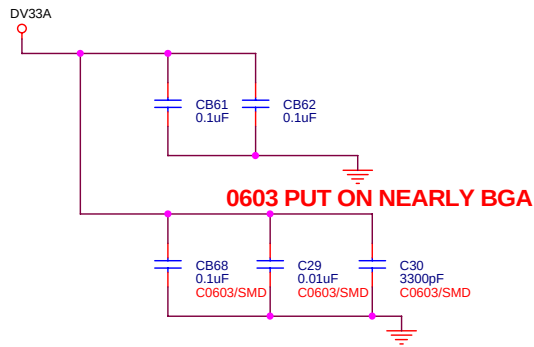
DACVREF >> DACVREF 3
 DACFS >> DACFS 3
 ADCPLLVD01 >> ADCPLLVD01 3
 ADCPLLVD >> ADCPLLVD 3
 APLLVD >> APLLVD 3
 ANALGVDD >> ANALGVDD 3
 VPLLVD >> VPLLVD 3
 LVDDA >> LVDDA 3
 ADCVDD >> ADCVDD 3
 DACVDD >> DACVDD 3
 AVCM >> AVCM 3
 VOCM >> VOCM 3
 VICM >> VICM 3

VREFP4 >> VREFP4 3
 VREFN4 >> VREFN4 3
 ADCVDD0 >> ADCVDD0 3
 PWMZVREF >> PWMZVREF 3
 AUXTOP >> AUXTOP 3
 AUXBOTTOM >> AUXBOTTOM 3
 REXTA >> REXTA 3
 APLL_CAP >> APLL_CAP 3
 XTALI >> XTALI 3
 XTALO >> XTALO 3

ADC_VDD >> ADC_VDD 3
 LVDDA >> LVDDA 3
 VPLLVD >> VPLLVD 3
 VREFP4 >> VREFP4 3
 VREFN4 >> VREFN4 3



MT8205 DIGITAL POWER & DECOUPLING



MiCO Confidential		
Title		
MiCO LCD TV - MediaTek MT8205 Solution		
Size	Doc Number	Rev
B	MT8205 DECOUPOMG--DIGITAL	v0.1
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SCL SDA 1,9,11,13 1,9,11,13

IOA[0..7] 3
ICE 3

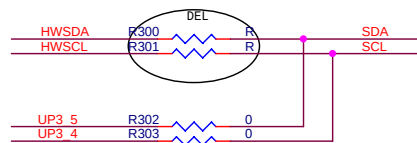
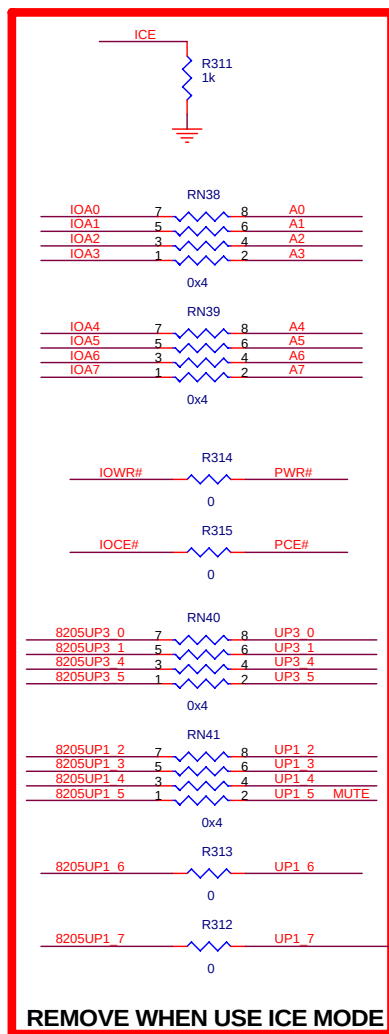
IOWR# 3
IOCE# 3
PWR# 7
PCE# 7

A[0..7] 7
F_A[8..21] 3,7

UP1_2 UP1_2 15
UP1_6 UP1_6 11
UP1_3 UP1_3 11
UP3_1 UP3_1 15
UP1_4 UP1_4 1
UP1_5 UP1_5 1
UP3_0 UP3_0 1
HWSCL HWSCL 3
HWSDA HWSDA 3

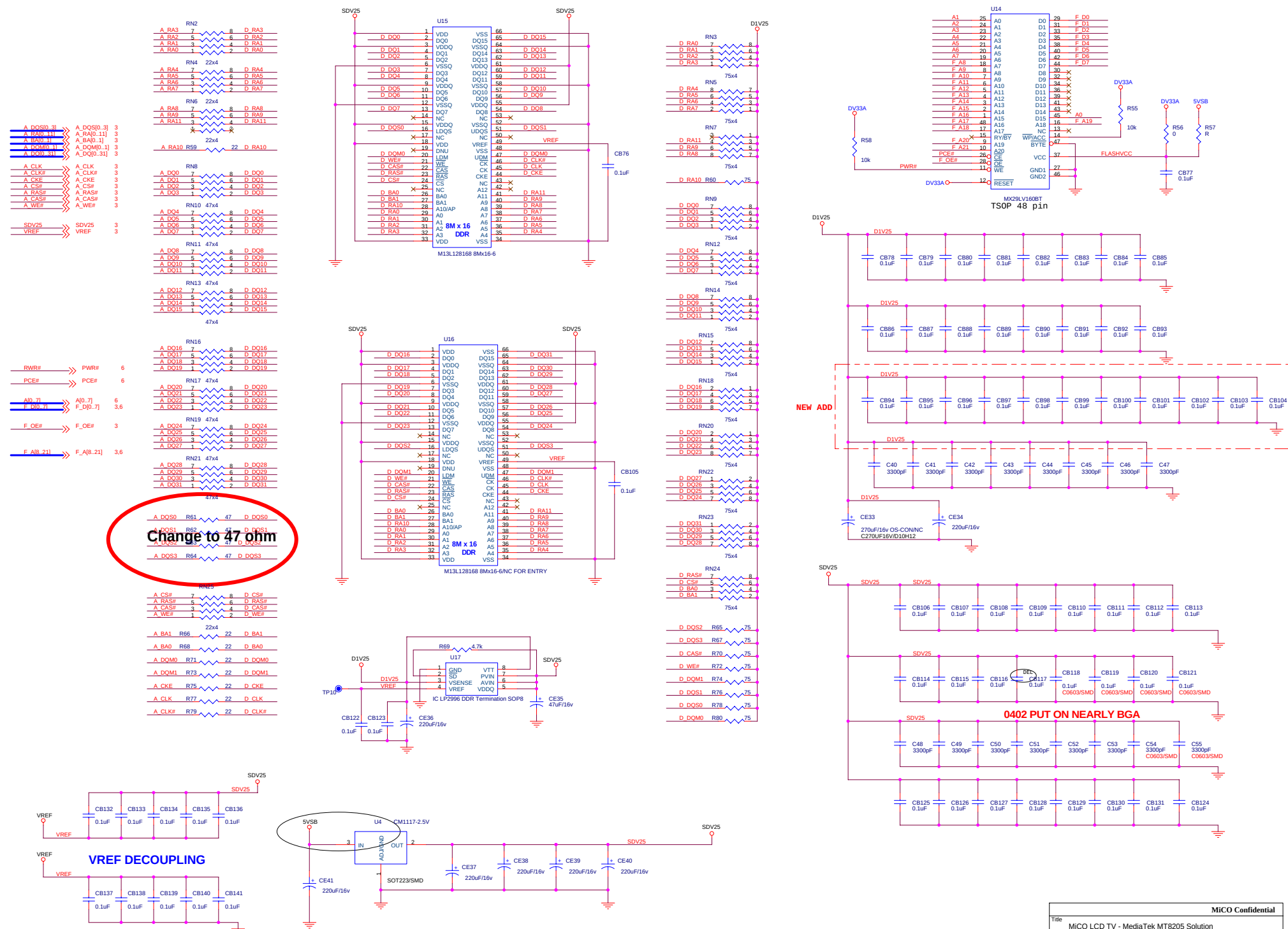
8205UP3_0 8205UP3_0 3
8205UP3_1 8205UP3_1 3
8205UP1 [2..7] 8205UP1_[2..7] 3
8205UP3_4 8205UP3_4 3
8205UP3_5 8205UP3_5 3

UP3_4 FOR S/W SCL
UP3_5 FOR S/W SDA



TP28
TEST POINT DIP1.0
TP/DIP/D1.0

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Title MiCO LCD TV - MediaTek MT8205 Solution			
Size B	Doc Number MT8205 DECOUPOMG--DIGITAL	Rev V0.1	
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Y >> Y 12
Y_GND >> Y_GND 12
CB >> CB 12
CB_GND >> CB_GND 12
CR >> CR 12
CR_GND >> CR_GND 12
SOY >> SOY 3

SY >> SY 12
SY_GND >> SY_GND 12

SC >> SC 12
SC_GND >> SC_GND 12

TV >> CVBS0 12
TV_GND >> CVBS0_GND 12

CVBS1 >> CVBS1 12
CVBS1_GND >> CVBS1_GND 12

SIF1_OUT >> SIF1_OUT 12
AF1_OUT >> AF1_OUT 12

SCL >> SCL 1,6,9,13
SDA >> SDA 1,6,9,13

TUNER 12V >> TUNER_12V 1

AV , TUNER I/O

TU VCC FOR TUNER POWER USE

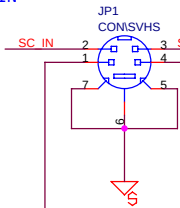
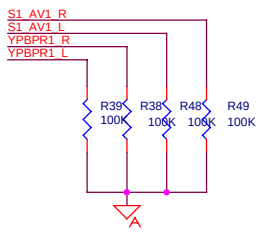
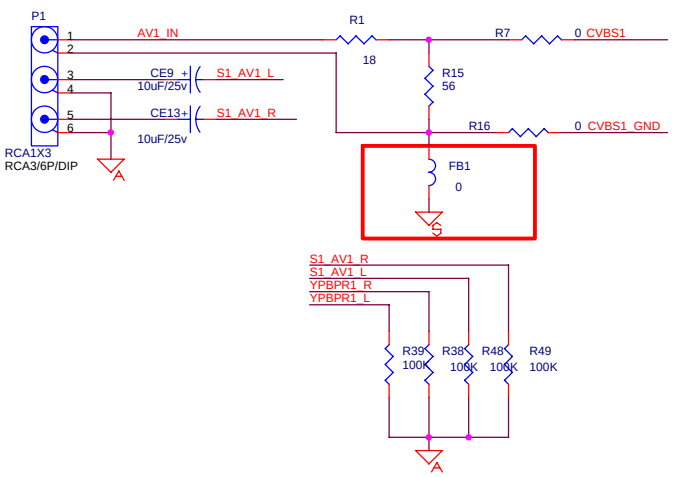
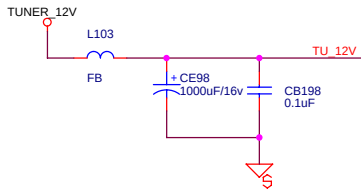
CVBS0---TUNER1
CVBS1---FRONT BD AV_IN
CVBS2---AV BD (AV/S-V) IN

OGO3 FOR AV SWITCH IO-1
ORO7 FOR TUNER SIF SELECT SWITCH IO-1
OGO0 FOR TUNER SIF SELECT SWITCH IO-2
OGO1 FOR TUNER BOARD PAL/NTSC SWITCH
ORO6 FOR AUDIO 1st SWITCH IO-1
OGO5 FOR AUDIO 1st SWITCH IO-2
OGO6 FOR AUDIO 2nd SWITCH IO-1
OGO2 FOR AUDIO 2nd SWITCH IO-2
OGO4 FOR AUDIO 2nd SWITCH IO-2

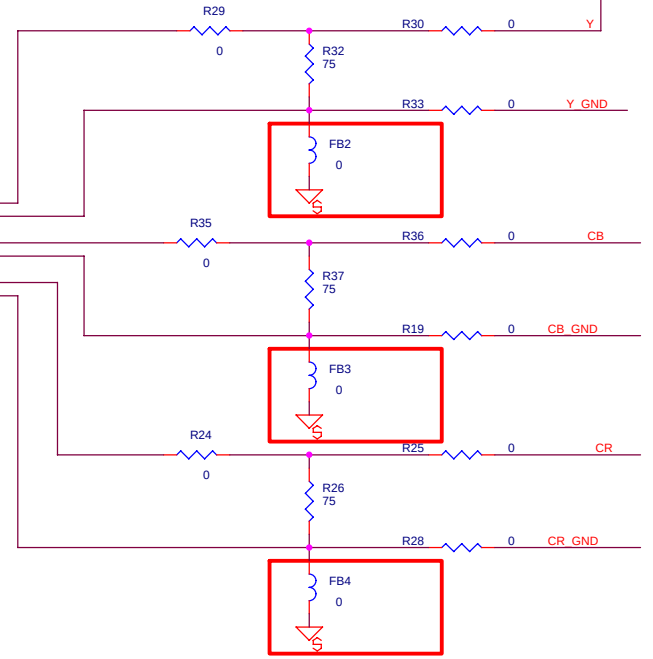
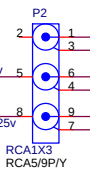
UP1_2 FOR POWER ON/OFF
ORO7 FOR SYS_PWR

OGO0 >> OGO0 3
OGO1 >> OGO1 3
OGO2 >> OGO2 3
OGO3 >> OGO3 3
OGO4 >> OGO4 3
OGO5 >> OGO5 3
OGO6 >> OGO6 3
ORO6 >> ORO6 3
ORO7 >> ORO7 3

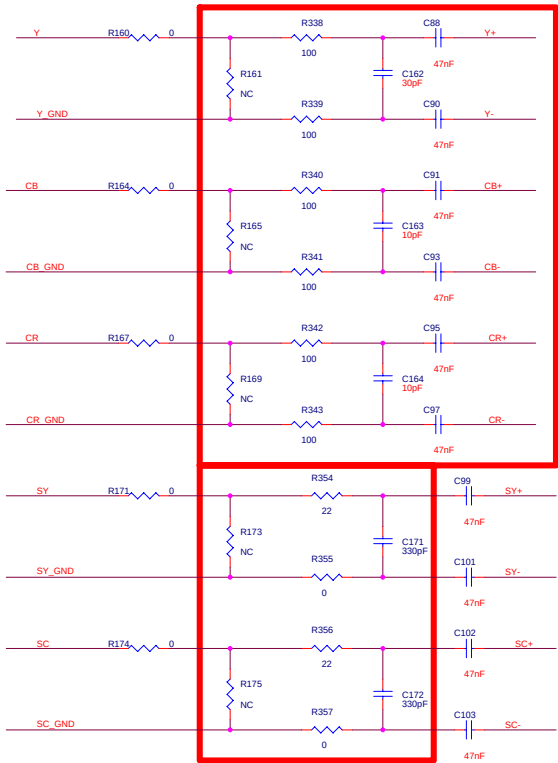
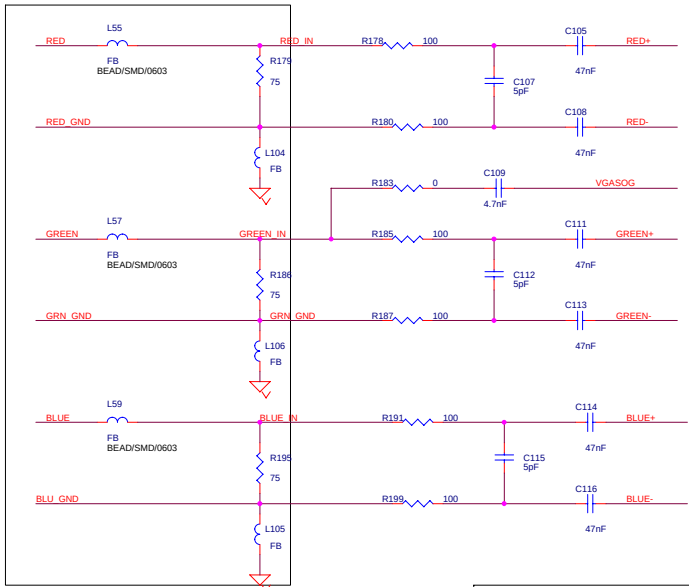
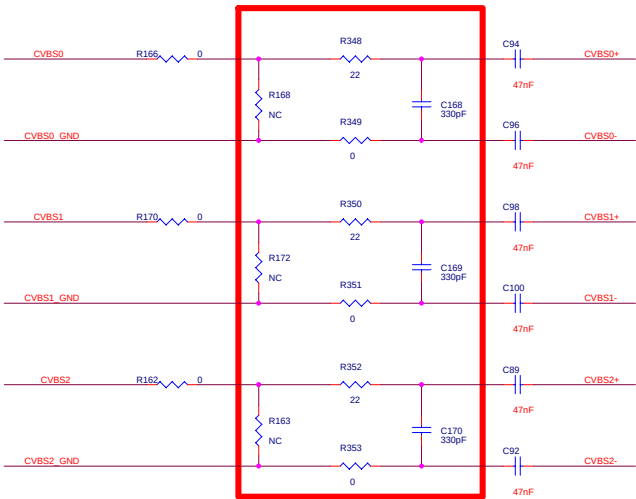
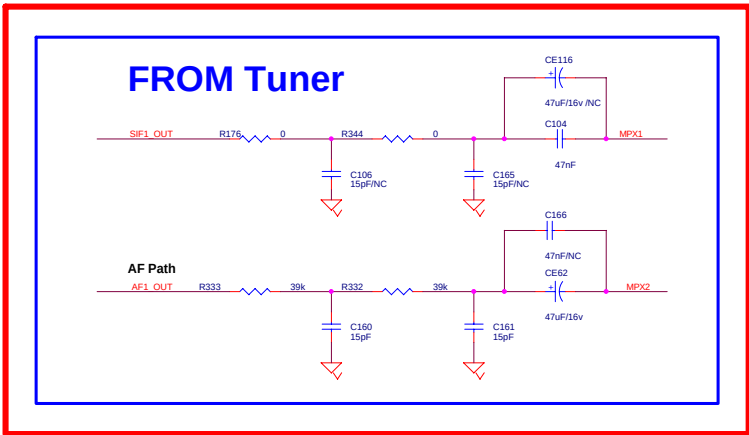
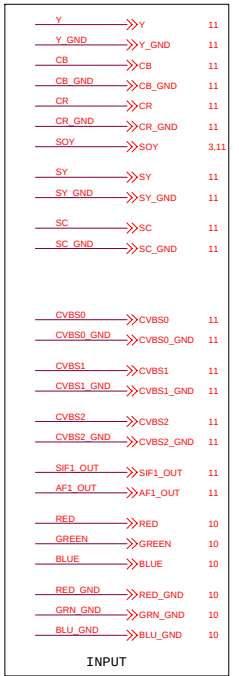
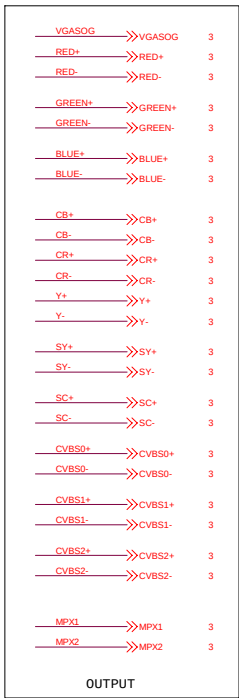
S1_AV1_L >> S1_AV1_L 11
S1_AV1_R >> S1_AV1_R 11
YPBPR1_L >> YPBPR1_L 11
YPBPR1_R >> YPBPR1_R 11

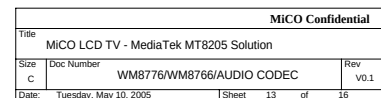
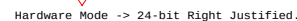


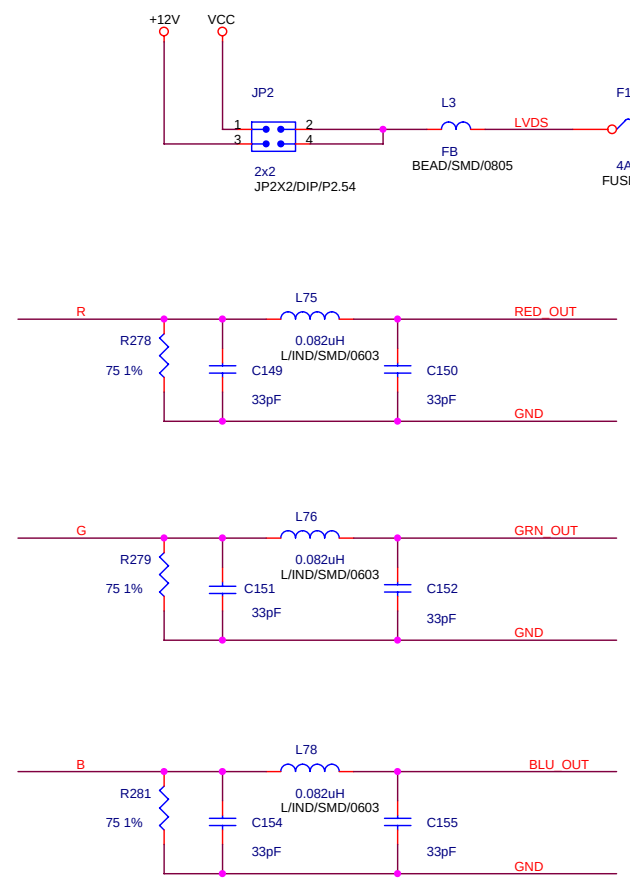
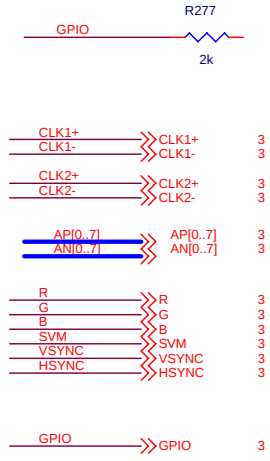
YPBPR



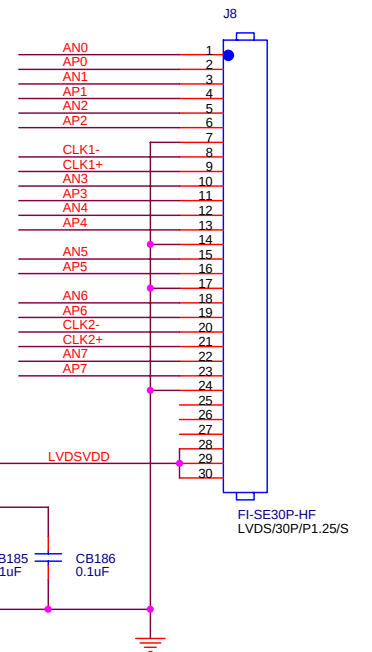
MiCO Confidential			
Title	MiCO LCD TV - MediaTek MT8205 Solution		
Size A3	Doc Number	VIDEO IN & TUNER IO	Rev V0.1
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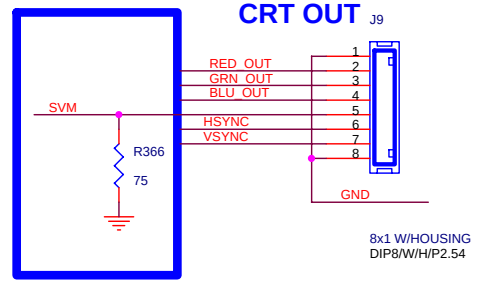




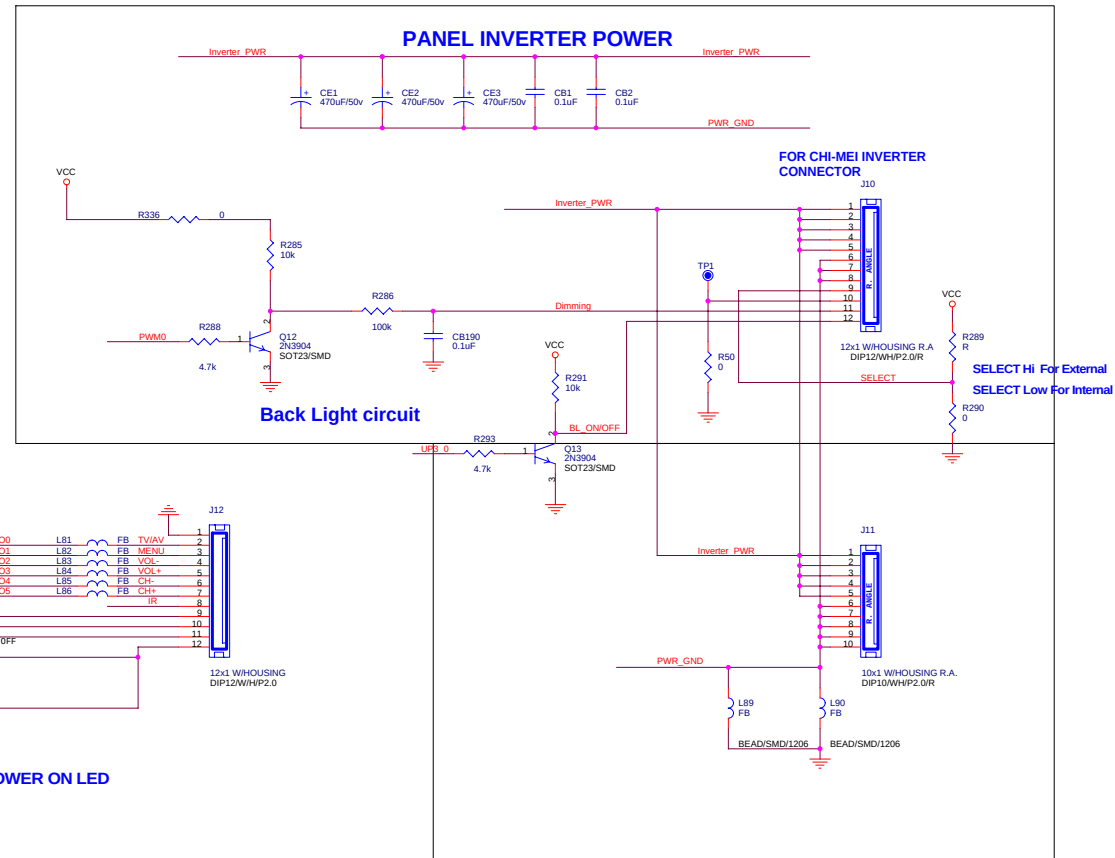
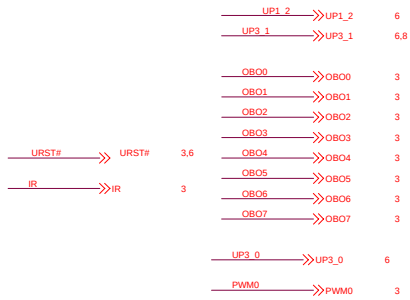
LVDS OUT

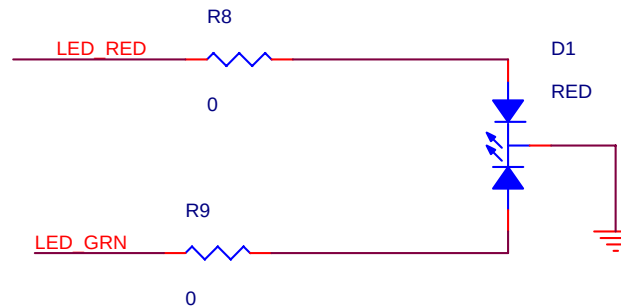
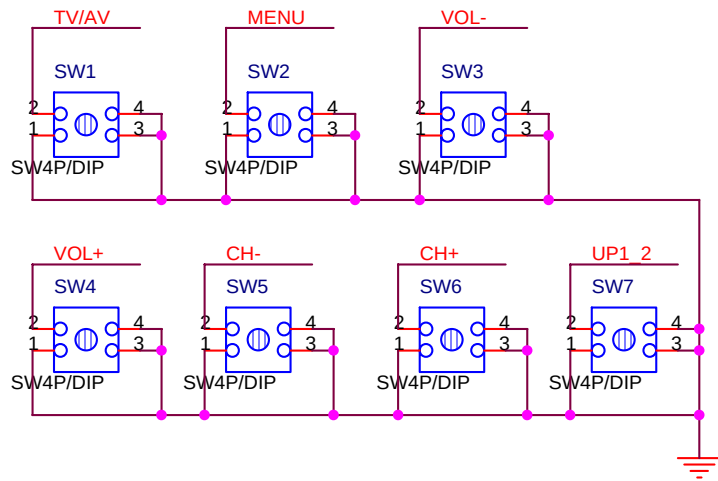
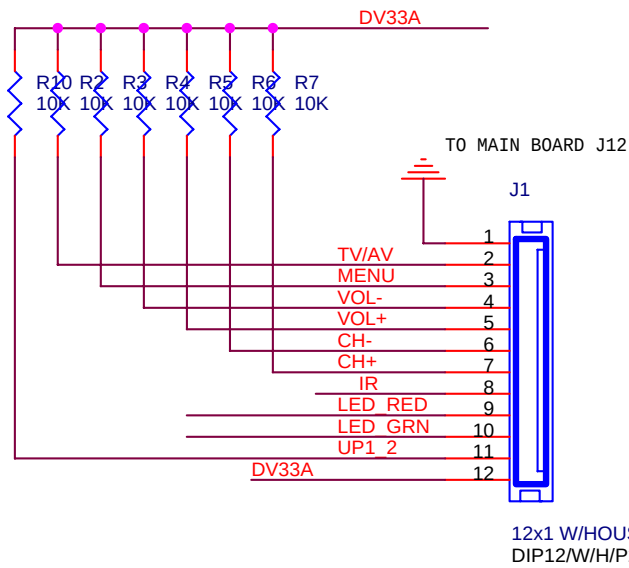
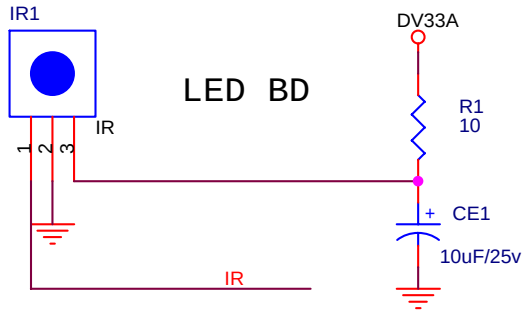


CRT OUT J9



MiCO Confidential			
Title MiCO LCD TV - MediaTek MT8205 Solution			
Size B	Doc Number LVDS/CRT OUT		Rev V0.1
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GREEN

Title		
<Title>		
Size	Document Number	Rev
A	<Doc>	0.1
Date:	Sunday, March 27, 2005	Sheet 1 of 1