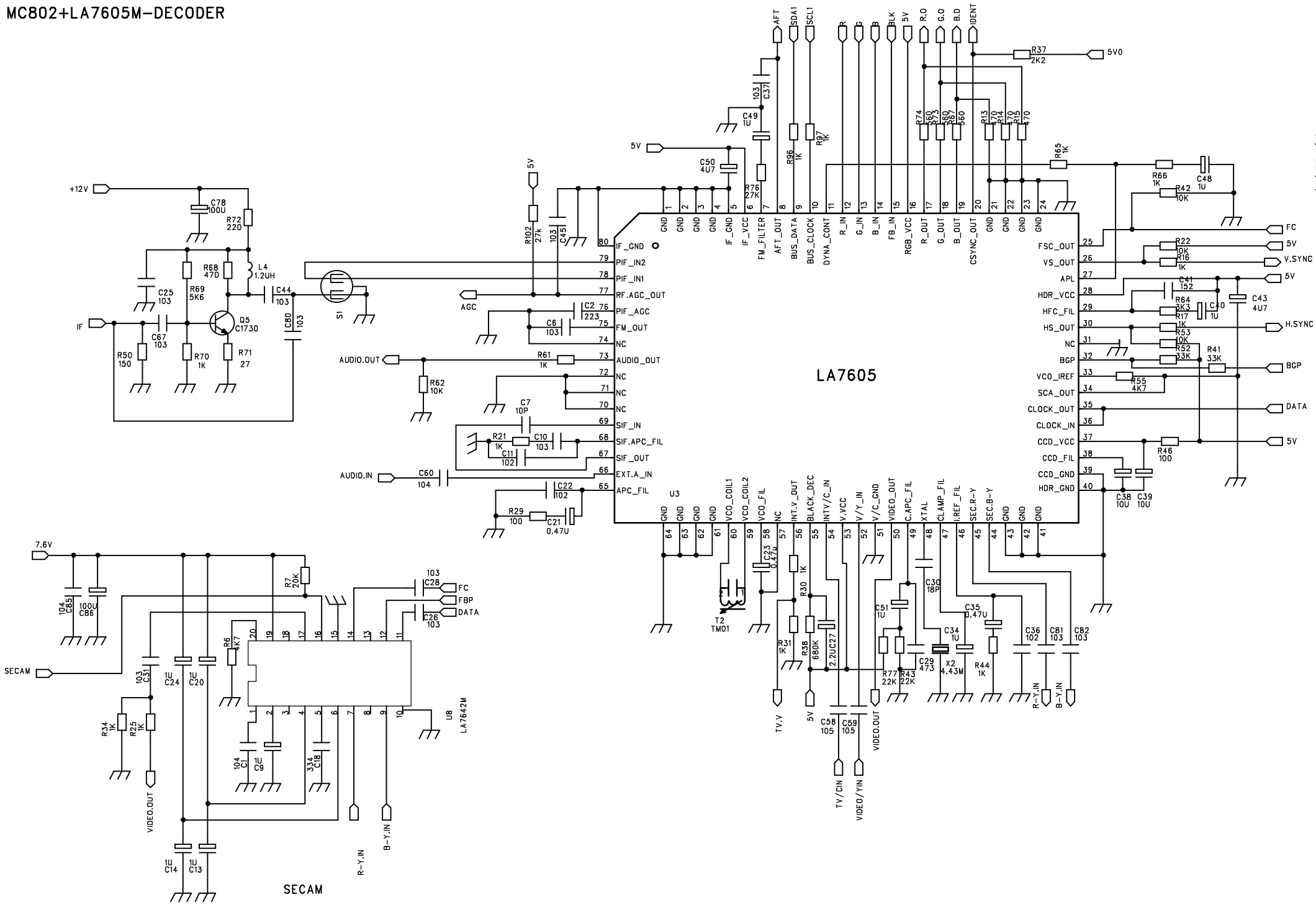
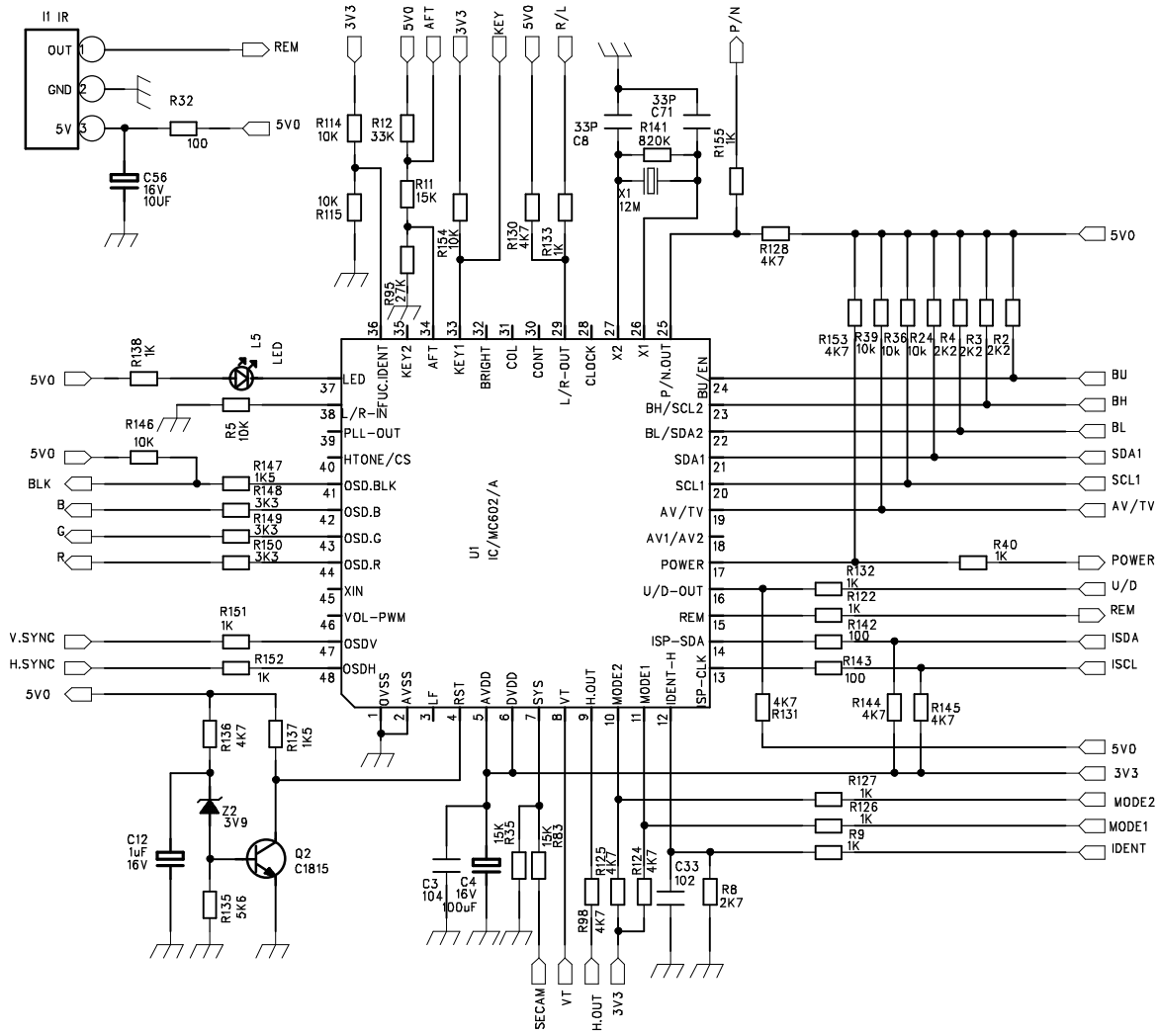
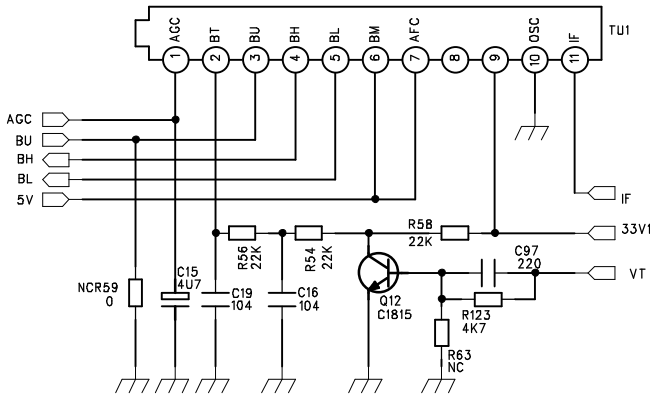
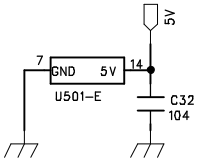
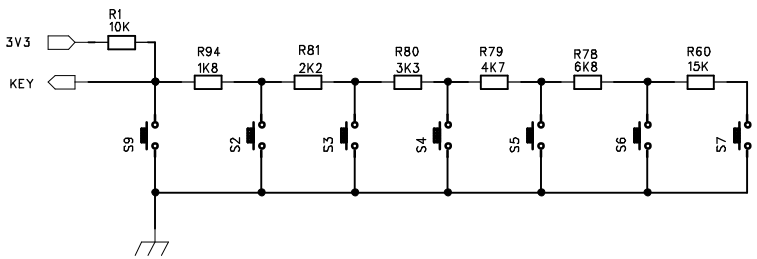
MC802+LA7605M-DECODER



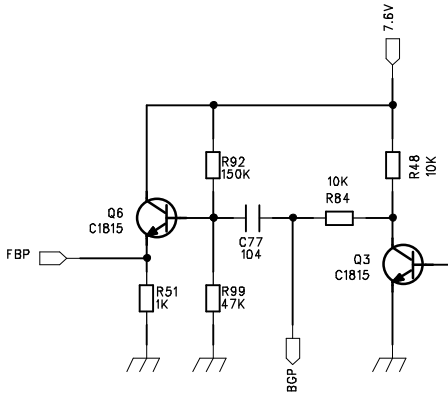

U1
IC/MC602/A
IR
REM
R32
100
C56
16V
10UF
R114
10K
R12
33K
R11
15K
R115
10K
R154
10K
R130
4K7
R133
1K
R95
27K
33P
C71
33P
C8
R141
820K
X1
12M
R155
1K
P/N
R128
4K7
R153
4K7
R39
10k
R36
10k
R24
10k
R4
2K2
R3
2K2
R2
2K2
5V0
BU
BH
BL
SDA1
SCL1
AV/TV
POWER
U/D
REM
ISDA
ISCL
R40
1K
R132
1K
R122
1K
R142
100
R143
100
R144
4K7
R145
4K7
R131
4K7
R138
1K
L5
LED
R5
10K
R146
10K
R147
1K5
R148
3K3
R149
3K3
R150
3K3
BLK
B
G
R
R151
1K
R152
1K
V.SYNC
H.SYNC
R136
4K7
R137
1K5
Z2
3V9
C12
1uF
16V
Q2
C1815
R135
5K6
C3
104
C4
16V
100uF
15K
R35
15K
R83
R125
4K7
R124
4K7
R98
4K7
C33
102
R8
2K7
R127
1K
R126
1K
R9
1K
MODE2
MODE1
IDENT
SECAM
VT
H.OUT
3V3
LED
L/R-IN
PLL-OUT
HTONE/CS
OSD.BLK
OSD.B
OSD.G
OSD.R
XIN
VOL-PWM
OSDV
OSDH
OVSS
AVSS
LF
RST
AVDD
DVDD
SYS
VT
H.OUT
MODE2
MODE1
IDENT-H
SP-CLK
BU/EN
BH/SCL2
BL/SDA2
SDA1
SCL1
AV/TV
AV1/AV2
POWER
U/D-OUT
REM
ISP-SDA
FUC.IDENT
KEY2
AFT
KEY1
BRIGHT
COL
CONT
L/R-OUT
CLOCK
X2
X1
P/N.OUT

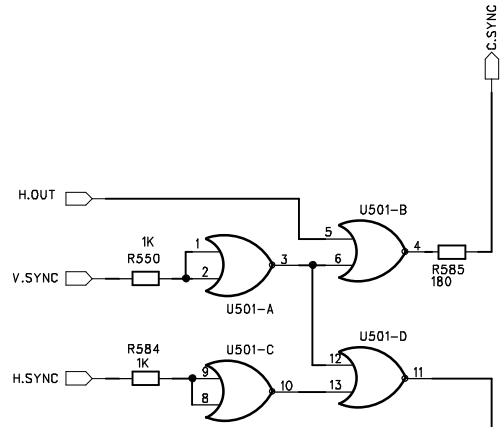

TU1
AGC
BT
BU
BH
BL
BM
AFC
OSC
IF
AGC
BU
BH
BL
5V
IF
33V1
VT
R58
22K
R56
22K
R54
22K
C97
220
NCR59
0
C15
4U7
C19
104
C16
104
Q12
C1815
R123
4K7
R63
NC


5V
GND
5V
U501-E
C32
104


R1
10K
3V3
KEY
R94
1K8
R81
2K2
R80
3K3
R79
4K7
R78
6K8
R60
15K
S9
S2
S3
S4
S5
S6
S7

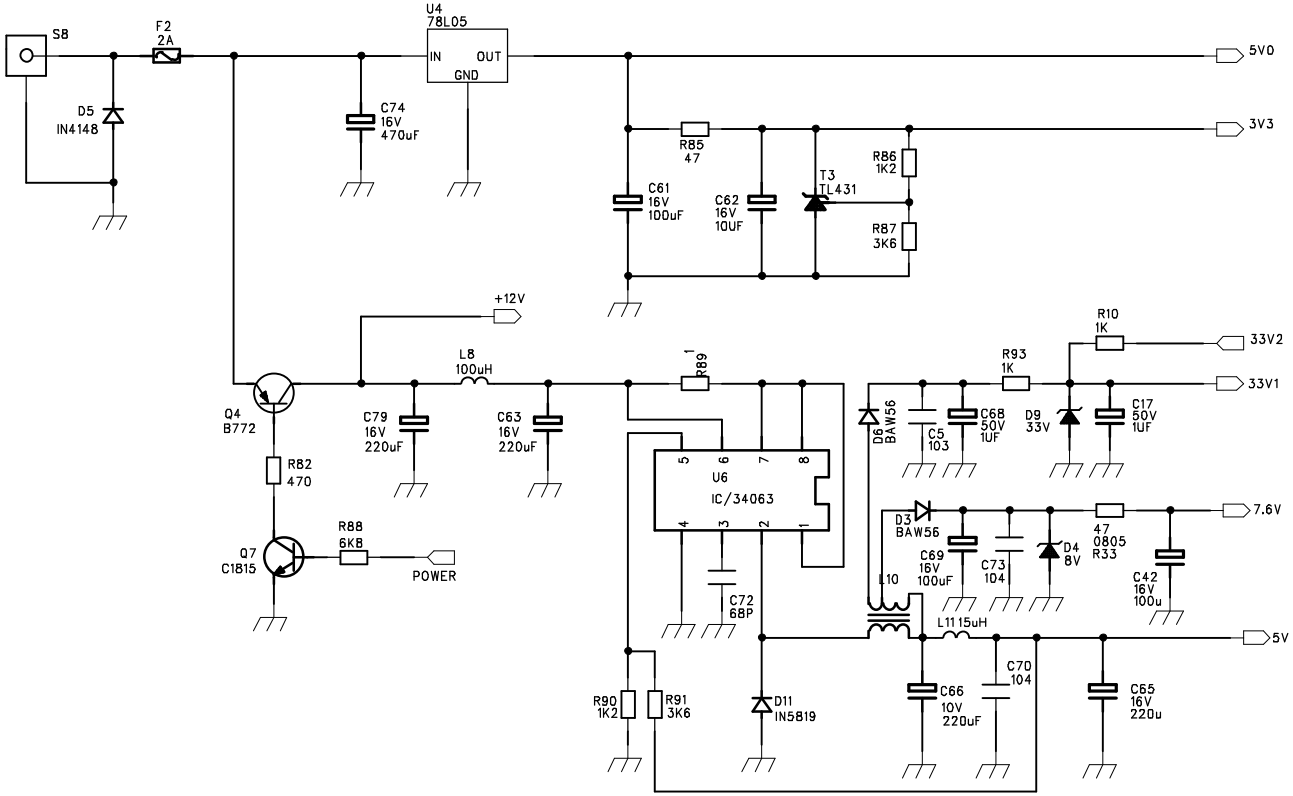
KEY


7.6V
R92
150K
R48
10K
10K
R84
Q6
C1815
C77
104
Q3
C1815
FBP
R51
1K
R99
47K
BGP

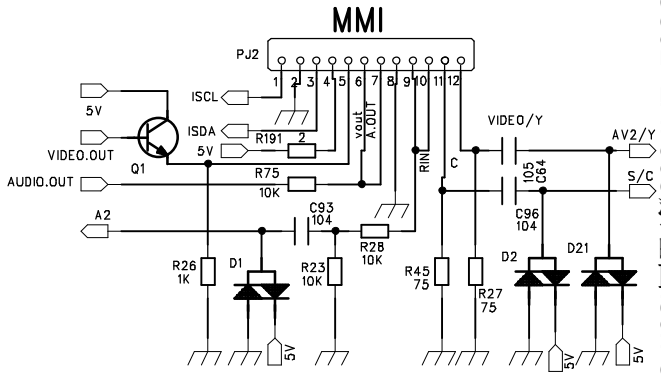

H.OUT
V.SYNC
H.SYNC
1K
R550
U501-A
U501-B
R585
180
R584
1K
U501-C
U501-D
SYNC.OUT

SYNC

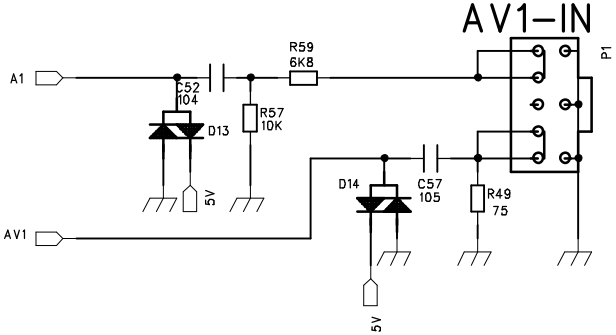
MC802+LA7605-POWER



MMI

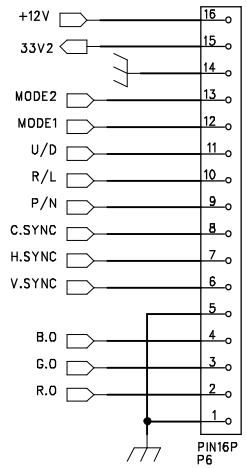


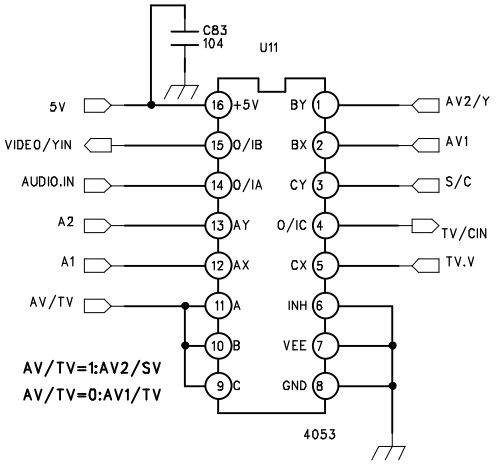
AV1-IN

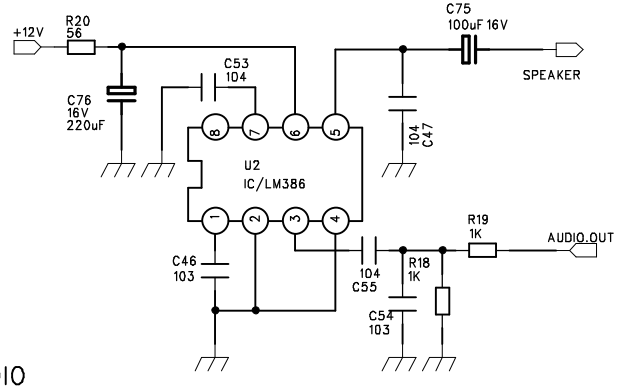


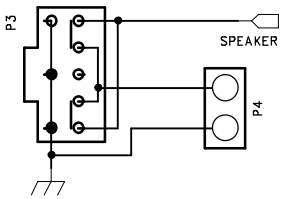
MC802+LA7605原理图.sch-3 - Sat Jul 03 15:31:16 2004

to:M1/M2/M3

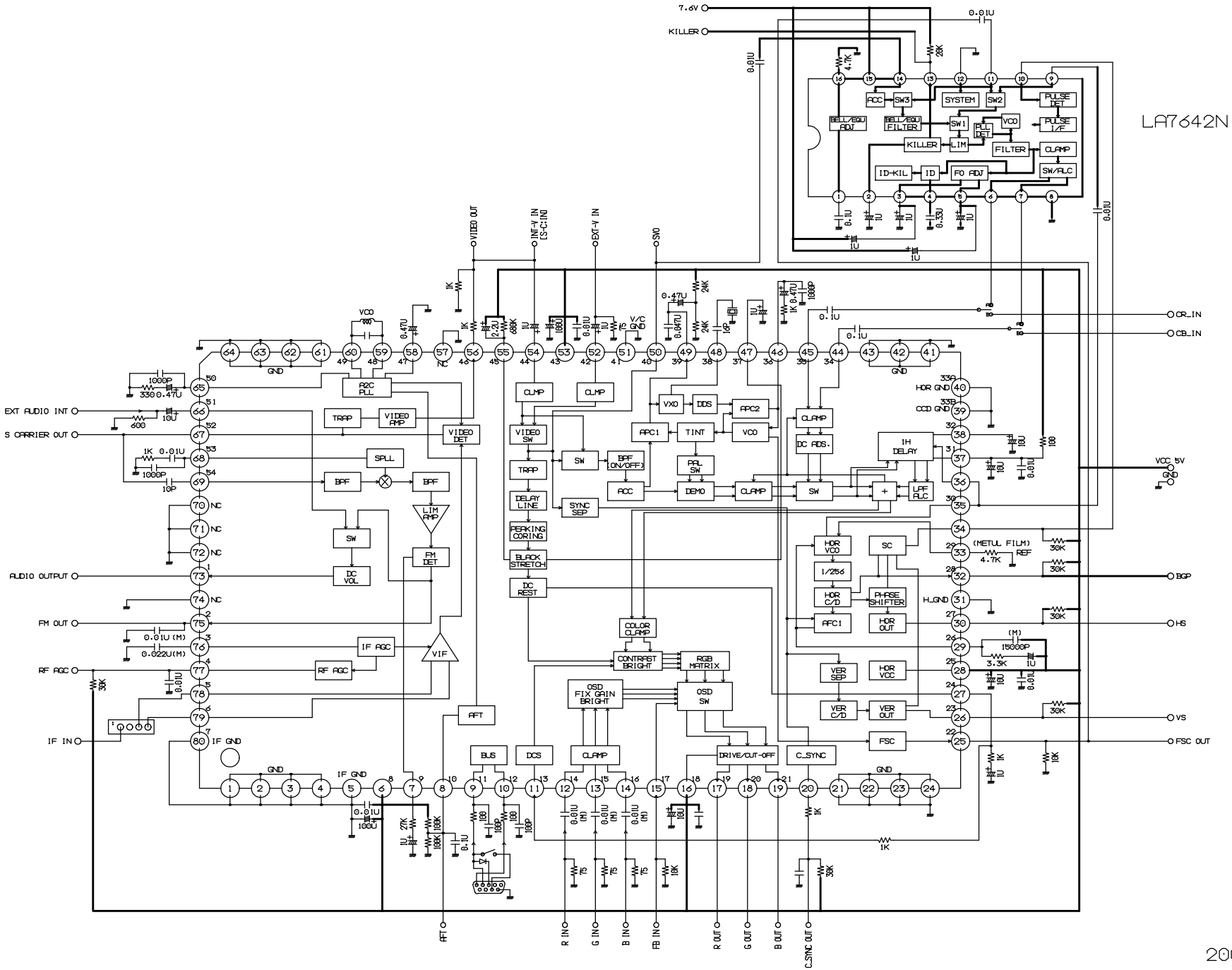








AUDIO



2003-09-26

LA7605NM(QFP-80E) 評価基板

LA7605NM BUS Control Register Bit Allocation Map

IC Address (WRITE) : 10111010

Control Register Bit Allocations								
Sub Address	MSB			DATA BITS				LSB
	DA0	DA1	DA2	DA3	DA4	DA5	DA6	DA7
00000000	T_Disable	AFC gain&gate	H.FREQ					
	1	0	0	1	1	1	1	1
00001	*	Audio.Mute	Video.Mute	Sync_Kill	RGB/YUV SW	**Gray_Mode**	Cross B/W	
	0	0	0	0	0	0	0	0
00010	*	*	*	*	*	*	*	*
	0	0	0	0	0	0	0	0
00011	*	*	*	*	*	*	*	*
	0	0	0	0	0	0	0	0
00100	H BLK L			*	*	*	*	*
	1	0	0	0	0	0	0	0
00101	H BLK R			*	*	*	*	*
	1	0	0	0	0	0	0	0
00110	*	*	*	*	*	*	*	*
	0	0	0	0	0	0	0	0
00111	R.BIAS							
	0	0	0	0	0	0	0	0
01000	G.BIAS							
	0	0	0	0	0	0	0	0
01001	B.BIAS							
	0	0	0	0	0	0	0	0
01010	**RGB.Test 4**	R.DRIVE						
	0	1	1	1	1	1	1	1
01011	Drive.Test	Half tone		Half tone Def	G.DRIVE			
	0	0	1	1	1	0	0	0
01100	**A2.SW**	B.DRIVE						
	0	1	1	1	1	1	1	1
01101	Blank.Def	Sub.Bias						
	1	1	0	0	0	0	0	0
01110	A.MONI.SW	Bright						
	0	1	0	0	0	0	0	0
01111	S.TRAP.SW	Contrast						
	1	1	0	0	0	0	0	0

(Bits are transmitted in this order.)

LA7605NM BUS Control Register Bit Allocation Map

Control Register Bit Allocations (continued)								
Sub Address	MSB			DATA BITS				LSB
	DA0	DA1	DA2	DA3	DA4	DA5	DA6	DA7
00010000	OSD Cnt.Test	OSD Contrast						
	0	1	0	0	0	0	0	0
10001	Coring Gain(W/Defeat)		Sharpness					
	0	0	0	0	0	0	0	0
10010	Tint.Test	Tint						
	0	1	0	0	0	0	0	0
10011	Color.Test	Color						
	0	1	0	0	0	0	0	0
10100	Video SW	S.Trap Test			Filter.Sys			
	0	1	0	0	0	0	1	0

10101	*	R_UP 1	R_UP 2	CbCr_IN	G-Y Angle	Color killer ope.		
	0	0	0	0	0	1	0	0
10110	C.Trap Test			Y_APF	Pre/Over-shoot adj.		WPL Ope. Point(W/Defeat)	
	1	0	0	0	0	0	0	0
10111	Y Gamma Start		DC.Rest		Blk.Str.start(W/Defeat)		Blk.Str.Gain	
	0	0	0	0	0	0	0	0
11000	Auto.Flesh	C.Ext	C.Bypass	C_Kill ON	C_Kill OFF	Color.Sys		
	0	0	1	0	0	0	0	0
11001	Cont.Test	Digital OSD	Dyn-C Defeat	Dyn-C Gain	RGB Temp SW	Dynamic Contrast Threshold		
	0	0	1	0	0	1	0	0
11010	R-Y/B-Y Gain Balance				R-Y/B-Y Angle			
	1	0	0	0	1	0	0	0
11011	B-Y DC Level (White-Balance)				R-Y DC Level (White-Balance)			
	1	0	0	0	1	0	0	0
11100	Audio SW	Volume						
	0	0	0	0	0	0	0	0
11101	OVER.MOD.SW	VOL.FIL	RF.AGC					
	0	0	1	0	0	0	0	0
11110	FM.Mute	deem.TC	VIF.Sys.SW		SIF.Sys.SW		FM.Gain	IF.AGC
	0	0	0	1	0	1	0	0
11111	VIDEO.LEVEL			*	*	*	*	*
	1	0	0	0	0	0	0	0

(Bits are transmitted in this order.)

LA7605NM BUS Control Register Bit Allocation Map

IC Address (WRITE) : 10111010

Control Register Bit Allocations (continued)								
Sub Address	MSB			DATA BITS				LSB
	DA0	DA1	DA2	DA3	DA4	DA5	DA6	DA7
00100000	Pre/Over SW	C.VCO Adj SW	*	*	*	*	*	*
	0	0	0	0	0	0	0	0
100001	C.VCO Adjust		*	*	*	*	*	*
	0	0	0	0	0	0	0	0
100010	*	Tint.Through	*	*	*	*	*	*
	0	0	0	0	0	0	0	0
100011	*	*	*	*	*	*	*	*
	0	0	0	0	0	0	0	0
100100	*	*	*	*	*	*	*	*
	0	0	0	0	0	0	0	0
100101	VIDEO.LEVEL.OFFSET		IF.TEST1	*	OVER.MOD.LEVEL			
	0	1	0	(0)	1	0	0	0

(Bits are transmitted in this order.)

LA7605 BUS Status Register Bit Allocation Map

IC Address (READ) : 10111011

	Status Register Bit Allocations							
	MSB			DATA BITS				LSB
	DA0	DA1	DA2	DA3	DA4	DA5	DA6	DA7
Status1	*	*	*	RF.AGC	*	V.TRI	50/60	ST/NONST
	*	*	*	*	*	*	*	*
Status2	H.Lock	*	*	Killer	*	Color.Sys		
	*	*	*	*	*	*	*	*

LA7605NM Pin Assignment

PIN	FUNCTION	PIN	FUNCTION
1	Gnd	80	IF Ground
2	Gnd	79	PIF Input2
3	Gnd	78	PIF Input1
4	Gnd	77	RF AGC Output
5	IF Ground	76	PIF AGC
6	IF Vcc	75	FM Output / Selected Audio Output
7	FM Filter	74	NC
8	AFT Output	73	Audio Output
9	Bus Data	72	NC
10	Bus Clock	71	NC
11	Dynamic Contrast input	70	NC
12	Red Input	69	SIF Input
13	Green Input	68	SIF APC Filter
14	Blue Input	67	SIF Output
15	Fast Blanking Input	66	Ext. Audio Input
16	RGB Vcc	65	APC Filter
17	Red Output	64	Gnd
18	Green Output	63	Gnd
19	Blue Output	62	Gnd
20	C_sync output	61	Gnd
21	Gnd	60	VCO Coil 1
22	Gnd	59	VCO Coil 2
23	Gnd	58	VCO Filter
24	Gnd	57	NC
25	fsc output	56	Video Output
26	VS	55	Black Level Detector
27	APL	54	Internal Video Input(S-C IN)
28	Horizontal/BUS Vcc	53	Video/Vertical Vcc
29	Horizontal AFC Filter	52	External Video Input(Y IN)
30	HS	51	Video/Vertical/BUS Ground
31	NC	50	Selected Video Output
32	BGP	49	Chroma APC1 Filter
33	VCO IREF	48	4.43MHz Crystal
34	Sand Castle Output	47	Clamp Filter
35	Clock (4MHz) Output	46	I ref. Filter
36	Clock (4MHz) Input	45	SECAM R-Y Input (Cr Input)
37	CCD Vcc	44	SECAM B-Y Input (Cb Input)
38	CCD Filter	43	Gnd
39	CCD Ground	42	Gnd
40	Horizontal Ground	41	Gnd

LA7605 Bus Control Register Descriptions

Control Register Descriptions		
Register Name	Bits	General Description
T Disable	1	Disable the Test SW & enable Audio / Video Mute SW
AFC Gain & gate	1	Select horizontal first loop gain & H-sync gating on/off
H Freq.	6	Align horizontal frequency
Audio Mute	1	Disable audio outputs
Video Mute	1	Disable video outputs
Sync Kill	1	Force free-run mode
Gray Mode	1	OSD Gray Tone Enable
Cross B/W	2	Service Test Mode (normal/Black/White/Cross)
H BLK L	3	HS edge Control (Left side of the screen)
H BLK R	3	HS edge Control (Right side of the screen)
Red Bias	8	Align Red OUT DC level
Green Bias	8	Align Green OUT DC level
Blue Bias	8	Align Blue OUT DC level
RGB.Test4	1	Enable RGB control DAC test modes
Red Drive	7	Align Red OUT AC level
Drive Test	1	Enable Drive control DAC test modes
Half Tone	2	Adjust half tone DC level
Half Tone Defeat	1	Half tone defeat SW
Green Drive	4	Align Green OUT AC level
A2.SW	1	Select 5.74MHz FM.Det
Blue Drive	7	Align Blue OUT AC level
Blank Def	1	Disable RGB output blanking
Sub Bias	7	Align common RGB DC level
A.MONI.SW	1	Select FM Output/Selected Audio Output
Brightness Control	7	Customer brightness control
S.TRAP.SW	1	Select Snd Trap bypass
Contrast Control	7	Customer contrast control

LA7605 Bus Control Register Descriptions

Control Register Descriptions		
Register Name	Bits	General Description
OSD Contrast Test	1	Enable OSD Contrast DAC test mode
OSD Contrast Control	7	Align OSD AC level
Coring Gain Select (with Defeat)	2	Select Coring Gain (0hex: Defeat)
Sharpness Control	6	Customer sharpness control
Tint Test	1	Enable tint DAC test mode
Tint Control	7	Customer tint control
Color Test	1	Enable color DAC test mode
Color Control	7	Customer color control
Video SW	1	Select Video source
Trap.Test	3	Sound Trap Test
Filter System	4	Select Y/C Filter mode
CbCr_Input Enable SW	1	Enable CbCr Input(Disable SECAM Input)
G-Y Angle Select	1	Select G-Y Angle
Color Killer Operational Point Select	3	Select Color Killer Operational Point
Y APF Enable SW	1	Select the frequency caracteristic of It is useful for 3.58MHzTrap or APF.
Pre/Over-shoot Adjustmant	2	Select Pre-shoot Width
WPL Ope. Point(W/ Defeat)	2	Select WPL Operating Point
Y Gamma Start	2	Enable luminance coring
Blk. Str. Start Point Select(w/ Defeat)	2	Select Black stretch Start Point(w/ Defeat)
Blk. Str. Gain Select	2	Select Black stretch Gain
AutoFlesh	1	Enable AutoFlesh function
C Ext	1	Selected-C In SW on
C Bypass	1	Select Chroma BPF bypass
C Kill On	1	C Kill Mode (1: Enable Killer circuit)
C Kill Off	1	Disable Killer circuit
Color System	3	Select Color System
Cont Test	1	Enable contrast DAC test mode
Digital OSD SW	1	Select Digital/Analogue OSD
Dynamic Contrast Defeat	1	Disable Dynamic Contrast function
Dynamic Contrast Gain	1	Select Dynamic Contrast Gain
RGB Temp SW	1	Select temprature caracteristic of RGB Output
Dynamic Contrast Threshold	3	Align Dynamic Contrast threshold
R-Y/B-Y Balance	4	R-Y/B-Y Gain Balance
R-Y/B-Y Angle	4	R-Y/B-Y Angle
B-Y DC Level	4	B-Y DC Level (White-Balance)
R-Y DC Level	4	R-Y DC Level (White-Balance)

LA7605 Bus Control Register Descriptions

Control Register Descriptions		
Register Name	Bits	General Description
Audio SW	1	Select Audio source
Volume Control	7	Customer volume control
OVER.MOD.SW	1	Select overmodulation circuit ON/OFF
Volume Filter Defeat	1	Disable volume DAC filter

RF AGC Delay	6	Align RF AGC threshold
FM Mute	1	Disable FM outputs
de-em TC.	1	Select de-emphasis Time Constant
VIF System SW	2	Select 38.0/38.9/39.5/45.75
SIF System SW	2	Select 4.5/5.5/6.0/6.5
FM Gain	1	Select FM Output Level
IF AGC Defeat	1	Disable IF and RF AGC
Video Level	3	Align IF video level
Pre/Over SW	1	Select control for Pre/Over-shoot Adjustmant
C VCO Adjust SW	1	Select the direction of C. VCO adjustmant.
C VCO Adjust	2	Align C. VCO's frequency
Tint Through	1	Select tint control operation(enable or disable)
VIDEO.LEVEL.OFFSET	2	Align IF video level
IF TEST1	1	Select test modes
OVER.MOD.LEVEL	4	Align overmodulation performance

LA7605 BUS Initial Conditions

Initial Test Conditions	
Register	
T.Disable	1 HEX
AFC gain&gate	0 HEX
H.FREQ	1F HEX
Audio.Mute	0 HEX
Video.Mute	0 HEX
Sync.Kill	0 HEX
Gray Mode	0 HEX
Cross B/W	0 HEX
H BLK L	4 HEX
H BLK R	4 HEX
R.BIAS	00 HEX
G.BIAS	00 HEX
B.BIAS	00 HEX
RGB Test 4	0 HEX
R.DRIVE	7F HEX
Drive Test	0 HEX
Half Tone	1 HEX
Half Tone Def	1 HEX
G.DRIVE	8 HEX
A2 SW	0 HEX
B.DRIVE	7F HEX
Blank.Def	1 HEX
Sub.Bias	40 HEX
A.MONI.SW	0 HEX
Bright	40 HEX
S.TRAP.SW	1 HEX
Contrast	40 HEX
OSD Cnt.Test	0 HEX
OSD Contrast	40 HEX
Coring Gain(w/ Defeat)	0 HEX
Sharpness	00 HEX
Tint.Test	0 HEX
Tint	40 HEX
Color.Test	0 HEX
Color	40 HEX
Video.SW	0 HEX
Trap.Test	4 HEX
Filter.Sys	2 HEX
CbCr_IN	0 HEX
G-Y Angle	0 HEX
Color Killer Ope.	4 HEX

Initial Test Conditions(continued)	
Register	
Y_APF	0 HEX
Pre/Over-shoot Adj.	0 HEX
WPL Ope. Point(W/ Defeat)	0 HEX
Y Gamma	0 HEX
Blk.Str.start(W/ Defeat)	0 HEX
Blk.Str.Gain	0 HEX
Auto Flesh	0 HEX
C.Ext	0 HEX
C.Bypass	1 HEX
C_Kill ON	0 HEX
C_Kill OFF	0 HEX
Color System	0 HEX
Cont.Test	0 HEX
Digitsl OSD	0 HEX
Dyn-C Def	1 HEX
Dyn-C Gain	0 HEX
RGB Temp SW	0 HEX
Dynamic Contrast Threshold	4 HEX
R-Y/B-Y Gain Balance	8 HEX
R-Y/B-Y Angle	8 HEX
B-Y DC Level	8 HEX
R-Y DC Level	8 HEX
Audio.SW	0 HEX
Volume	00 HEX
OVER.MOD.SW	0 HEX
VOL.FIL	0 HEX
RF.AGC	20 HEX
FM.Mute	0HEX
deem.TC	0HEX
VIF.Sys.SW	1 HEX
SIF.Sys.SW	1 HEX
FM.Gain	0 HEX
IF.AGC	0 HEX
VIDEO.LEVEL	4 HEX
Pre/Over SW	0 HEX
C.VCO Adj SW	0 HEX
C.VCO Adjust	0 HEX
Tint Through	0 HEX
VIDEO.LEVEL.OFFSET	1 HEX
IF.TEST1	0 HEX
OVER.MOD.LEVEL	8 HEX

LA7605 BUS : Status Byte Truth Table

Status Byte Truth Table		
Register	0 HEX	1 HEX
RF.AGC	RF.AGC.OUT="L"	RF.AGC.OUT="H"
V.TRI	V.Triger Undetected	V.Triger Detected
50/60	50	60
ST/NONST	Non-Standard	Standard
H.LOCK	Horiz Unlocked	Horiz Locked
KILLER	KILLER OFF	KILLER ON

Color System	0 HEX	B/W
	1 HEX	PAL
	2 HEX	PAL-M
	3 HEX	PAL-N
	4 HEX	NTSC
	5 HEX	4.43NTSC
	6 HEX	SECAM
	7 HEX	Do not care

LA7605 BUS : Control Register Truth Table

Control Register Truth Table				
Register Name	0 HEX	1 HEX	2 HEX	3 HEX
T.Disable	Tset Enable	Test Disable		
AFC gain&gate	Auto(Gain)	Gain:Fast		
	Auto(Gate)	Non-Gate		
Audio.Mute	Active	Mute		
Video.Mute	Active	Mute		
Gray Mode	Normal	Gray OSD		
Cross B/W	Normal	Black	White	Cross
Sync.Kill	Sync active	Sync killed		
RGB.Test4	Normal	Test Mode		
Drive.Test	Normal	Test Mode		
Half Tone	Min(Dark)	->	->	Max
Half Tone Def	Half Tone on	Half Tone off		
Blank.Def	Blanking	No Blank		
S.TRAP.SW	Bypass ON	Bypass OFF		
OSD Cnt.Test	Normal	Test Mode		
Coring Gain(w/ Defeat)	Defeat	Min	->	Max
Tint.Test	Normal	Test Mode		
Color.Test	Normal	Test Mode		
Video.SW	Internal Mode	External Mode		
CbCr_IN	SECAM Input mode	CbCr Input mode		
G-Y Angle	240deg	253deg		
Y APF	Y Trap	Y APF		
Pre/Over-shoot adj.	Normal	+10ns	+20ns	+30ns
WPL Ope. Point(w/ Defeat)	Defeat	High	->	Low
Y Gamma Start	Y Gamma off	Min	->	Max
Blk.Str.start(w/ Defeat)	Defeat	Low	->	High
Blk.Str.Gain	Min	->	Max	
Auto Flesh	OFF	ON		
C.Ext	Internal Mode	External Mode		
C.Bypass	Bypass OFF	Bypass ON		
C_Kill ON	Auto Mode	Killer ON		
C_Kill OFF	Auto Mode	Killer OFF		
Cont.Test	Normal	Test Mode		
Digital OSD	Analogue	Digital		
Dyn-C Defeat	Brt ABL On	Brt ABL Off		
Dyn-C Gain	Mid Stp On	Mid Stp Off		
RGB Temp SW	-1Vbe	Flat		
OVER.MOD.(circuit)SW	circuit OFF	circuit ON		
VOL.FIL	Normal	Filte OFF		
FM.Mute	Active	Mute		
de-em TC.	50μs	75μs		
VIF.Sys.SW	38.0MHz	38.9MHz	45.75MHz	39.5MHz
FM Gain	50kHz dev.	25kHz dev		
IF.AGC	AGC active	AGC defeat		
Pre/Over SW	Pre-shoot Adj.	Over-shoot Adj.		
C.VCO Adj SW	direction: Plus	direction: Minus		
C.VCO Adjust (C.VCO Adj SW: 0)	normal	+30kHz	+60kHz	+90kHz
(C.VCO Adj SW: 1)	-30kHz	-60kHz	-90kHz	-120kHz
Tint Through	Tint Control Enable	Tint Control Disable		
VIDEO.LEVEL.OFFSET	direction: Minus	Center		direction: Plus
IF.TEST1	Normal	Test Mode		

LA7605 BUS : Control Register Truth Table

Color System

0 HEX	Auto Mode1 PAL/NTSC/4.43NTSC(/SECAM)
1 HEX	Auto Mode2 PAL-M/PAL-N/NTSC
2 HEX	PAL
3 HEX	PAL-M
4 HEX	PAL-N
5 HEX	NTSC
6 HEX	4.43NTSC
7 HEX	SECAM

Filter System

	Y Filter	Chroma Filter
0 HEX	3.58MHz Trap	Peaked 3.58MHz BPF
1 HEX	3.58MHz Trap	Symmetrical 3.58MHz BPF
2 HEX	4.43MHz Trap	Peaked 4.43MHz BPF
3 HEX	4.43MHz Trap	Symmetrical 4.43MHz BPF
4 HEX	6.0MHz Trap	Peaked 3.58MHz BPF
5 HEX	6.0MHz Trap	Symmetrical 3.58MHz BPF
6 HEX	6.0MHz Trap	Peaked 4.43MHz BPF
7 HEX	6.0MHz Trap	Symmetrical 4.43MHz BPF
8-15HEX	4.286MHz Trap	Symmetrical 4.43MHz BPF

Snd.Trap & FM.Det

A2.SW	SIF.Sys.SW	Snd.Trap	FM.det
0 HEX	0 HEX	4.5MHz	4.5MHz
	1 HEX	5.5MHz	5.5MHz
	2 HEX	6.0MHz	6.0MHz
	3 HEX	6.5MHz	6.5MHz
1 HEX	0 HEX	----------	----------
	1 HEX	5.5NHz	5.74MHz
	2 HEX	----------	----------
	3 HEX	----------	----------

Audio Monitor Output

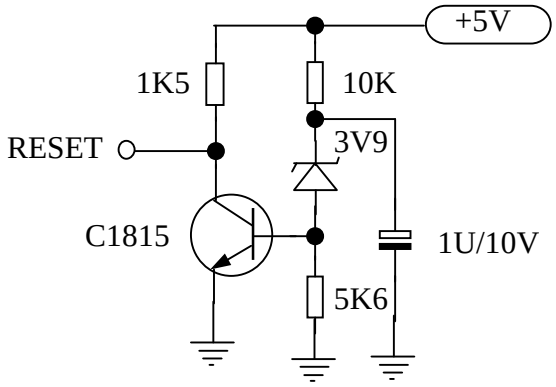
A.MONI.SW	AUDIO.SW	1pin Output	2pin Output
0 HEX	0 HEX	Internal	Internal
	1 HEX	External	
1 HEX	0 HEX	Internal	Internal
	1 HEX	External	External (before VOLUME)

液晶小电视CPU（MC802）引脚定义总表 2004/04/03

引脚号	接口	端口特性	总定义	具体定义
1	DVSS		DVSS	GND
2	AVSS		AVSS	GND
3	LF		LF	NC
4	RST	复位	RST	RESET NOTE1
5	AVDD	3.3V	AVDD	VCC
6	DVDD	3.3V	DVDD	VCC
7	P4.5	3.3V输出	S.SYS/killer	S.SYS/killer for la7642 NOTE2
8	P4.4/DA14bit	PWM.14bit	VT/VOL	14BIT PWM 电压调宽输出 NOTE8
9	P4.3	3.3V输出	H.OUT	NOTE18
10	P4.2	3.3V输出	MODE2	MODE2 NOTE3
11	P4.1	5V输出	MODE1	MODE1 NOTE3
12	P4.0	5V输出	IDENT-H	IDENT-H NOTE4
13	P3.0	3V3输出	ISP-CLK	ISP-SCK NOTE5
14	P3.1	5V输出	ISP-SDA	ISP-SDA NOTE5
15	P3.2/INT0	5V输出	REM	REMOT
16	P3.4	上拉5V输出	U/D_OUT	U/D_OUT NOTE17
17	P3.5	上拉**5V**输出	POWER	POWER ON= OFF=
18	P6.0	上拉5V输出	AV1/AV2	AV1/AV2 NOTE6
19	P6.1	上拉5V输出	AV/TV	AV/TV NOTE6
20	P1.0	上拉**5V**输出	SCL1	CLK/SCL1 for decoder NOTE7
21	P1.1	上拉**5V**输出	SDA1	DATA/DI/SDA1 for decoder NOTE7
22	P1.2	上拉**5V**输出	BL/SDA2	BL/SDA2 for tuner NOTE8
23	P1.3	上拉**5V**输出	BH/SCL2	BH/SCL2 for tuner NOTE8
24	P1.4	上拉**5V**输出	BU/EN	BU/EN for tuner NOTE8
25	P1.5	上拉**5V**输出	P/N.OUT	PAL/SECAM=0，NTSC=1
26	X1		X1	外接12M晶体
27	X2		X2	外接12M晶体
28	P1.6	上拉**5V**输出	CLOCK	62429CLK(时钟线) NOTE7
29	P1.7	上拉**5V**输出	L/R_OUT	L/R_OUT NOTE17
30	P5.4	上拉**5V**输出/**PWM**	CONT	对比度模拟量控制输出
31	P5.5	上拉5V输出/PWM	COL	色度模拟量控制输出
32	P5.6	上拉**5V**输出/**PWM**	BRIGHT	亮度模拟量控制输出
33	P5.0	上拉3.3V输出/AD	KEY1	键盘控制输入1
34	P5.1	上拉3.3V输出/AD	AFT	AFT输入 NOTE9
35	P5.2	上拉3.3V输出/AD	KEY2	键盘控制输入2
36	P5.3	上拉3.3V输出/AD	FUC_IDENT	FUC_IDENT NOTE10
37	P4.7	上拉3.3V输出	LED	LED NOTE11
38	P4.6	上拉3.3V	L/R_IN	L/R_IN NOTE12
39	PLL.OUT		PLL.OUT	NC
40	P6.2		HTONE/CS	HTONE/CS forRB5P006 NOTE13
41	BLK		BLK	字符消隐脉冲输出
42	B.OUT		B.OUT	字符蓝色输出
43	G.OUT		G.OUT	字符绿色输出
44	R.OUT		R.OUT	字符红色输出
45	X.IN		X.IN	NC
46	PLL.COAST	芯片测试脚		NC
47	OSDVS		OSDVS	字符场脉冲输入 NOTE15
48	OSDHS		OSDHS	字符行脉冲输入 NOTE16

备注：

NOTE1: 为了保证mcu可靠稳定地工作，请使用下图所示复位电路。



NOTE2: 3.3V电平输出，用于切换两种不同的伴音制式			0：SYS1　　1：SYS2
NOTE3:	3.3V电平输出，用于切换两种不同的显示模式	MODE1(P4.3)	MODE2(P4.2)	显示模式
		0	1	4:3
		1	0	16:9(16:9--1)
		1	1	ZOOM1(16:9--1)
		0	0	ZOMM2(16:9--2)
NOTE4: 信号识别		IDENT-H 行同步信号输入，用于作电视信号识别		
NOTE5: 在系统编程接口(不能做其它用)			ISP-SCK	串行时钟接口
			ISP-SDA	串行数据接口
NOTE6: 信号输入选择5V电平输出		TV/AV	AV1/AV2	INPUT
		0	0	TV
		0	1	AV1
		1	0	AV2
		1	1	SVIDEO
NOTE7: 串行通讯接口		CLK/SCL1	串行时钟接口（解码IC的时钟线）	
		DATA/DI/SDA1	串行数据接口（解码IC和音量IC62429的数据线）	
		62429CLK	串行数据接口（音量IC62429的时钟线）	
NOTE8: 高频头控制口		用PWM高频头时	BL/BH/BU为波段选择脚，VT/VOL作调谐电压控制脚	
		用PLL高频头时	BU接地为识别脚，BL(SDA2)/BH(SCL2)为IIC通讯口 VT/VOL可作电压调节的音量控制	
NOTE9: 中放AFT检测输入脚			AFT输入电压范围为0--3.3V	
NOTE10: 厂家功能识别脚，用于不同功能的识别，用需定制。				
NOTE11: MCU工作状态指示，有按键时快闪，关机时慢闪，其它状态正常亮。				
NOTE12: L/R IN 倒车控制脚，用于倒车时水平翻转图像信号，输入电平 +3V3				
NOTE13: HTONE/CS ，使用RB5P006解码时作芯片选择脚。				
NOTE14: 字符场脉冲输入，用于OSD字符发生，负极性场同步脉冲。				
NOTE15: 字符行脉冲输入，用于OSD字符发生，负极性行同步脉冲。				
NOTE17: 图像旋转控制		U/D OUT	L/R OUT	操作状态
		1	1	正常
		翻转一次	翻转一次	按垂直翻转键（或U/D菜单操作）一次
		不变	翻转一次	按水平翻转键（或L/R菜单操作）一次
		不变(U/D=1时)	0	倒车时 L/R_IN输入为高电平时，L/R根据U/D状态输出相应电平,输入切换倒AV2。倒车结束后恢复
		不变(U/D=0时)	1	
NOTE18: HSYNC输出,可给液晶屏作为同步信号，其极性、相位、占空比均可任意调整。				