

# JVC

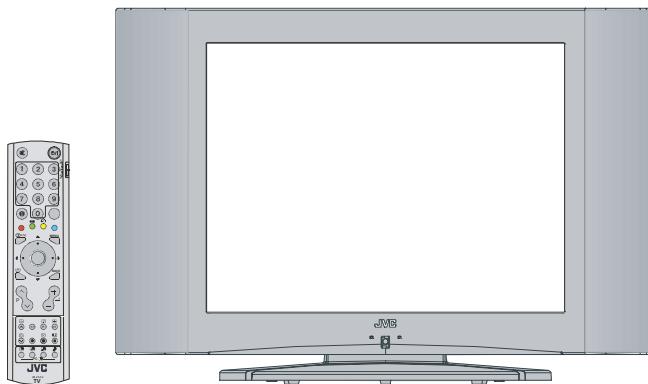
## SCHEMATIC DIAGRAMS

LCD PANEL TELEVISION

**LT-20E50SJ,  
LT-20E50SU,  
LT-20E50SU<sub>/Z</sub>**

CD-ROM No.SML200508

InteríArt



# LT-20E50SJ, LT-20E50SU, LT-20E50SU/Z

## STANDARD CIRCUIT DIAGRAM

### ■ NOTE ON USING CIRCUIT DIAGRAMS

#### 1.SAFETY

The components identified by the  symbol and shading are critical for safety. For continued safety replace safety critical components only with manufacturer's recommended parts.

#### 2.SPECIFIED VOLTAGE AND WAVEFORM VALUES

The voltage and waveform values have been measured under the following conditions.

- (1) Input signal : Colour bar signal
- (2) Setting positions of each knob/button and variable resistor : Original setting position when shipped
- (3) Internal resistance of tester : DC 20kΩ/V
- (4) Oscilloscope sweeping time : H ⇒ 20μs / div  
: V ⇒ 5ms / div  
: Others ⇒ Sweeping time is specified
- (5) Voltage values : All DC voltage values

\* Since the voltage values of signal circuit vary to some extent according to adjustments, use them as reference values.

#### 3. INDICATION OF PARTS SYMBOL [EXAMPLE]

- In the PW board : R209 → R209

#### 4. INDICATIONS ON THE CIRCUIT DIAGRAM

##### (1) Resistors

###### ● Resistance value

- No unit : [Ω]
- K : [kΩ]
- M : [MΩ]

###### ● Rated allowable power

- No indication : 1/16 [W]
- Others : As specified

###### ● Type

- No indication : Carbon resistor
- OMR : Oxide metal film resistor
- MFR : Metal film resistor
- MPR : Metal plate resistor
- UNFR : Uninflammable resistor
- FR : Fusible resistor

\* Composition resistor 1/2 [W] is specified as 1/2S or Comp.

##### (2) Capacitors

###### ● Capacitance value

- 1 or higher : [pF]
- less than 1 : [ $\mu$ F]

###### ● Withstand voltage

- No indication : DC50[V]
- Others : DC withstand voltage [V]
- AC indicated : AC withstand voltage [V]

\* Electrolytic Capacitors

47/50[Example]: Capacitance value [ $\mu$ F]/withstand voltage[V]

##### ● Type

- |               |                                      |
|---------------|--------------------------------------|
| No indication | : Ceramic capacitor                  |
| MM            | : Metallized mylar capacitor         |
| PP            | : Polypropylene capacitor            |
| MPP           | : Metallized polypropylene capacitor |
| MF            | : Metallized film capacitor          |
| TF            | : Thin film capacitor                |
| BP            | : Bipolar electrolytic capacitor     |
| TAN           | : Tantalum capacitor                 |

##### (3) Coils

- |         |                |
|---------|----------------|
| No unit | : [ $\mu$ H]   |
| Others  | : As specified |

##### (4) Power Supply

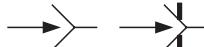
- |  |      |   |            |
|--|------|---|------------|
|  | : B1 |  | : B2 (12V) |
|  | : 9V |  | : 5V       |

\* Respective voltage values are indicated

##### (5) Test point

- |   |              |   |                           |
|---|--------------|---|---------------------------|
|  | : Test point |  | : Only test point display |
|---|--------------|---|---------------------------|

##### (6) Connecting method

- |  |              |   |                         |
|--|--------------|---|-------------------------|
|  | : Connector  |  | : Wrapping or soldering |
|  | : Receptacle |   |                         |

##### (7) Ground symbol

- |   |                                 |
|---|---------------------------------|
|  | : LIVE side ground              |
|  | : ISOLATED(NEUTRAL) side ground |
|  | : EARTH ground                  |
|  | : DIGITAL ground                |

#### 5. NOTE FOR REPAIRING SERVICE

This model's power circuit is partly different in the GND. The difference of the GND is shown by the LIVE : () side GND and the ISOLATED(NEUTRAL) : () side GND. Therefore, care must be taken for the following points.

- (1) Do not touch the LIVE side GND or the LIVE side GND and the ISOLATED(NEUTRAL) side GND simultaneously. If the above caution is not respected, an electric shock may be caused. Therefore, make sure that the power cord is surely removed from the receptacle when, for example, the chassis is pulled out.
- (2) Do not short between the LIVE side GND and ISOLATED(NEUTRAL) side GND or never measure with a measuring apparatus measure with a measuring apparatus (oscilloscope, etc.) the LIVE side GND and ISOLATED(NEUTRAL) side GND at the same time. If the above precaution is not respected, a fuse or any parts will be broken.

◆ Since the circuit diagram is a standard one, the circuit and circuit constants may be subject to change for improvement without any notice.

##### NOTE

- ◆ Due improvement in performance, some part numbers show in the circuit diagram may not agree with those indicated in the part list.  
When ordering parts, please use the numbers that appear in the Parts List.

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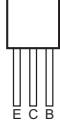
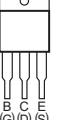
**LED PWB PATTERN .....** ..... **2-25**

## USING P.W. BOARD

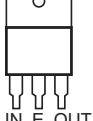
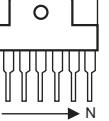
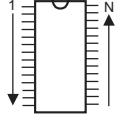
| P.W.B ASS'Y name              | LT-20E50SJ  | LT-20E50SU | LT-20E50SU/Z |
|-------------------------------|-------------|------------|--------------|
| CONTROL P.W. BOARD            | VE-20180094 | ←          | ←            |
| LED P.W. BOARD                | VE-20180117 | ←          | ←            |
| AV JACK/ HEADPHONE P.W. BOARD | VE-20180067 | ←          | ←            |
| MAIN P.W. BOARD               | VE-20177794 | ←          | ←            |

## SEMICONDUCTOR SHAPES

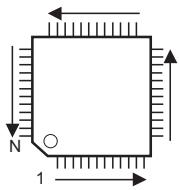
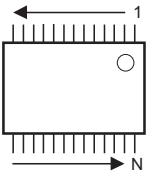
### TRANSISTOR

| BOTTOM VIEW   | FRONT VIEW  |   |   |   | TOP VIEW  |
|---|---|---|---|---|---|
|  |  |  |  |  |  |

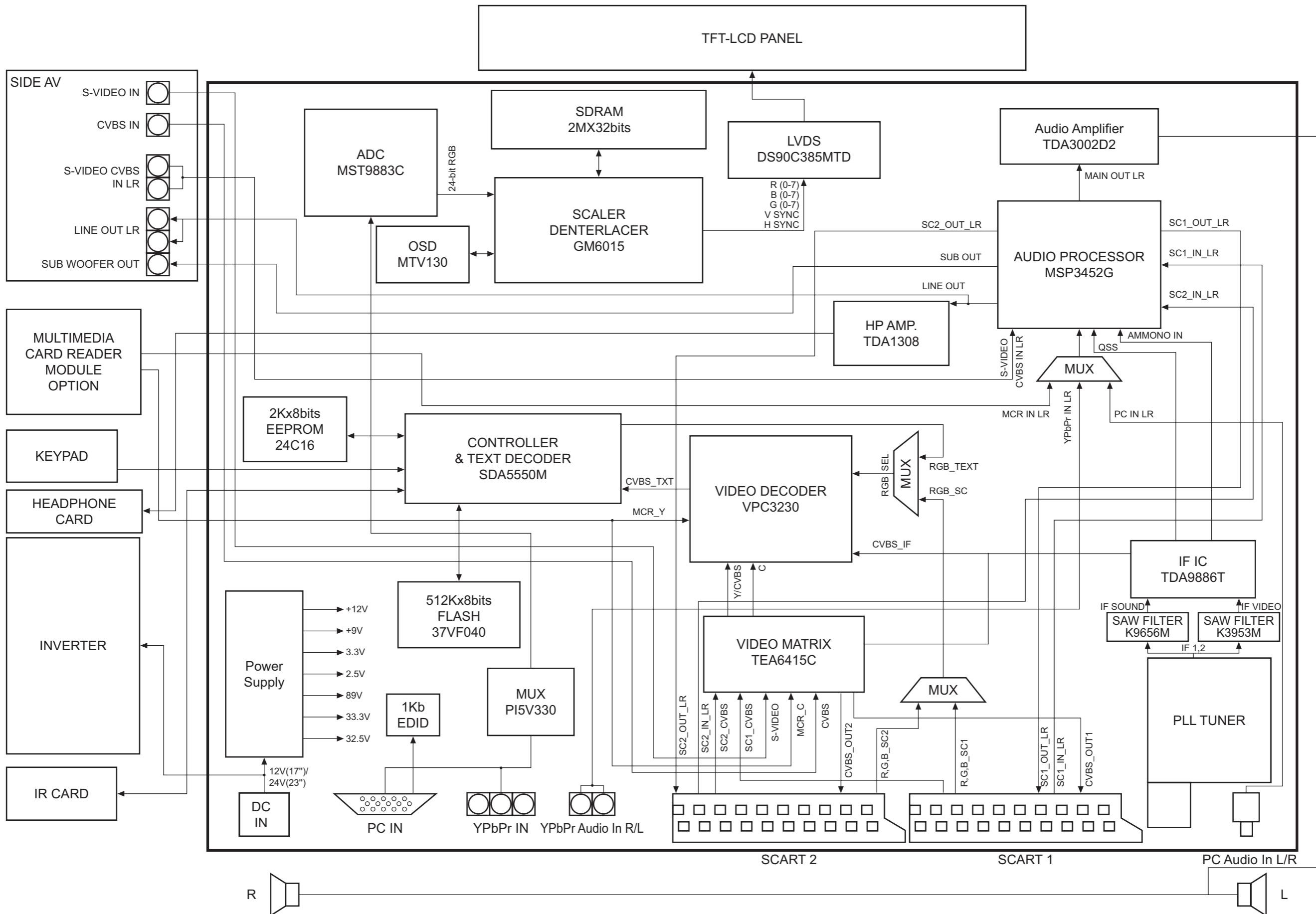
### IC

| BOTTOM VIEW   | FRONT VIEW  |   |  | TOP VIEW  |
|---|---|---|--|---|
|  |  |  |  |  |

### CHIP IC

| TOP VIEW  |   |  |
|---|---|--|
|  |  |  |

## BLOCK DIAGRAM

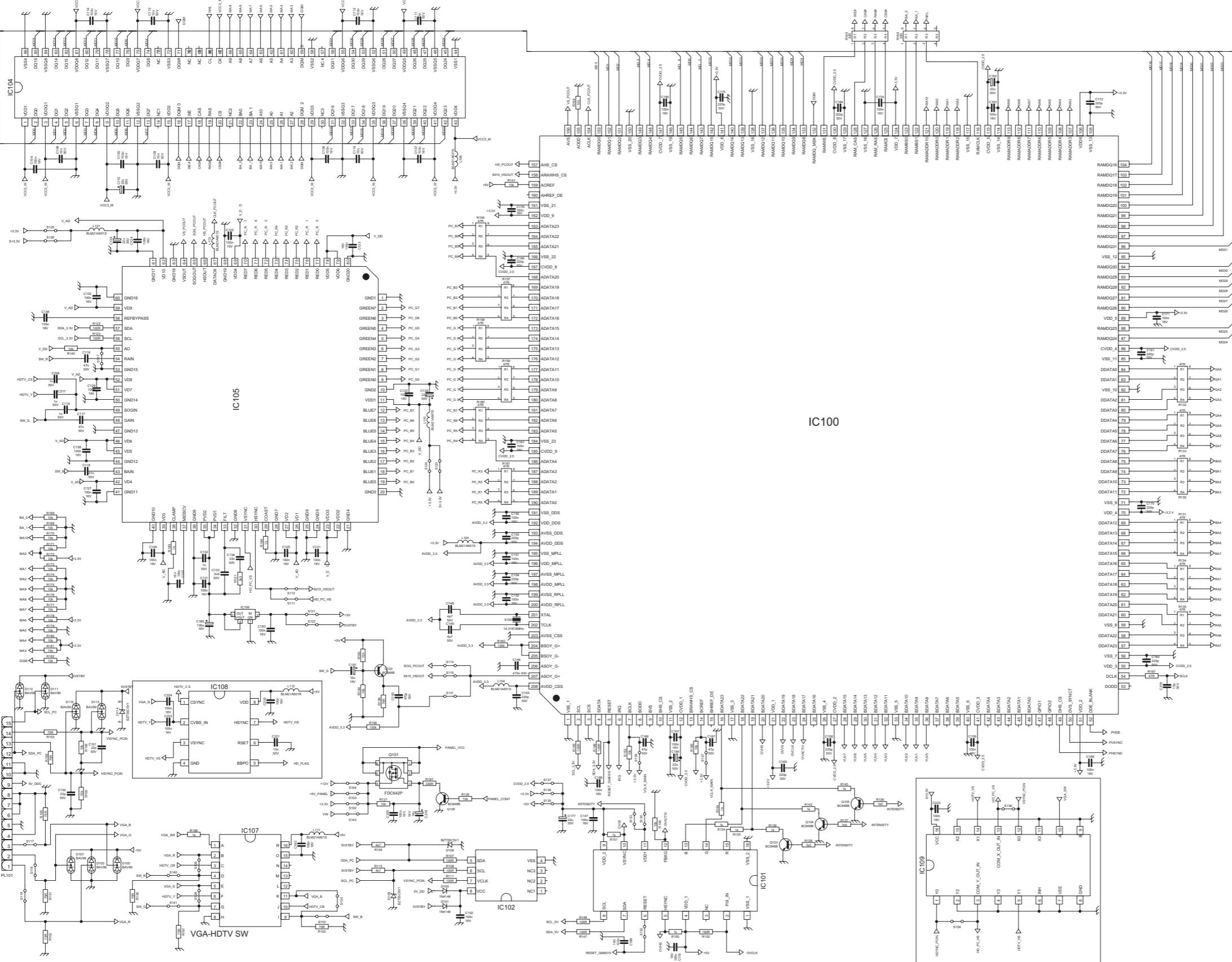


## CIRCUIT DIAGRAMS

## *MAIN PWB CIRCUIT DIAGRAM (1/8)*

# MAIN PWB ASS'Y (1/8)

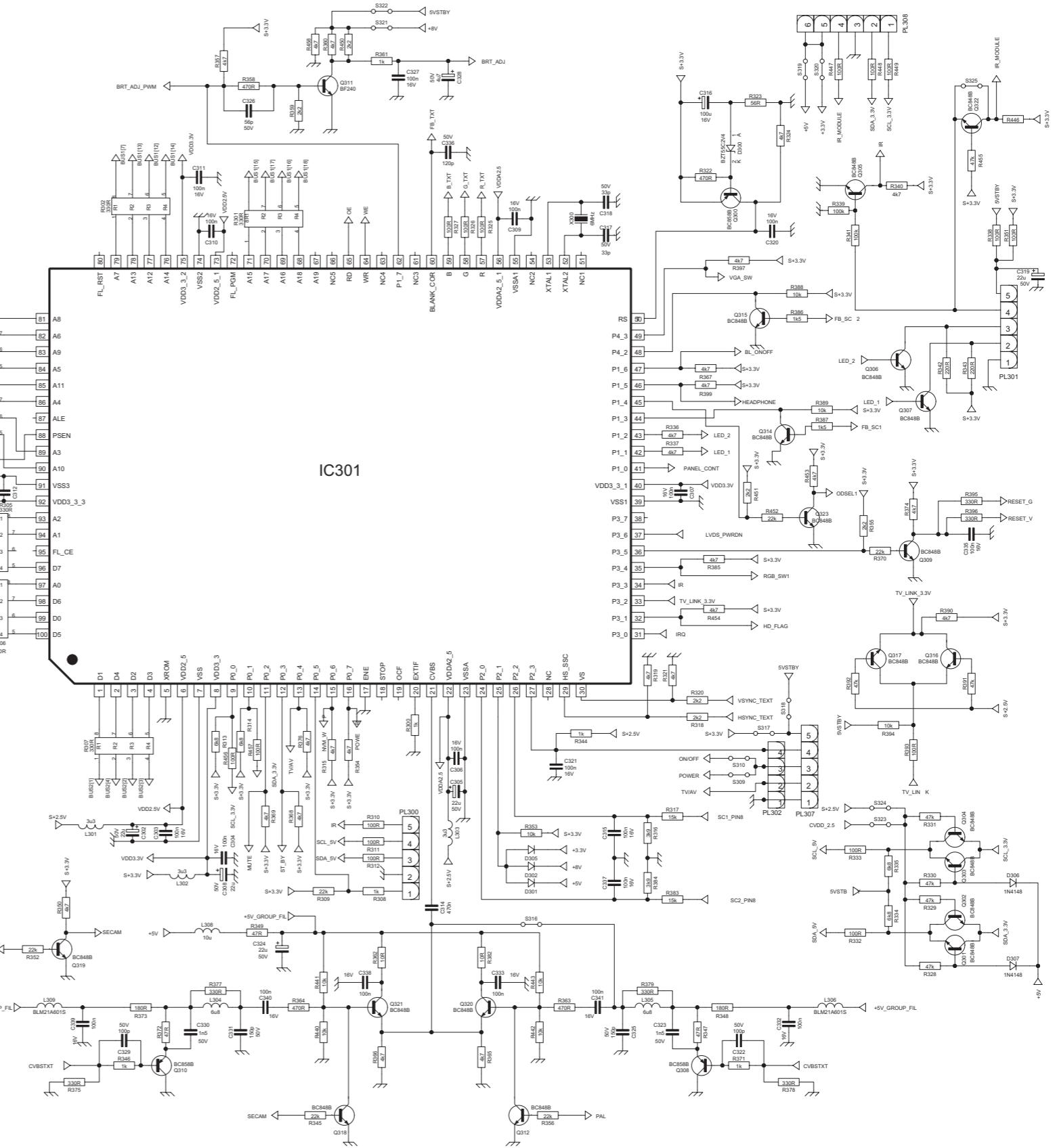
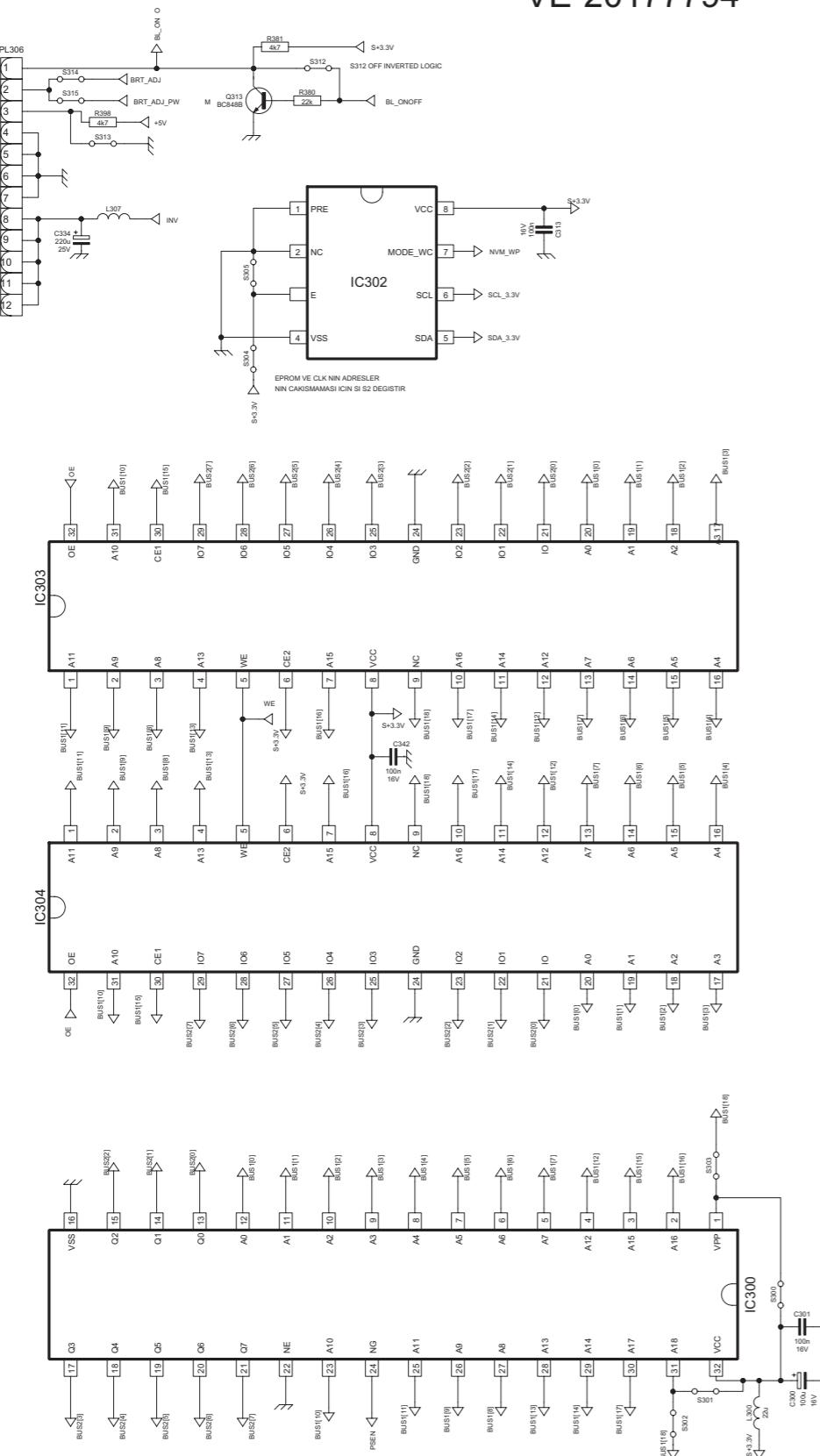
VE-20177794



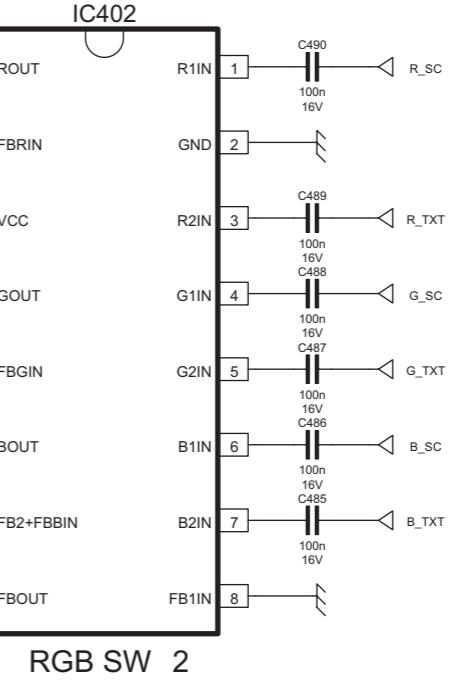
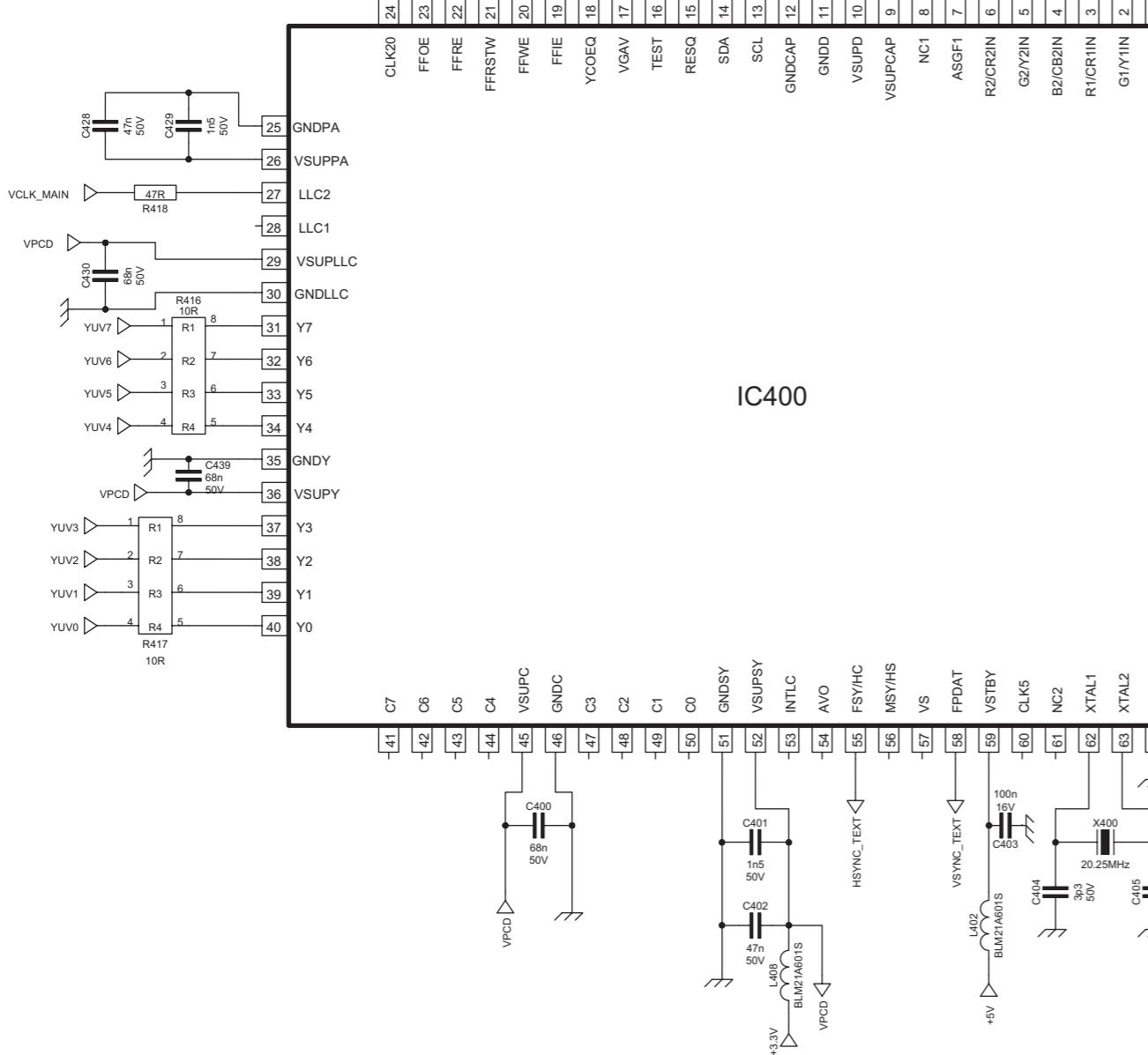
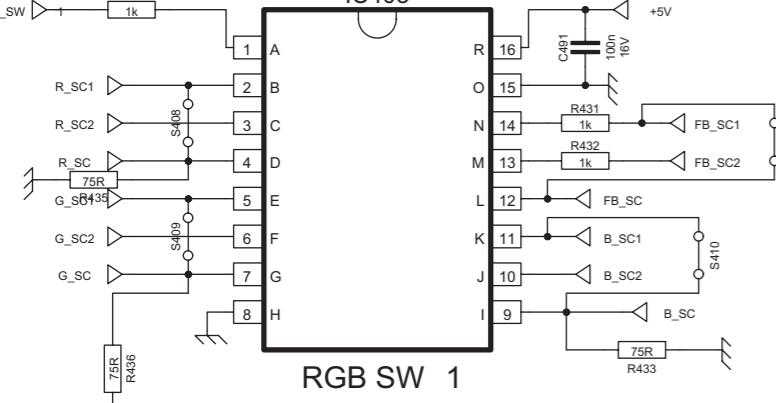
NOTE : Refer to the part list for the part number of IC301 and IC302.

# MAIN PWB ASS'Y (2/8)

VE-20177794

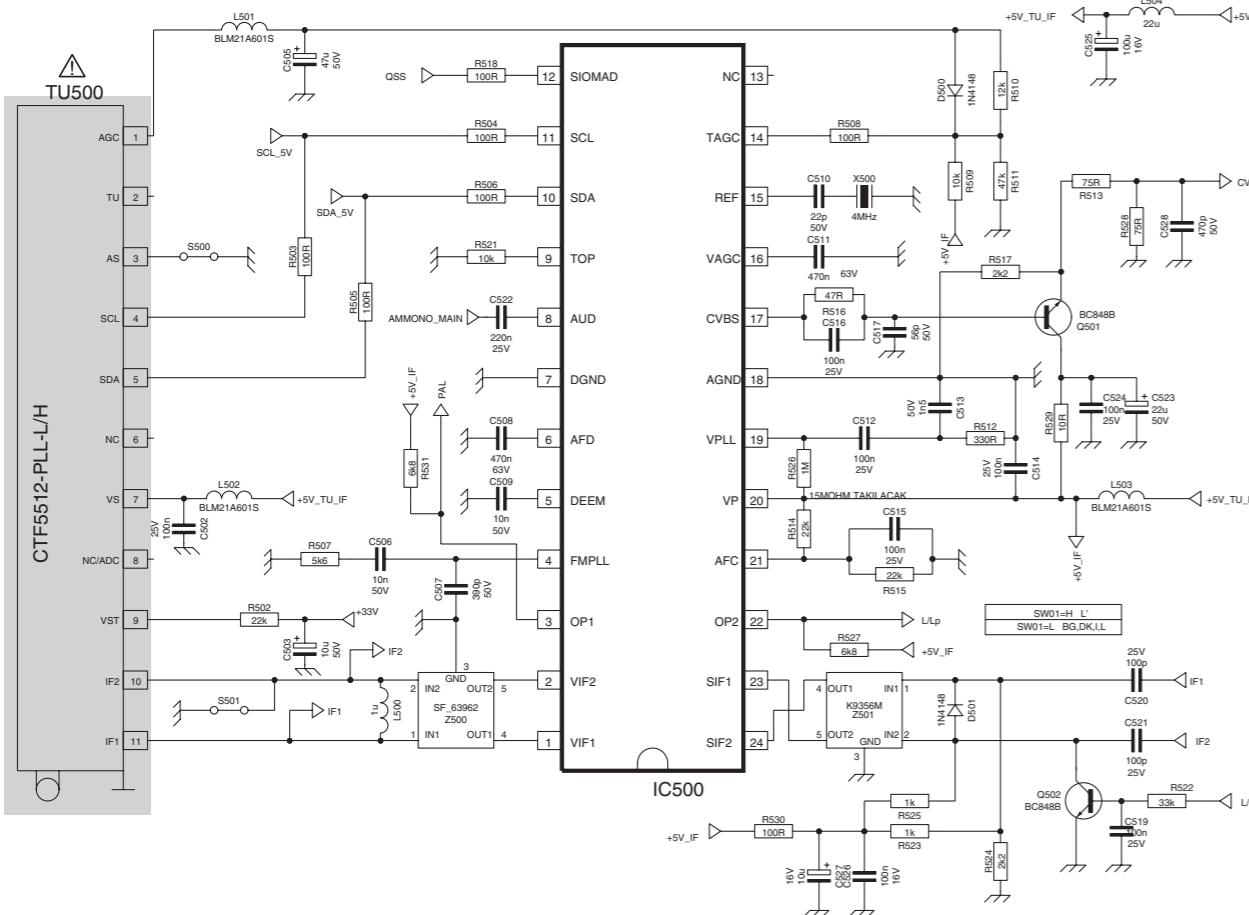


MAIN PWB CIRCUIT DIAGRAM (3/8)

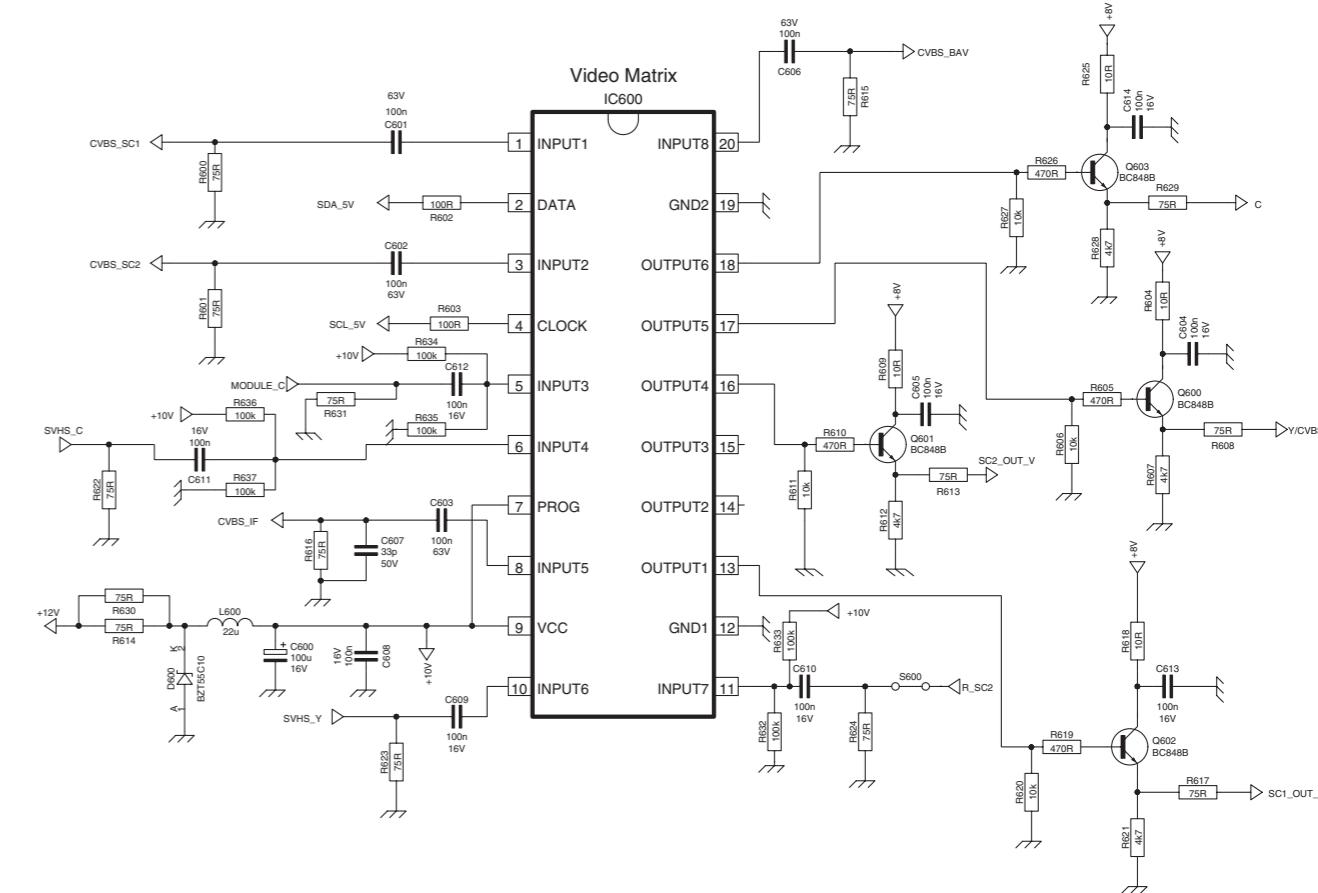


MAIN PWB ASS'Y (3/8)  
VE-20177794

MAIN PWB CIRCUIT DIAGRAM (4/8)



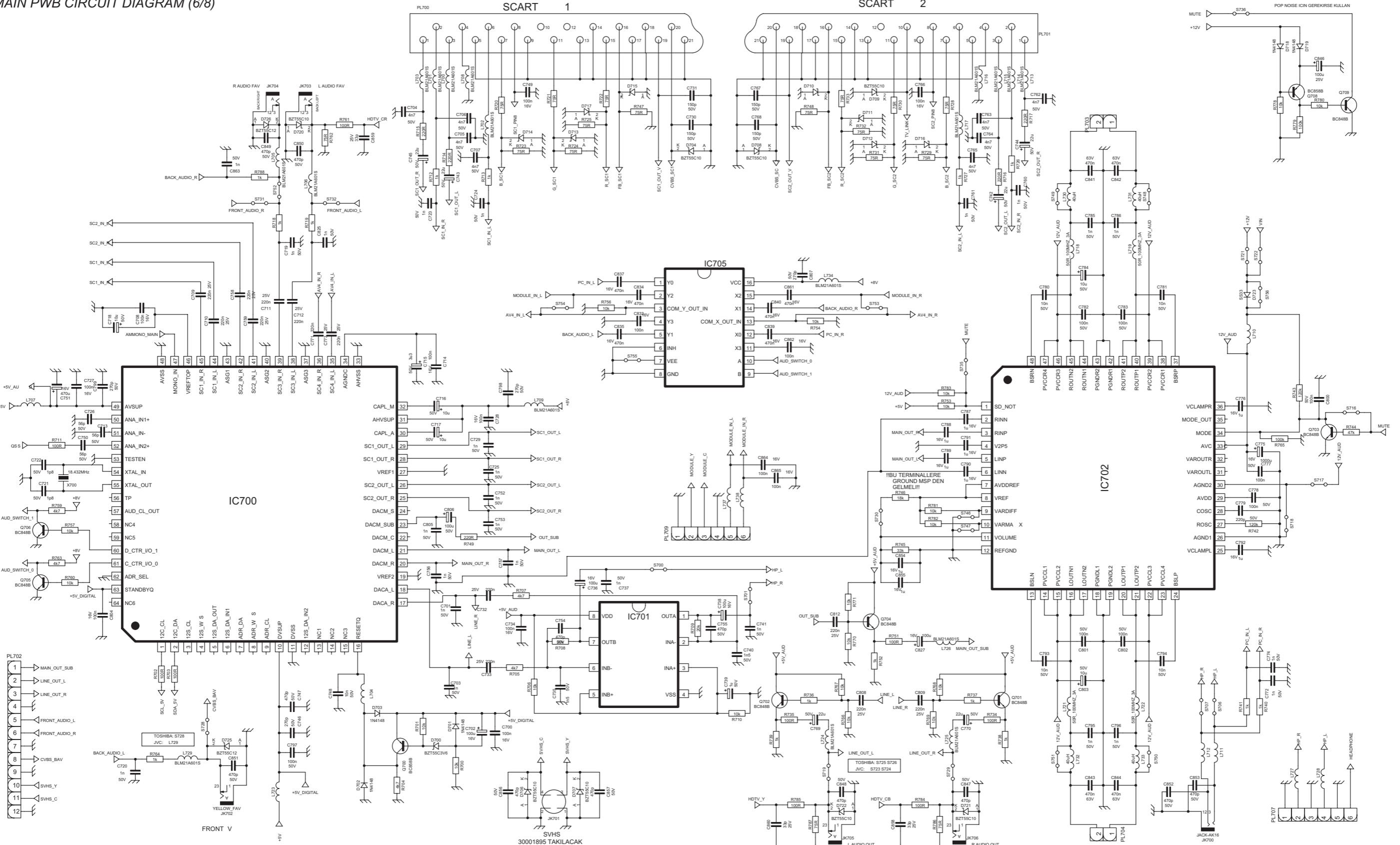
MAIN PWB CIRCUIT DIAGRAM (5/8)



MAIN PWB ASS'Y (4/8)  
VE-20177794

MAIN PWB ASS'Y (5/8)  
VE-20177794

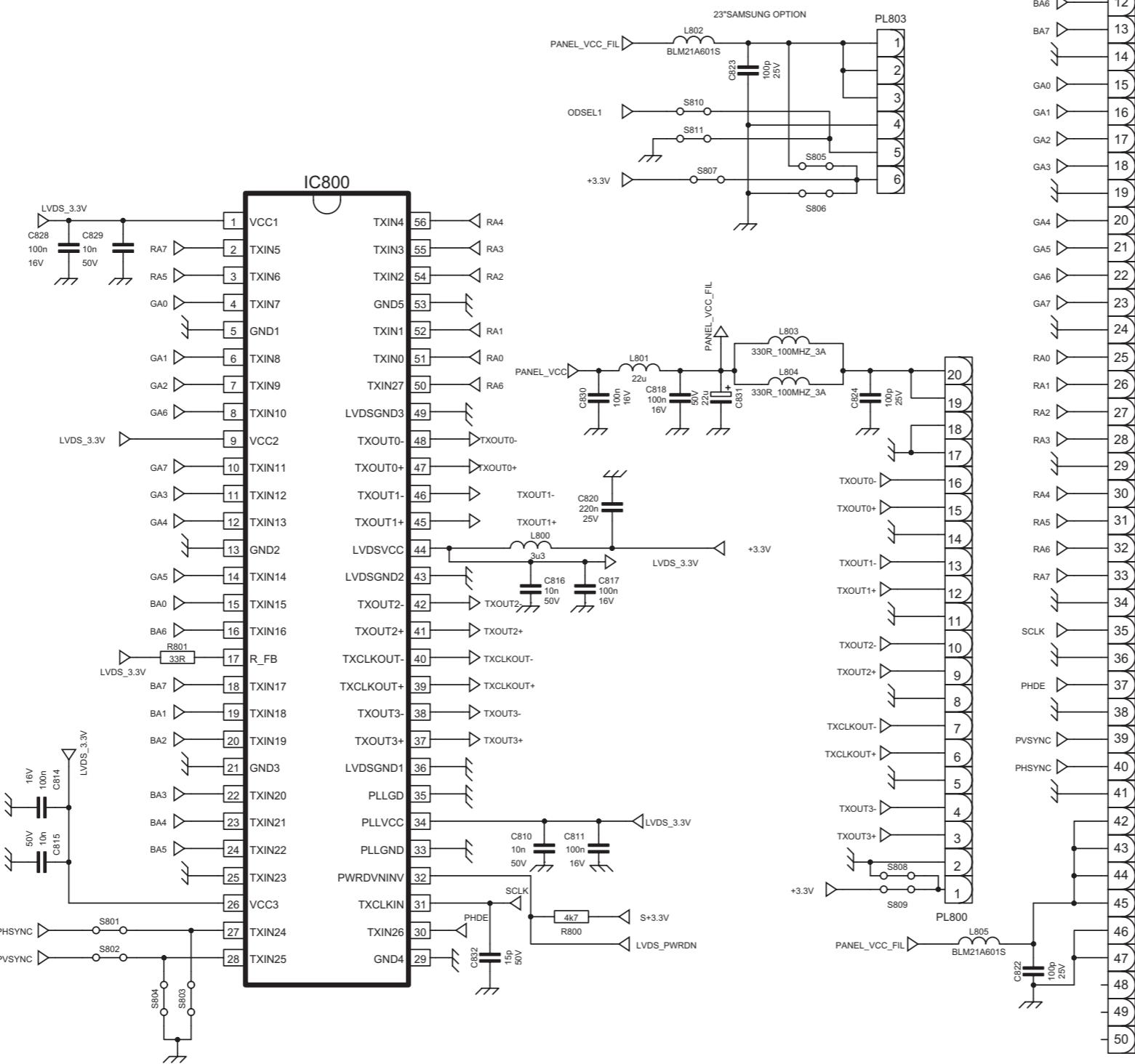
## *MAIN PWB CIRCUIT DIAGRAM (6/8)*



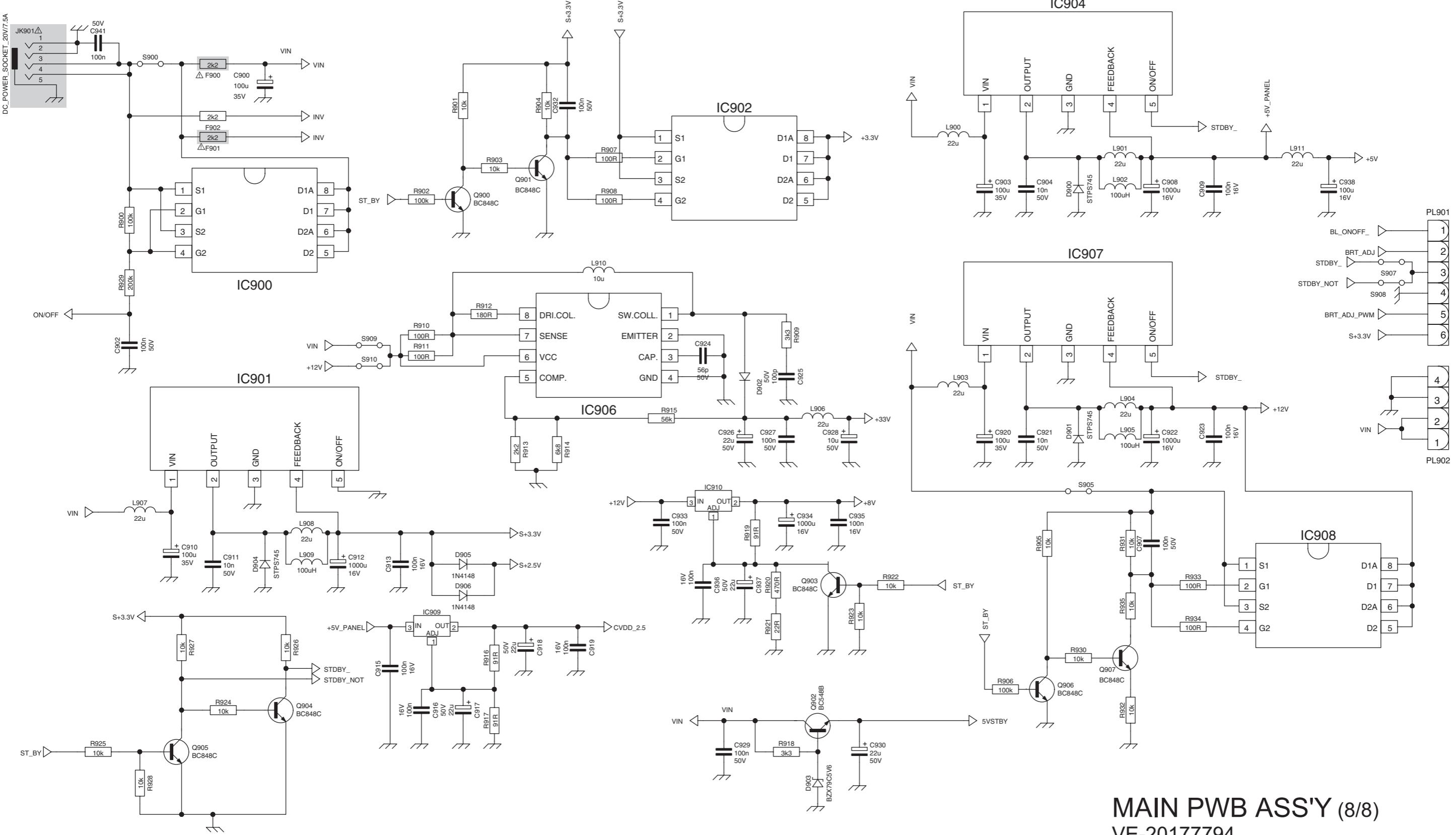
# MAIN PWB ASS'Y (6/8)

VE-20177794

**MAIN PWB ASS'Y (7/8)**  
VE-20177794

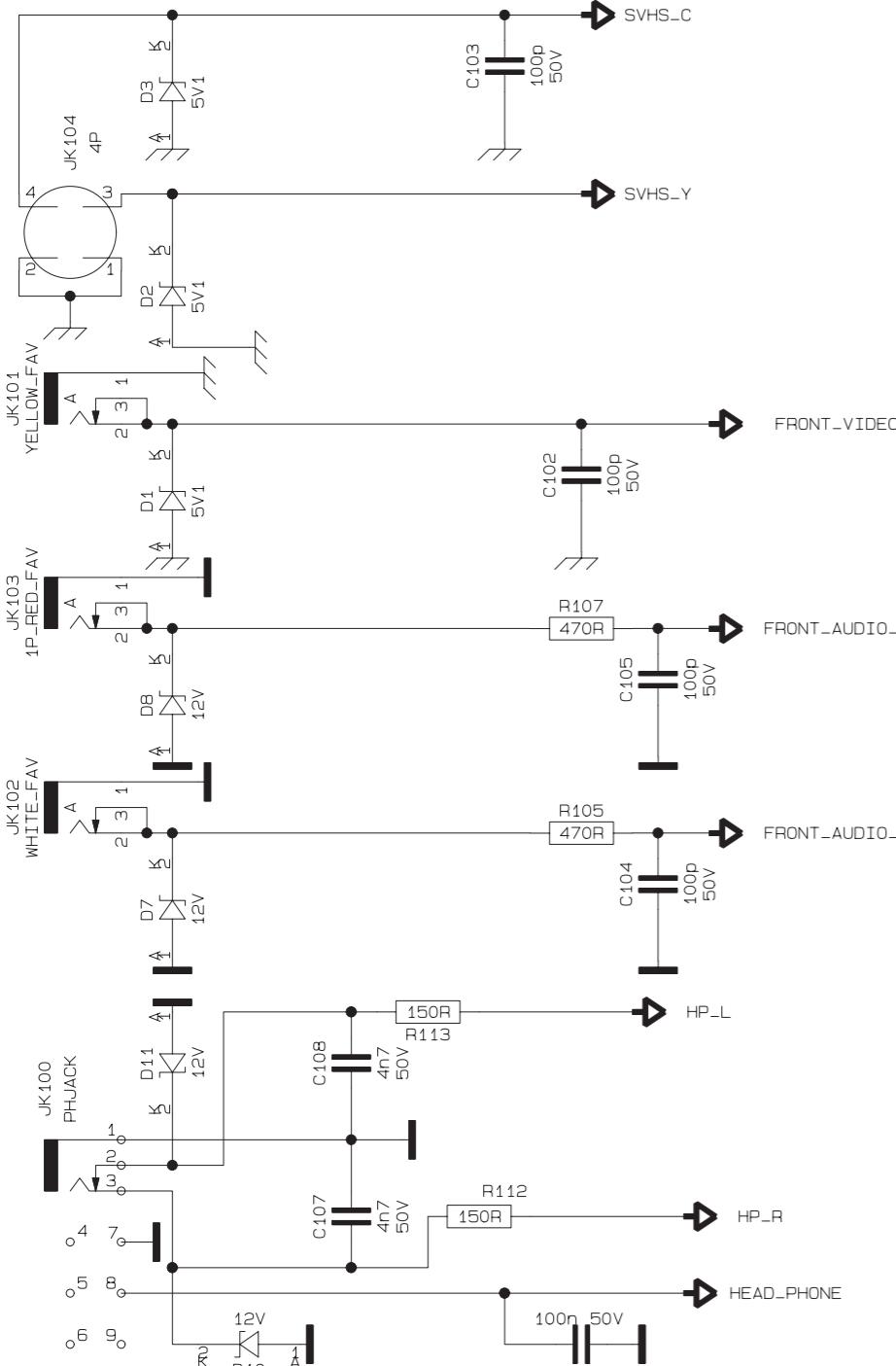


MAIN PWB CIRCUIT DIAGRAM (8/8)



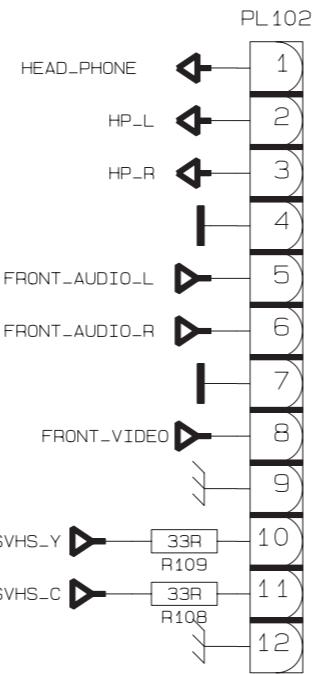
MAIN PWB ASS'Y (8/8)  
VE-20177794

AV JACK/ HEADPHONE PWB CIRCUIT DIAGRAM

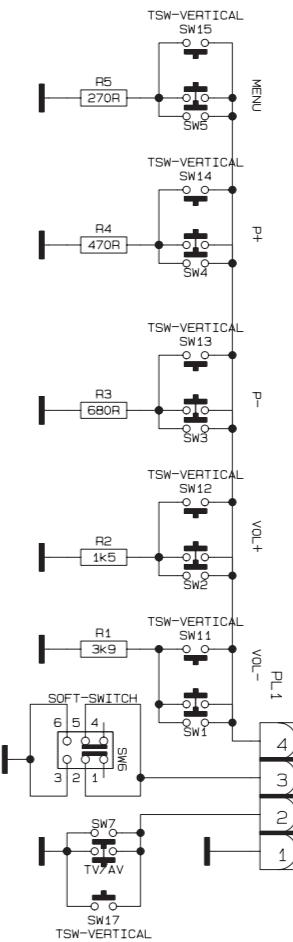


AV JACK/ HEADPHONE PWB ASS'Y  
VE-20180067

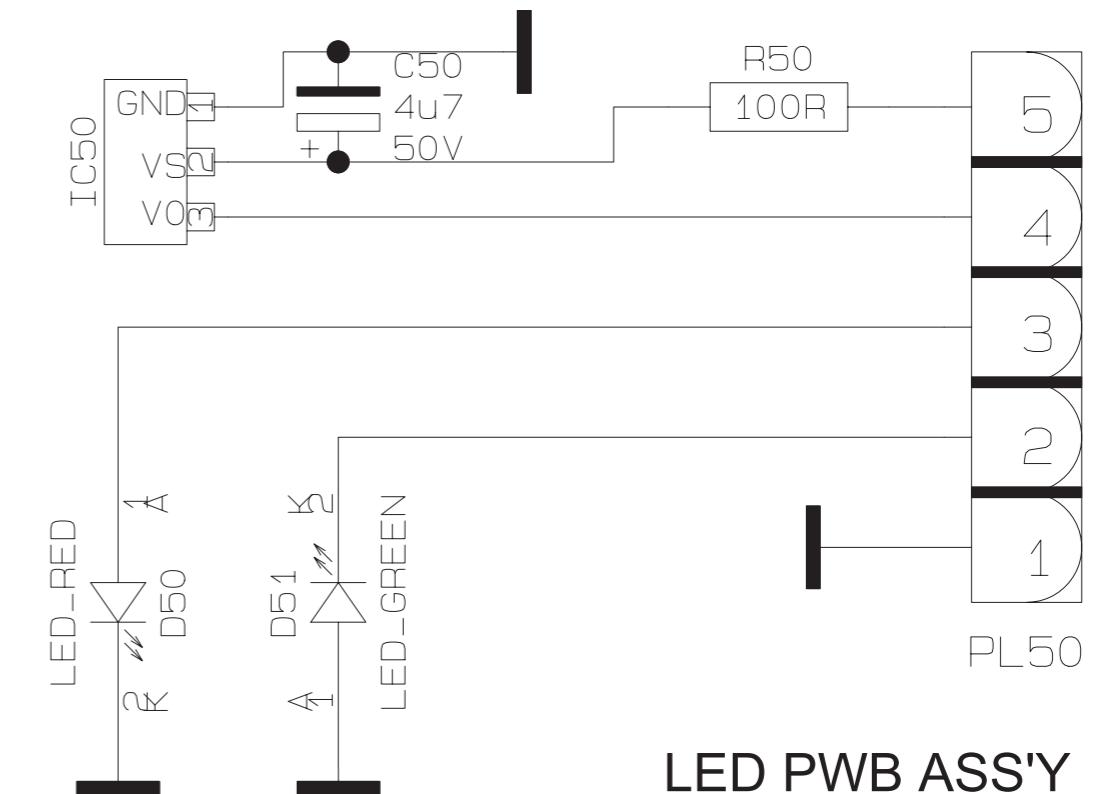
CONTROL PWB CIRCUIT DIAGRAM



CONTROL  
PWB ASS'Y  
VE-20180094



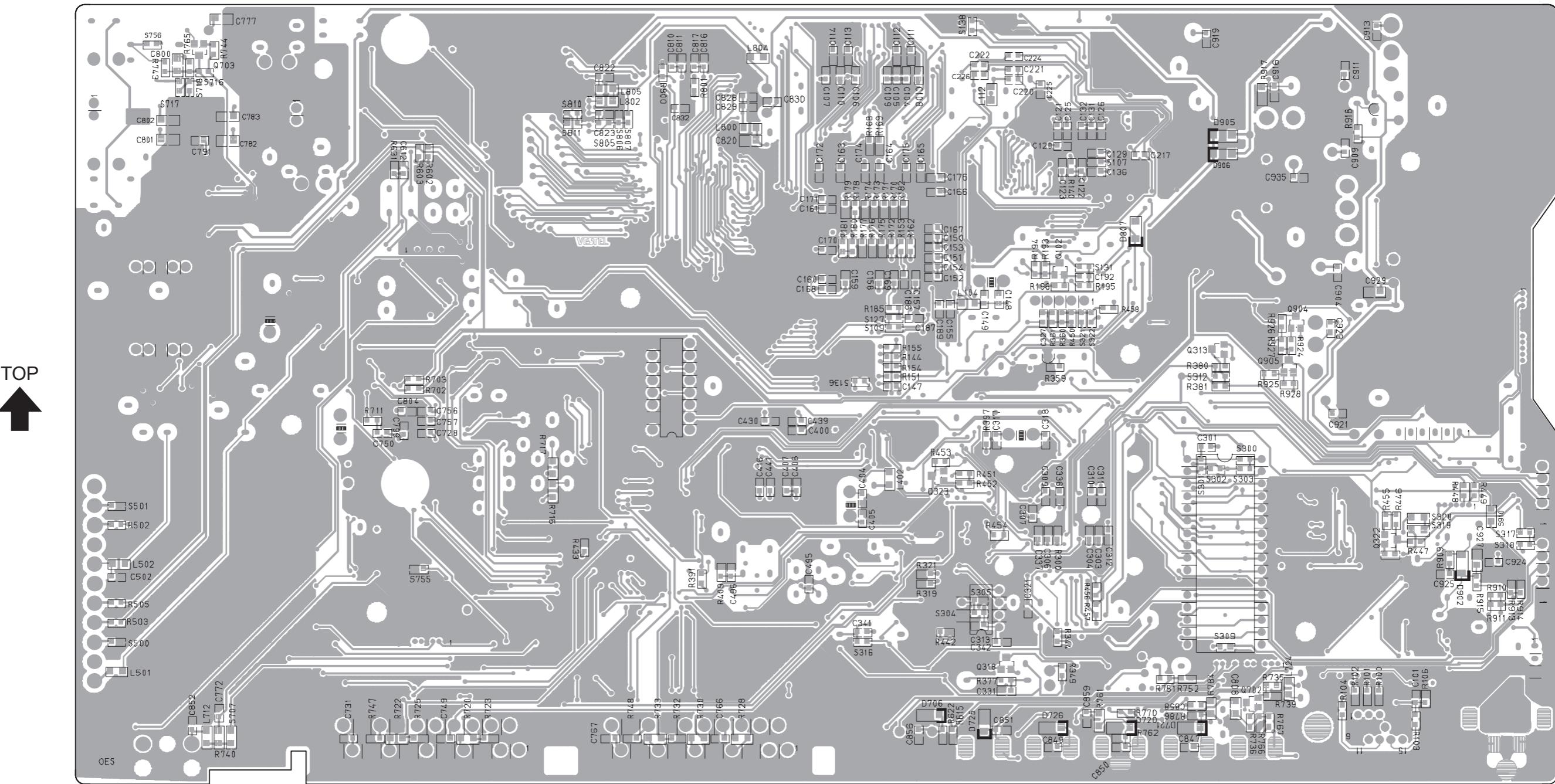
LED PWB CIRCUIT DIAGRAM



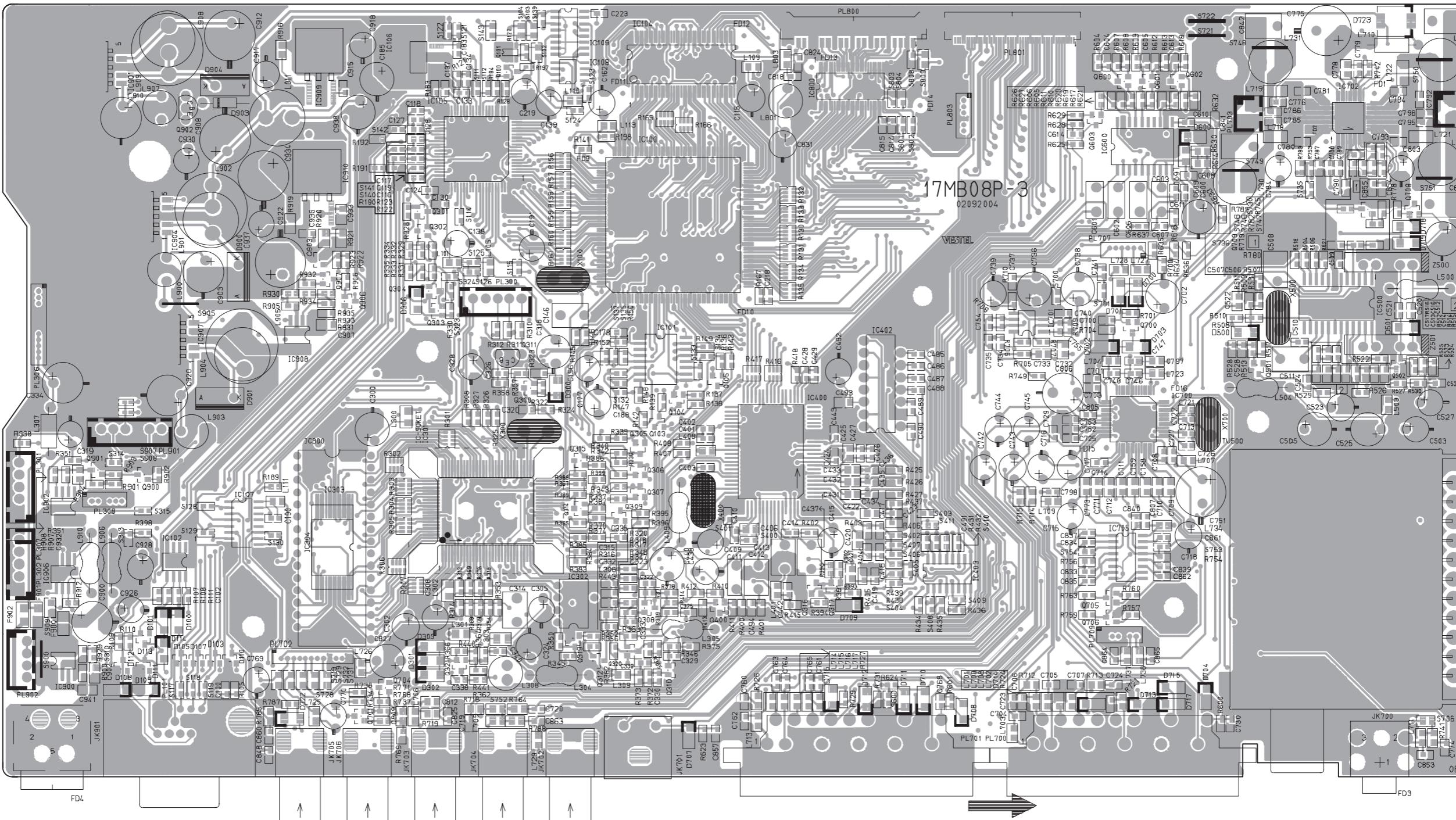
LED PWB ASS'Y  
VE-20180117

## PATTERN DIAGRAMS

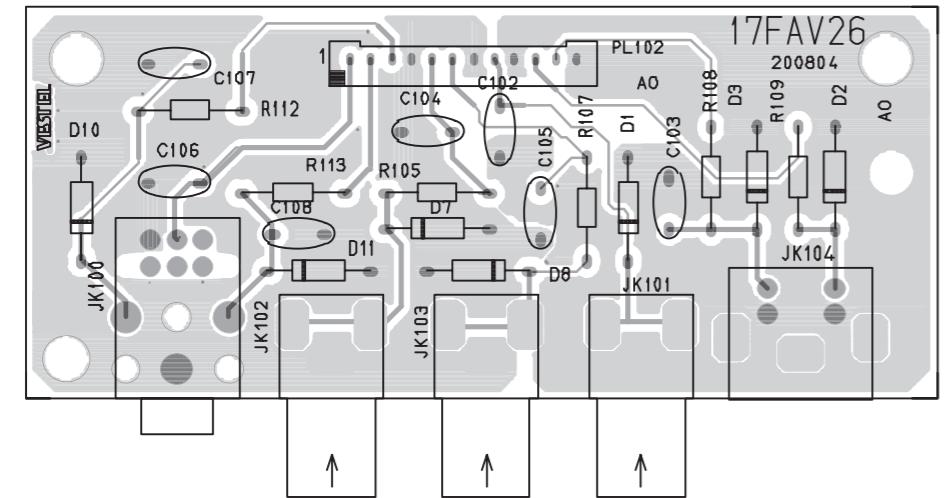
### *MAIN PWB PATTERN [SOLDER SIDE]*



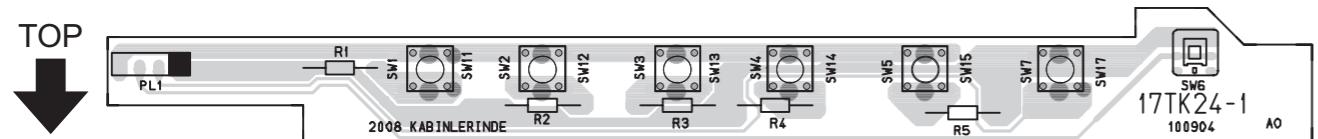
### *MAIN PWB PATTERN [PARTS SIDE]*



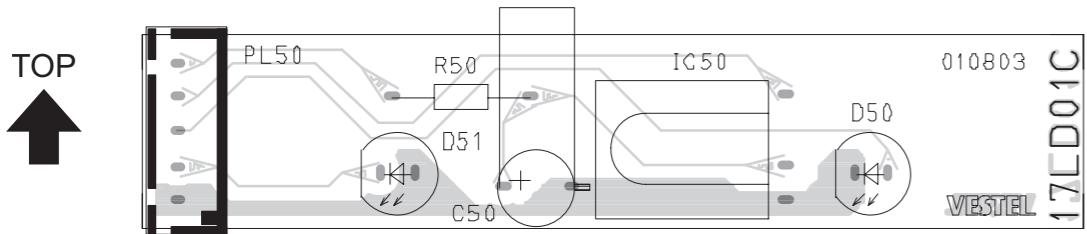
## AV JACK/ HEADPHONE PWB PATTERN



CONTROL PWB PATTERN



LED PWB PATTERN



The JVC logo consists of the letters "JVC" in a bold, black, sans-serif font. The "J" and "V" are connected vertically, while the "C" is separate.

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