

GRUNDIG SERVICE MANUAL

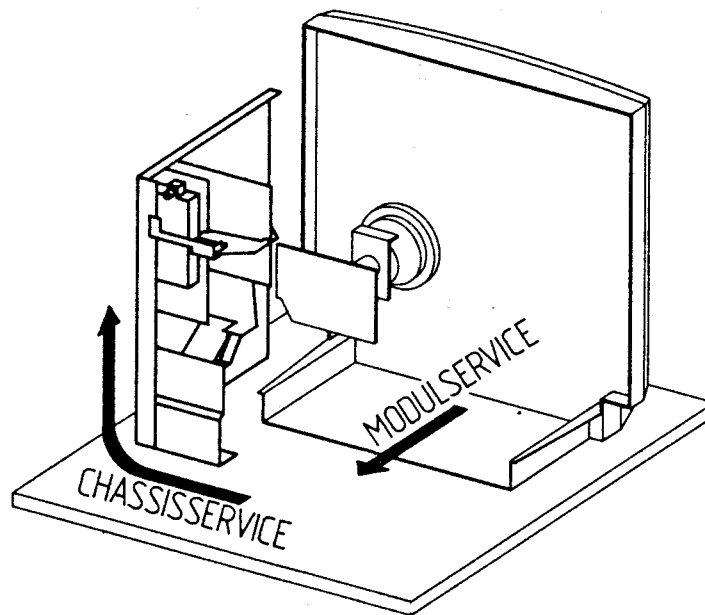


Ⓟ Btx * 32700 #

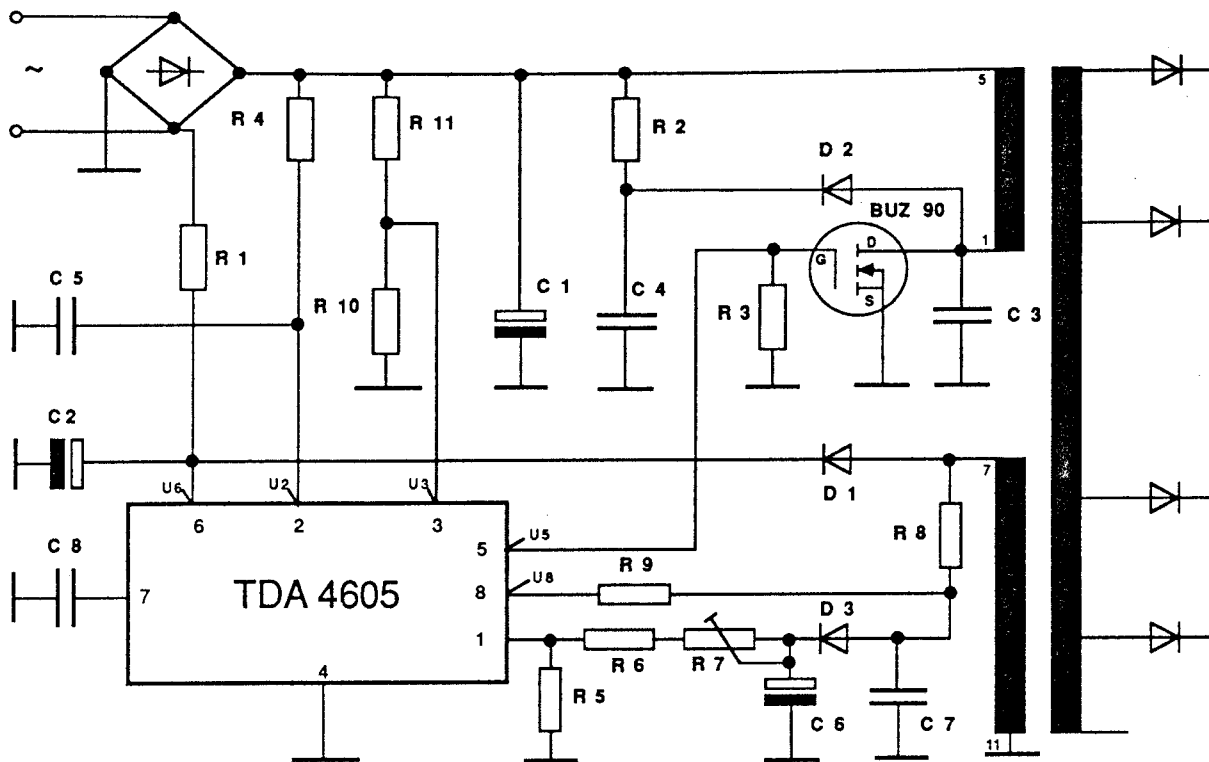
CUC 4511

M 63 - 575 BL / NIC	(9.25737-68)	ST 63 - 575 BL text	(9.25737-06)	ST 70 - 560 P.I.P. / NIC	(9.25718-68)
M 63 - 575 / 9 BL text	(9.25737-02)	ST 70 - 560	(9.25681-01)	ST 70 - 560 text	(9.25681-02)
ST 63 - 560 text	(9.25682-02)	ST 70 - 560 NIC	(9.25681-68)	TVC 7000	(9.25671-01)
ST 63 - 560 NIC	(9.25682-68)	ST 70 - 560 NIC / FT / GB	(9.25681-66)	TVC 7000 NIC	(9.25671-68)
ST 63 - 560 NIC / FT / GB	(9.25682-66)	ST 70 - 560 P.I.P. text	(9.25718-02)	TVC 7000 NIC / GB	(9.25671-65)

Servicestellung
Service position



MAIN CIRCUIT DIAGRAM WITH A DESCRIPTION OF THE START-UP PROCESS



START-UP PROCESS

From the application of the Mains Voltage to Time t_0 the voltages applied to the IC rise as follows:
 U_6 (Pin 6) corresponds to the half-wave charging process via R1
 U_2 (Pin 2) to U_{2MAX}
 U_3 (Pin 3) to a value fixed by the divider R10/R11.

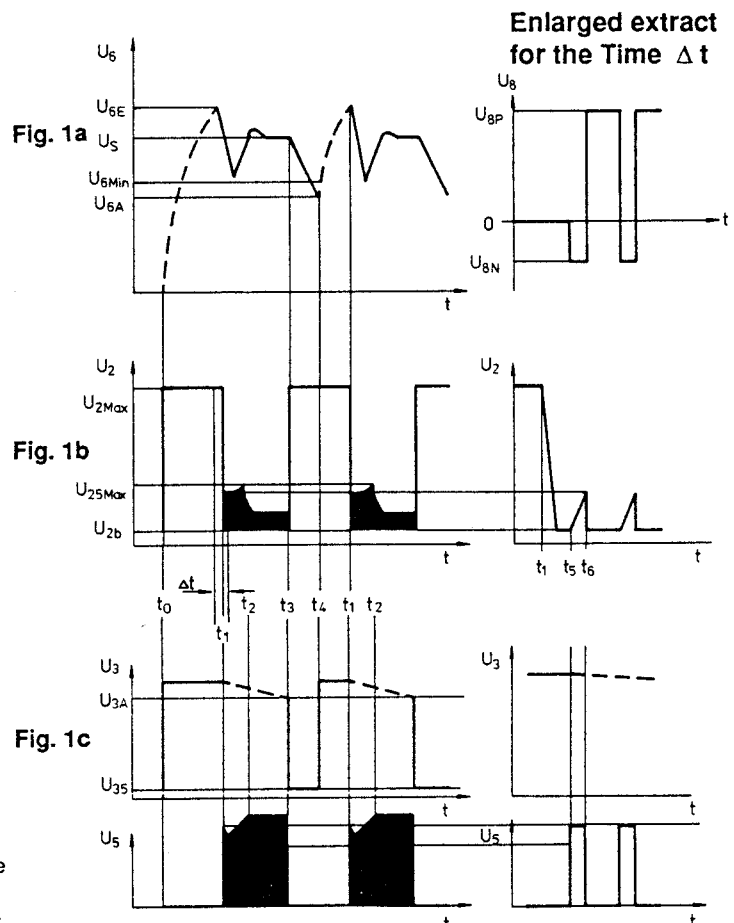
The current consumption of the IC in this operating mode is less than 1,6mA.

When U_6 reaches the Treshold U_{6E} (Time Point t_1), the IC switches the internal Reference Voltage on. The current consumption rises to a max. 12mA. The Primary Current-Voltage Converter controls U_2 to below the level on U_{2B} and from Time Point t_5 to t_6 the Start Pulse Circuit generates the Start Pulse. The Feedback to Pin 8 starts the next pulse and so on. All pulses, including the Start Pulse, are controlled in pulse width in relation to the Control Voltage on Pin 1. This corresponds, on Switch on, to the Short Circuit case, i.e. $U_1 = 0V$. During this the IC operates with "Short Circuit Pulses" which are then increased in pulse width due to the Control Voltage in the Feedback path. (The IC operates in the Overload Range). At Time Point t_2 the maximum pulse width is reached ($U_2 = U_{2B MAX}$). The IC operates now in reverse mode. Thereafter the peak value reduces quickly to U_2 because the IC is operating in the Control Range. The control loop is now in a steady state (locked in).

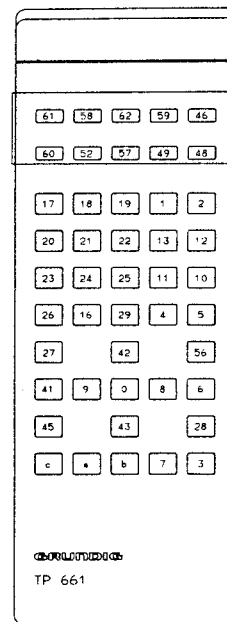
If the Voltage U_6 falls below the Switch Off Treshold U_{6MIN} before the reversal point is reached, the Start attempt is interrupted (Pin 5 is switched to LOW). As the IC remains switched on, the U_6 reduces further to U_{6A} . The IC switches off, U_6 can now rise (Time Point t_4) and a new switch on attempt can begin from Time Point t_1 .

When, due to loading, the rectified Mains Alternating Voltage (primary voltage) breaks down, U_3 reduces as indicated from Time Point t_3 to below U_{3A} . The Primary Voltage monitoring circuit clamps U_3 to U_{3B} until the IC switches off ($U_6 < U_{6A}$) at Time Point t_4 . Then a new switch on attempt begins from Time Point t_1 .

Start-Up Diagram

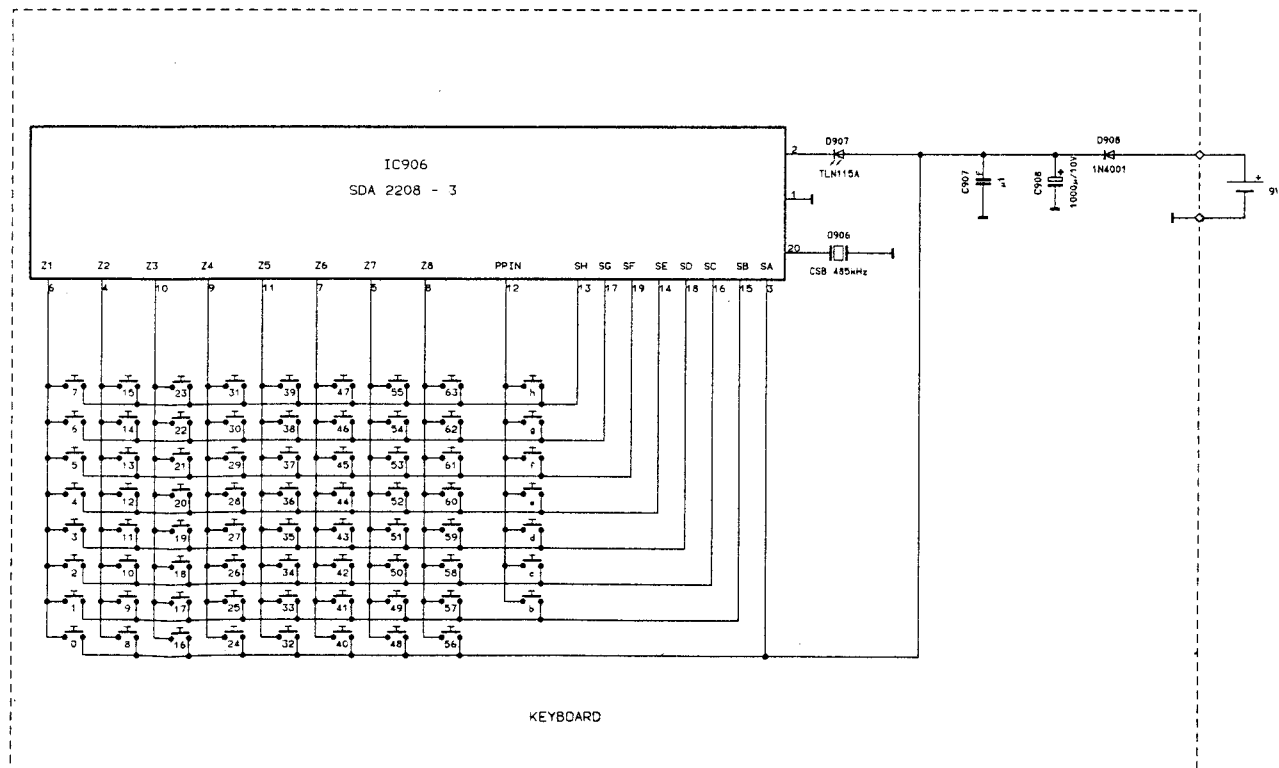


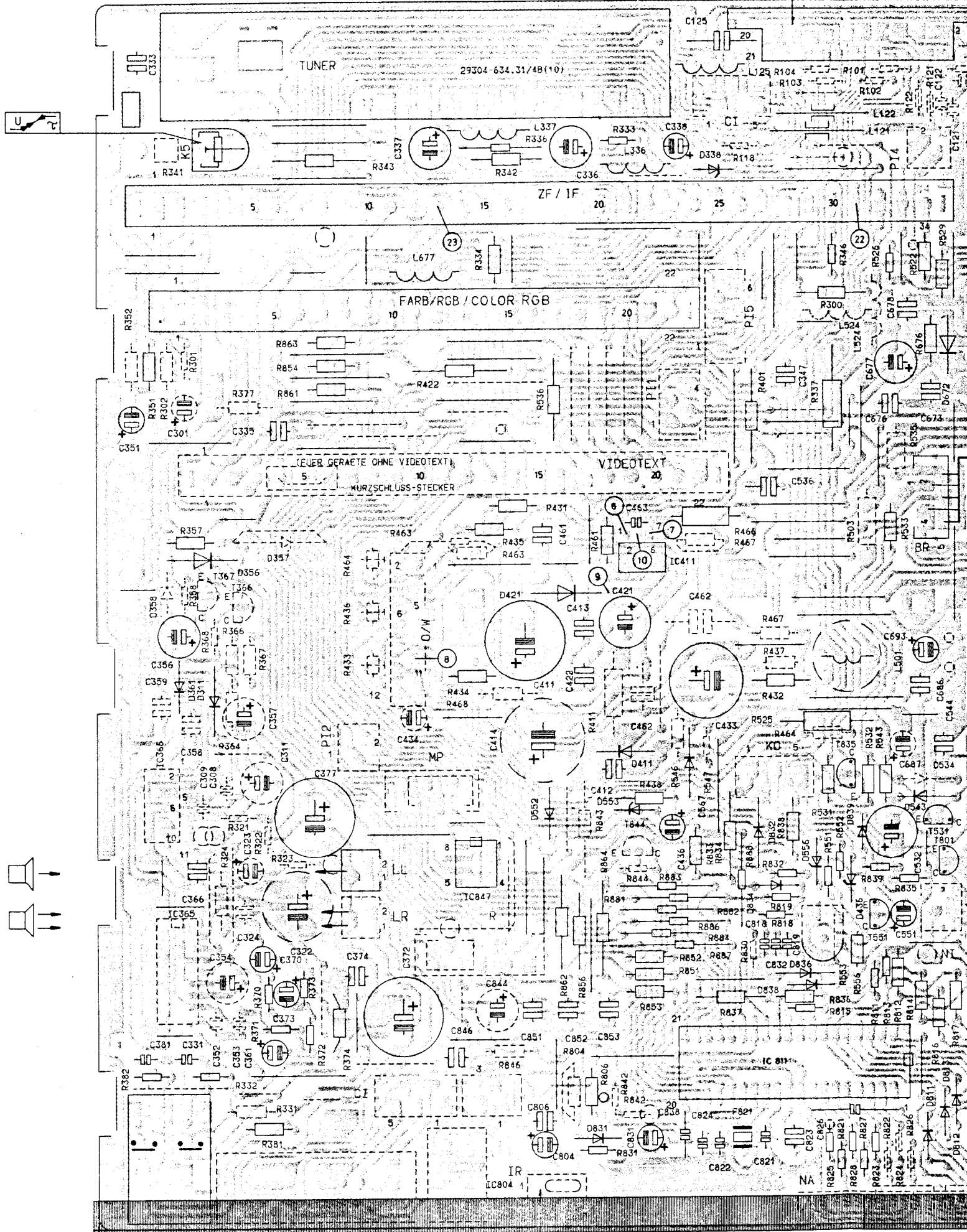
TP 661
 TP 661 FASTTEXT
 TP 661 TOP



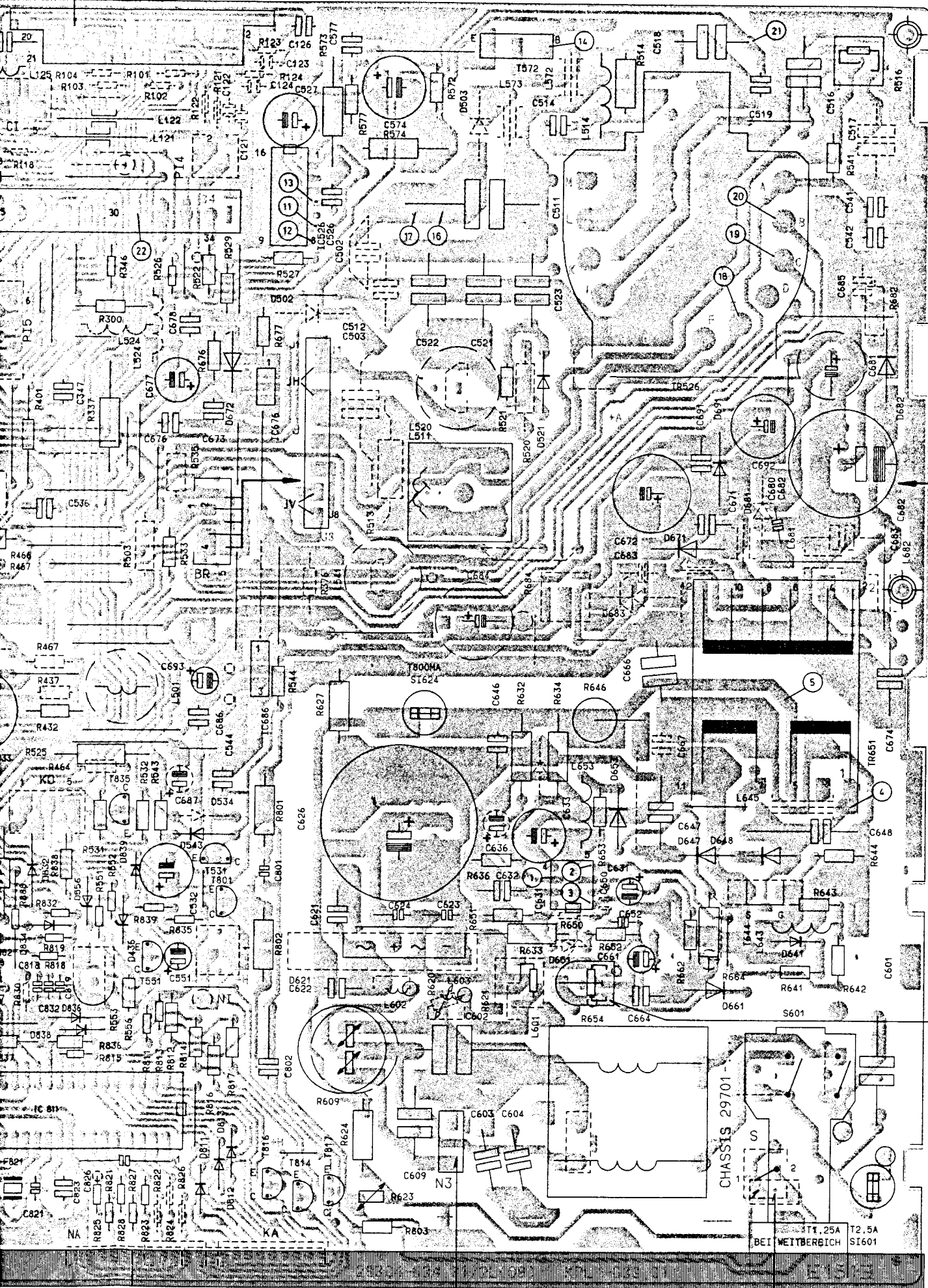
STOP FLER
 STANDBILD/DIG.

FERNBEDIENUNG 29622-053.01
 -053.12 FASTTEXT
 -053.13 TOPTEXT
 29304-620.71 KEYBOARD





EURO AV-BUCHSE
EURO AV-SOCKET



+UB
U LEUCHTPUNKT
SSB
U 220
U 220

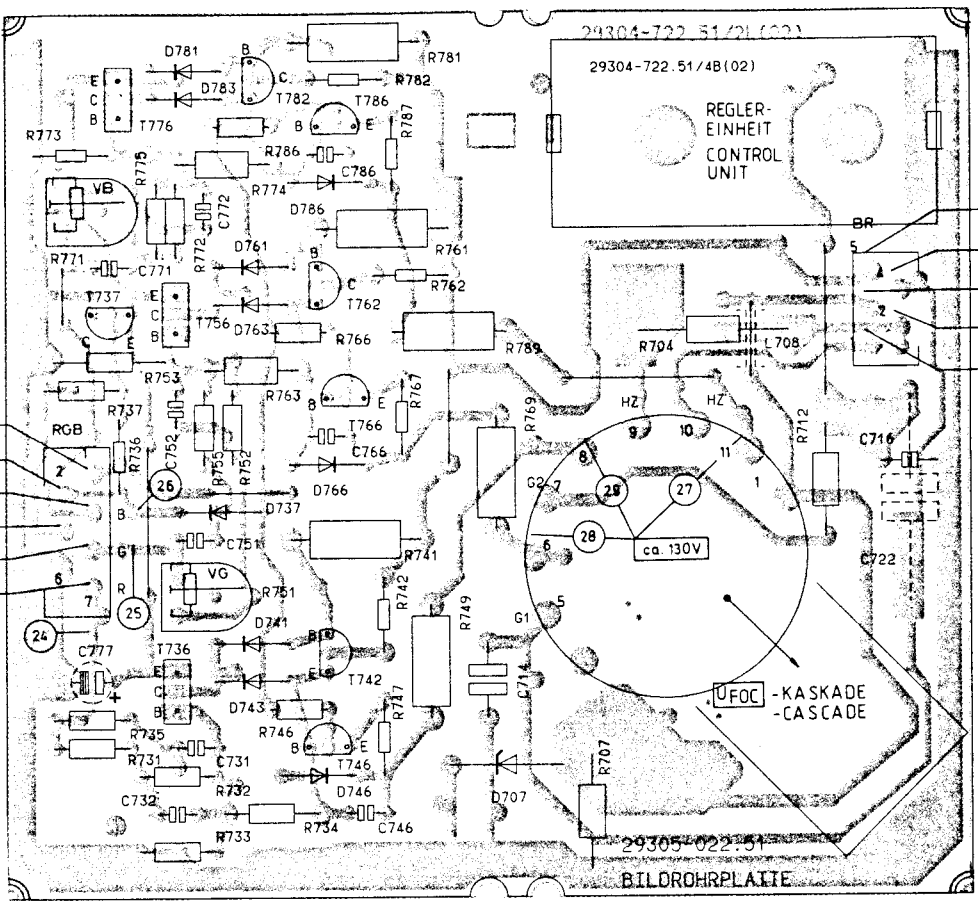
+A / min

ENTMAGNETISIERUNG
DEGAUSSING

WISCHER
TEMP CONTACT

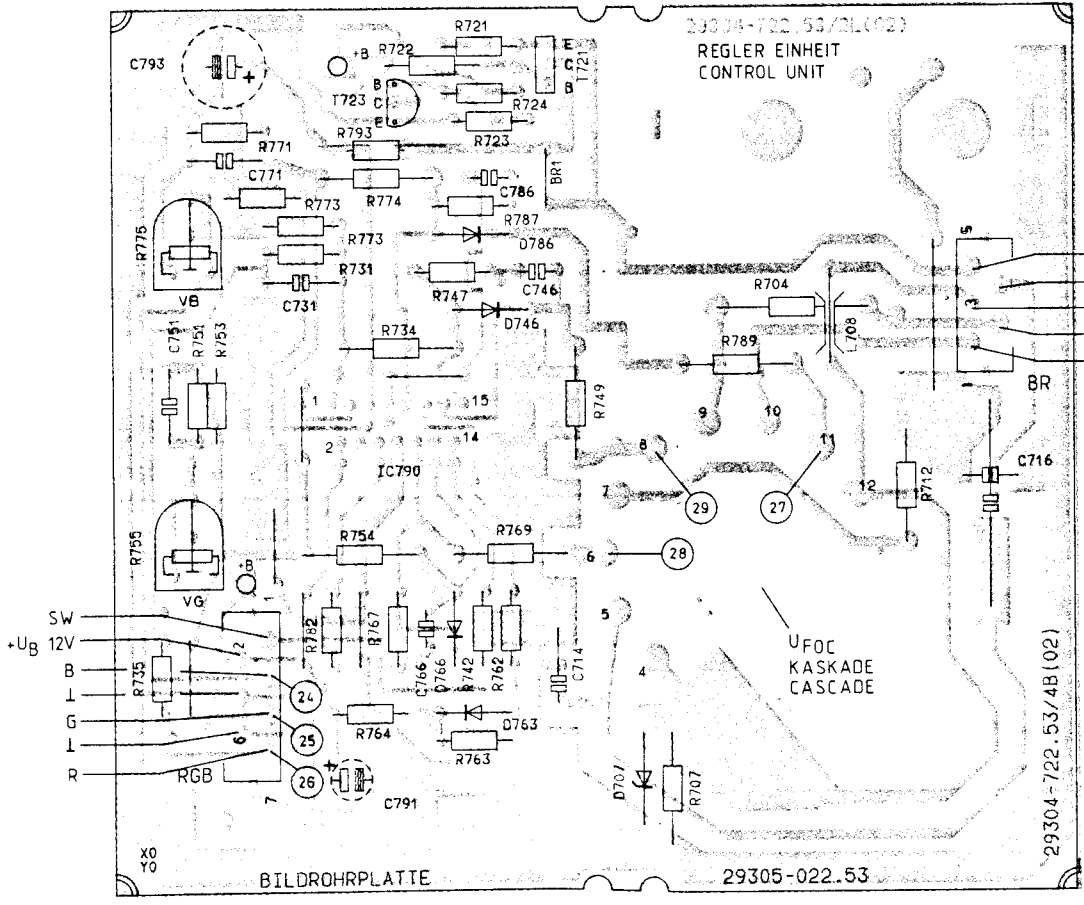
NKT
ZUR BILDROHRPLATTE
TO CRS BASE

ZUM FARB RGB
TO COLOUR RGR



+1/200V
U LEUCHTP
SSB
mm
mm

ZUM CHASSIS
TO CHASSIS

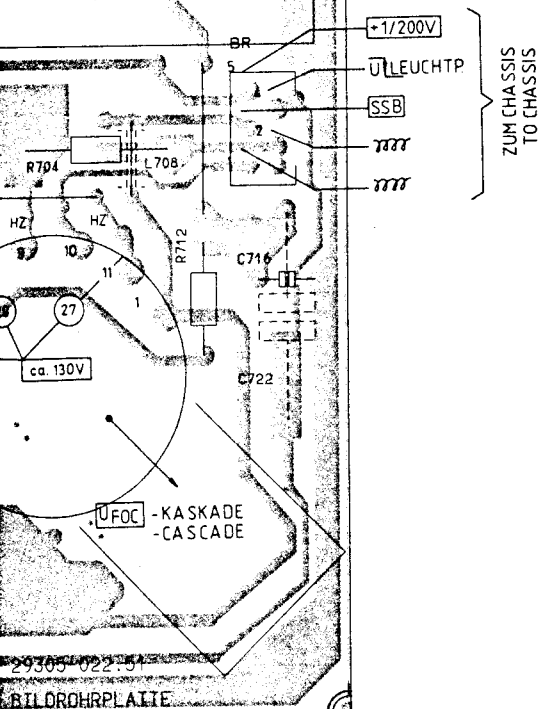


+UB 200V
U LEUCHTPUNKT
SSB
U mm
U mm

ZUM CHASSIS
TO CHASSIS

304-722,51/4B(02)

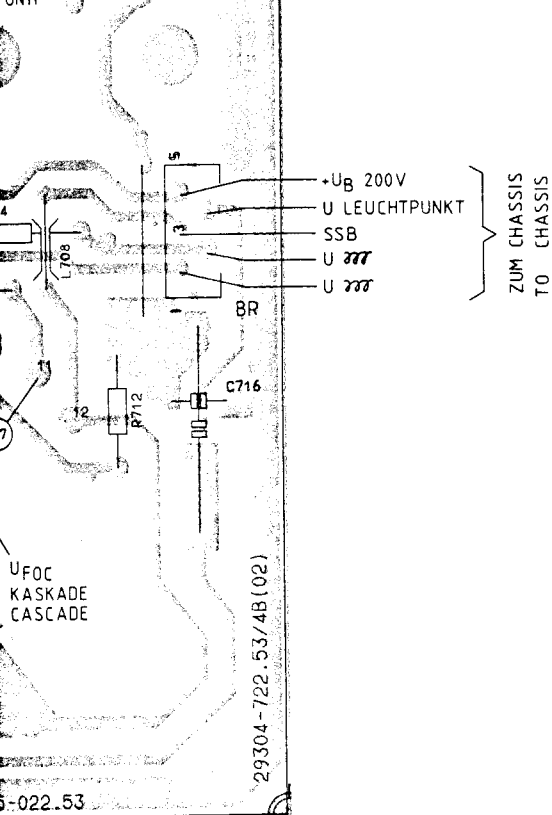
REGLER-
EINHEIT
CONTROL
UNIT



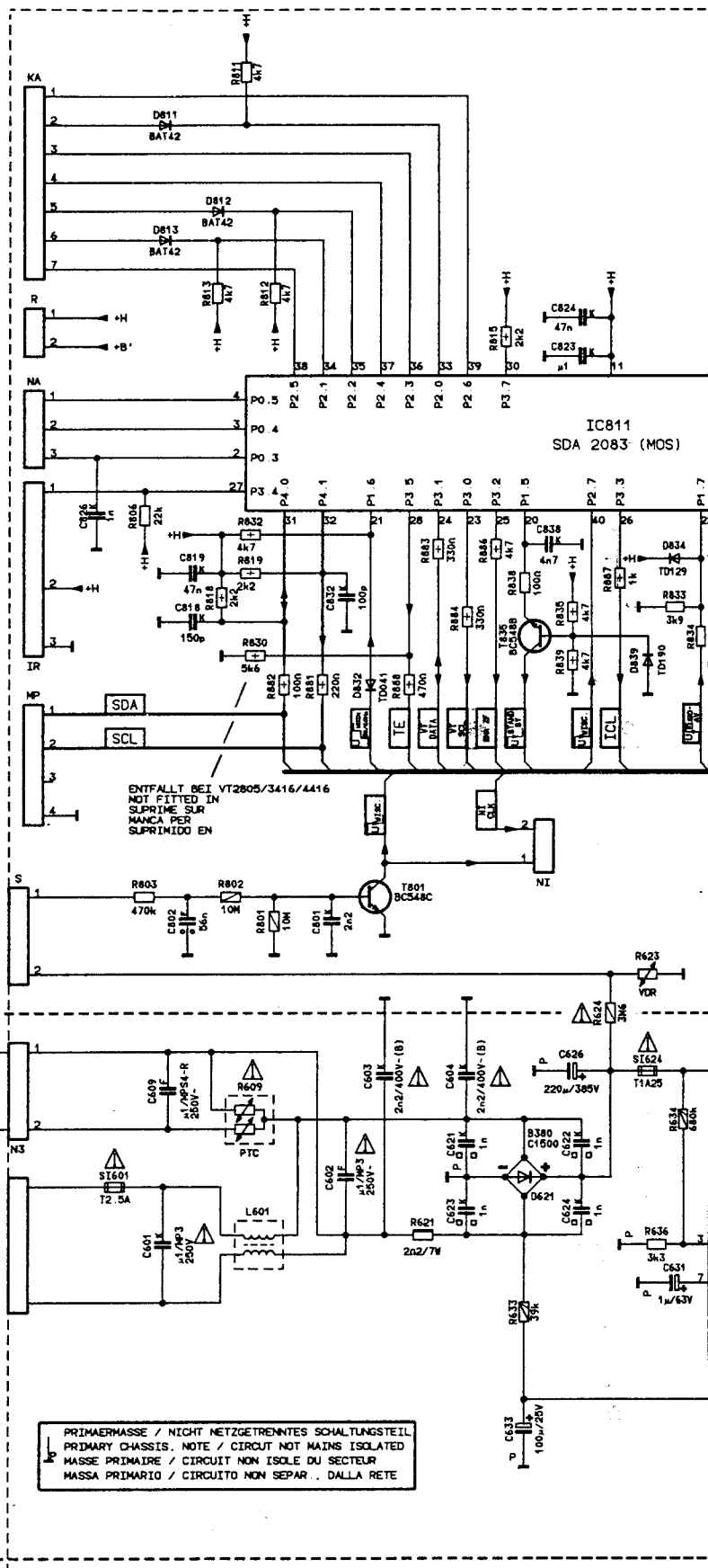
BILDROHRPLATTE

2 53/21 (02)

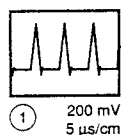
UNIT



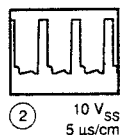
5-022.53



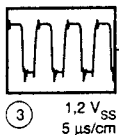
PRIMAERMASSE / NICHT NETZGETRENNTES SCHALTUNGSTEIL
PRIMARY CHASSIS. NOTE / CIRCUIT NOT MAINS ISOLATED
MASSE PRIMAIRE / CIRCUIT NON ISOLE DU SECTEUR
MASSA PRIMARIO / CIRCUITO NON SEPAR. DALLA RETE



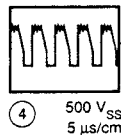
① 200 mV
5 μ s/cm



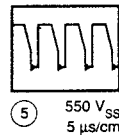
② 10 V_{SS}
 $5\text{ }\mu\text{s/cm}$



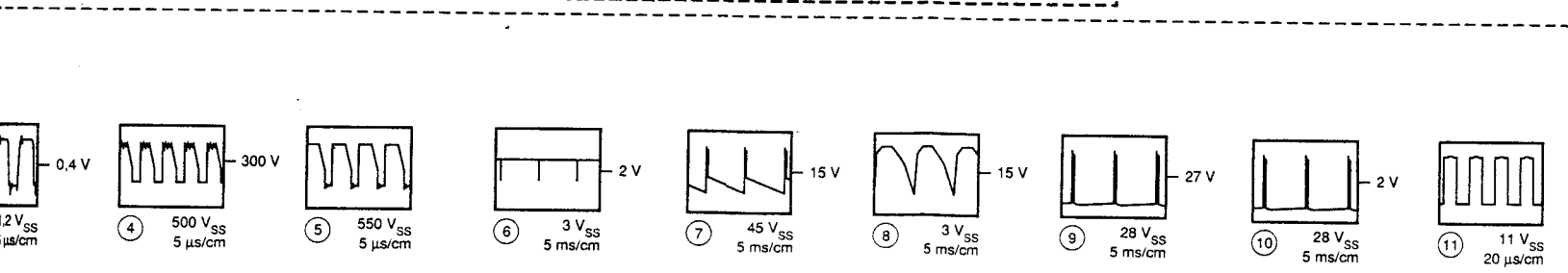
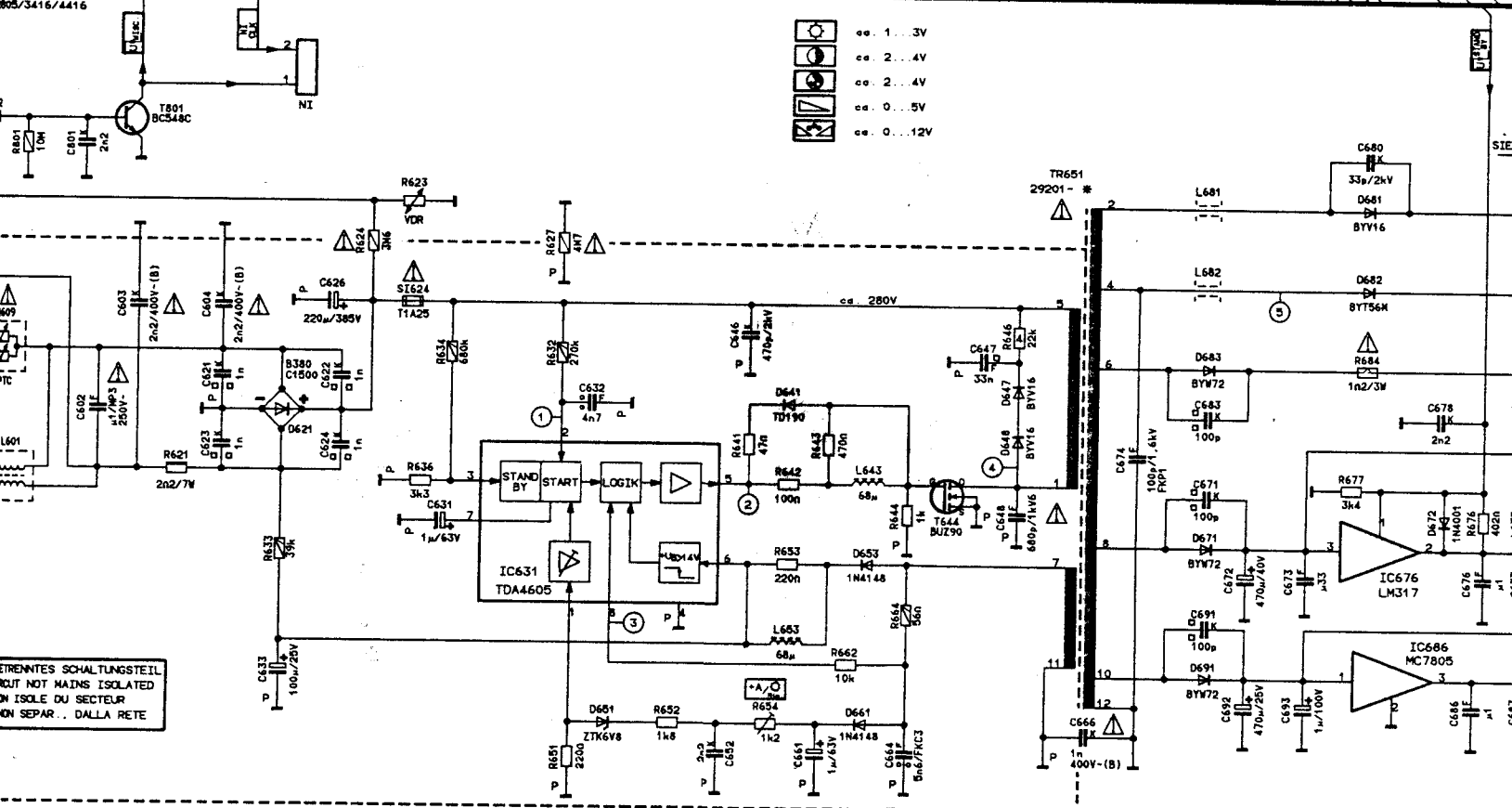
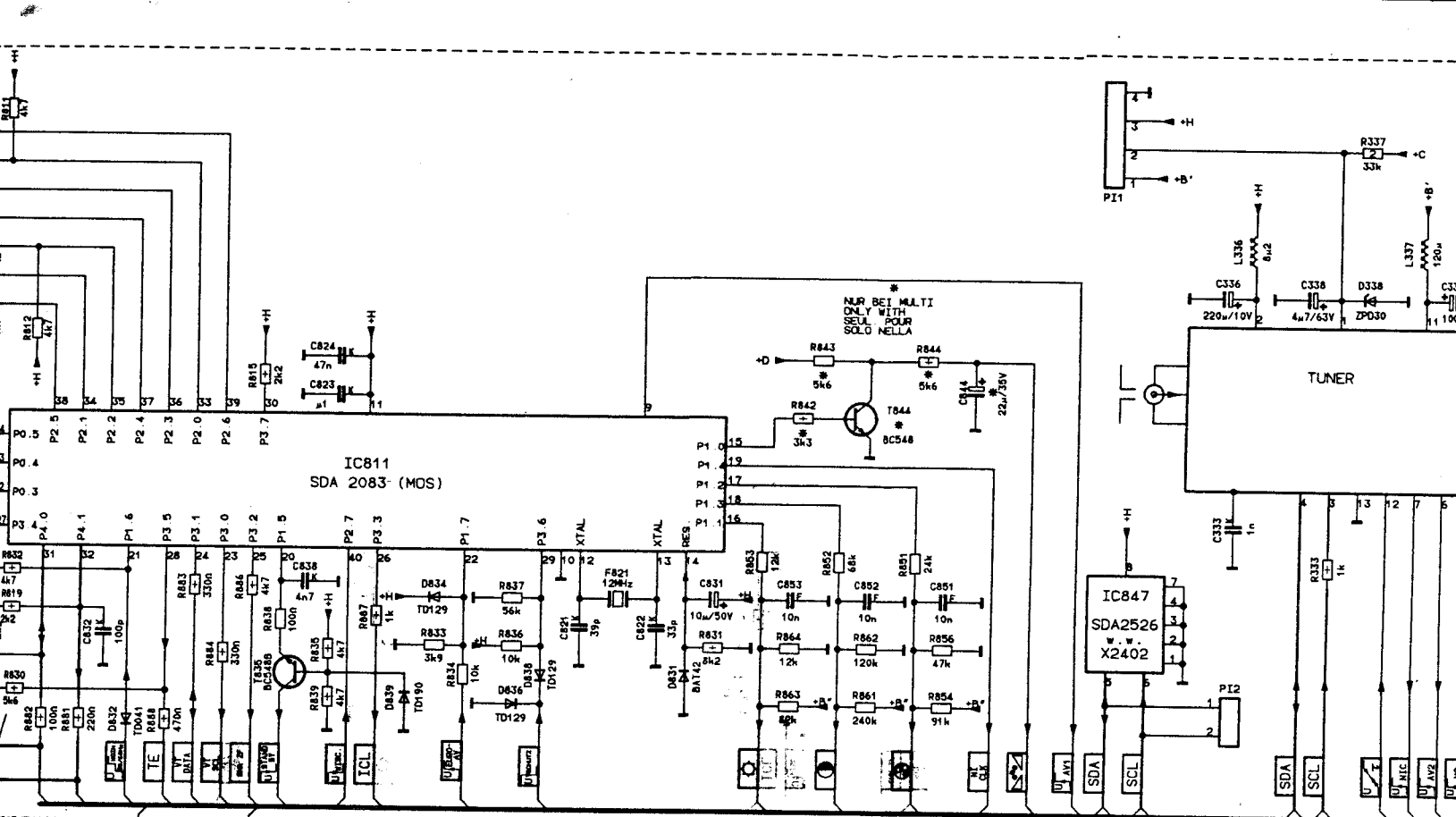
③ 1,2 V_{SS}
5 μs/cm

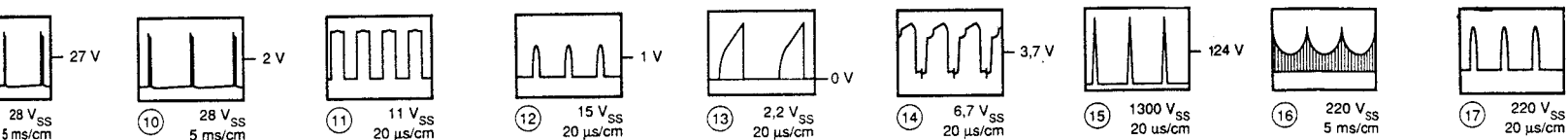
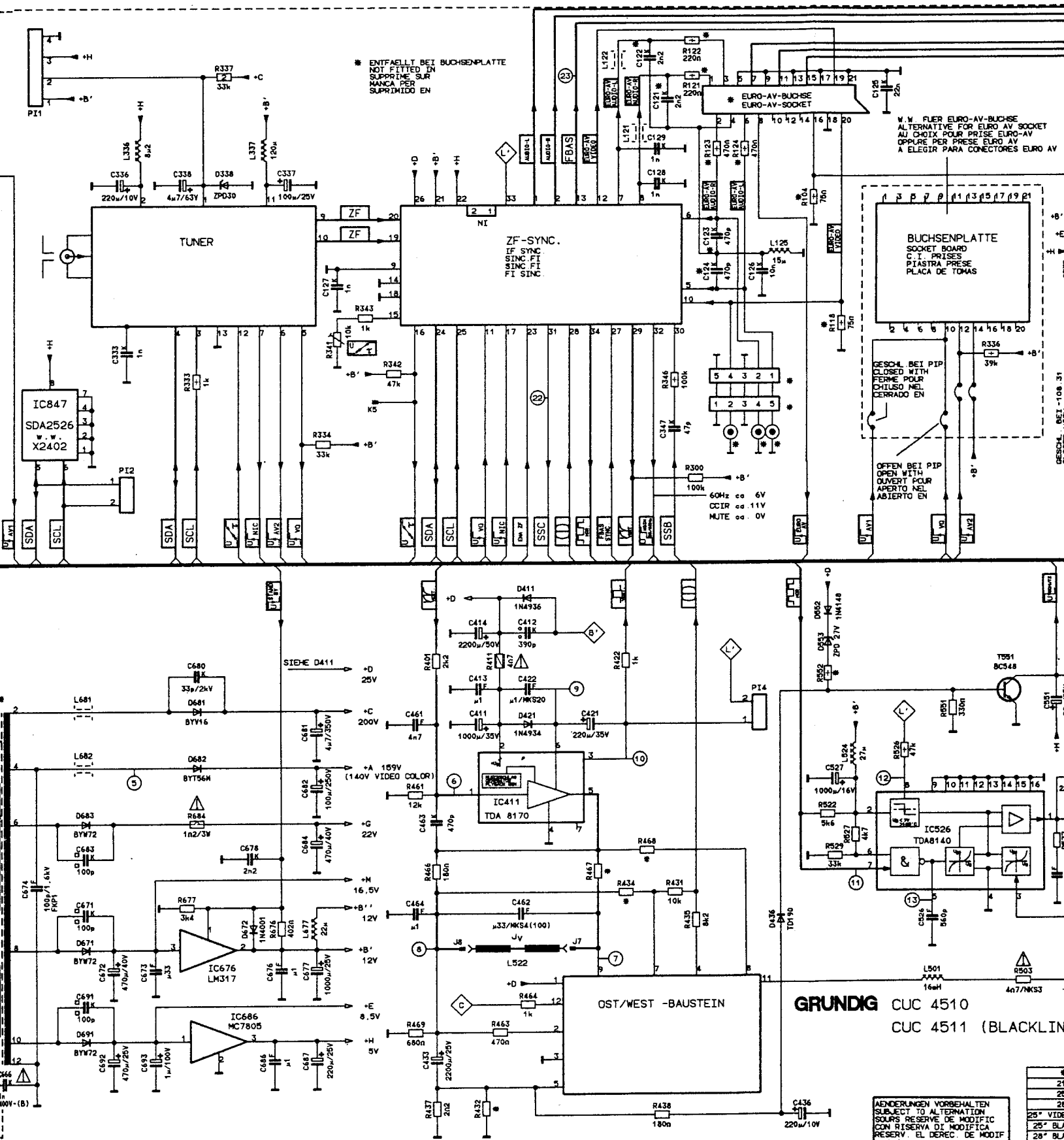


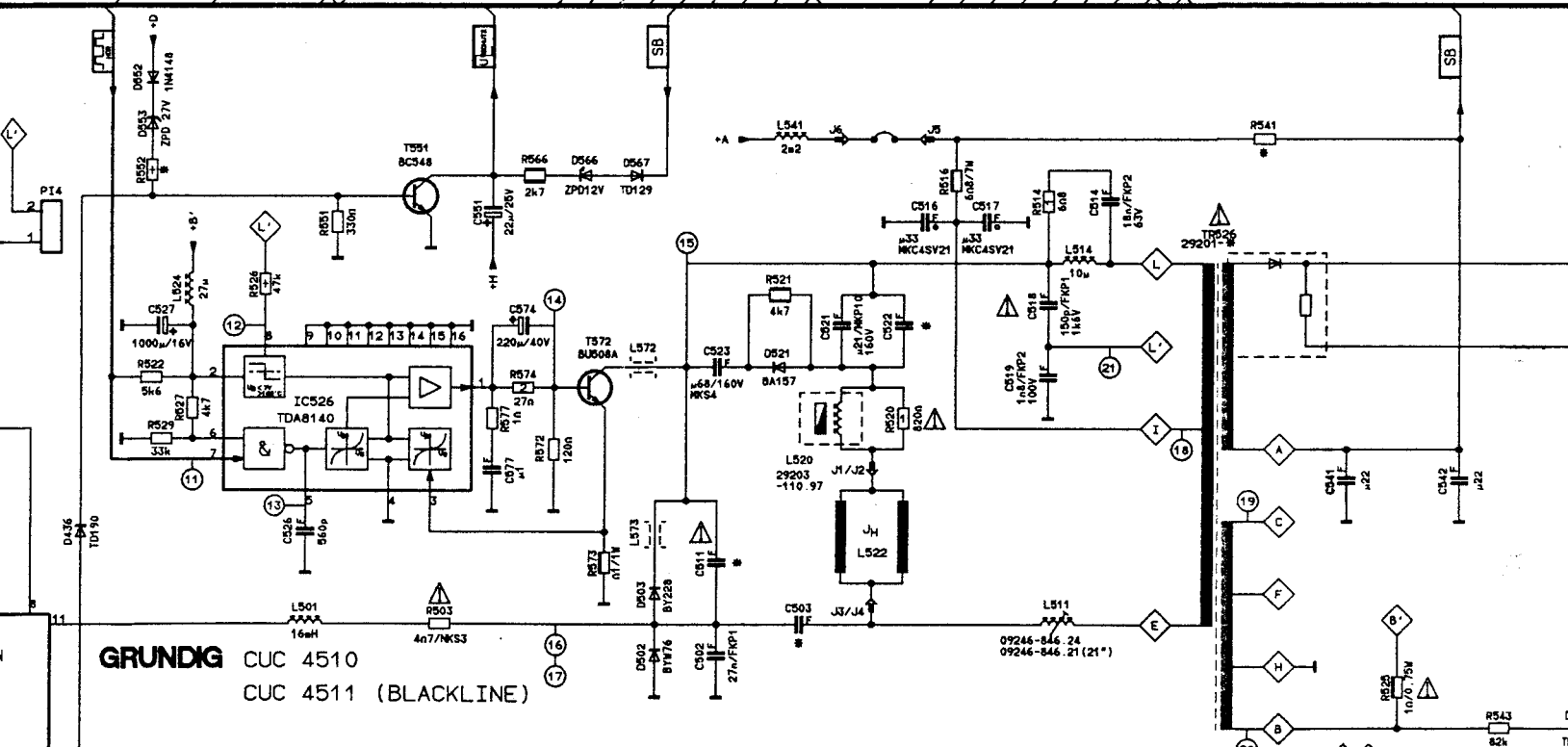
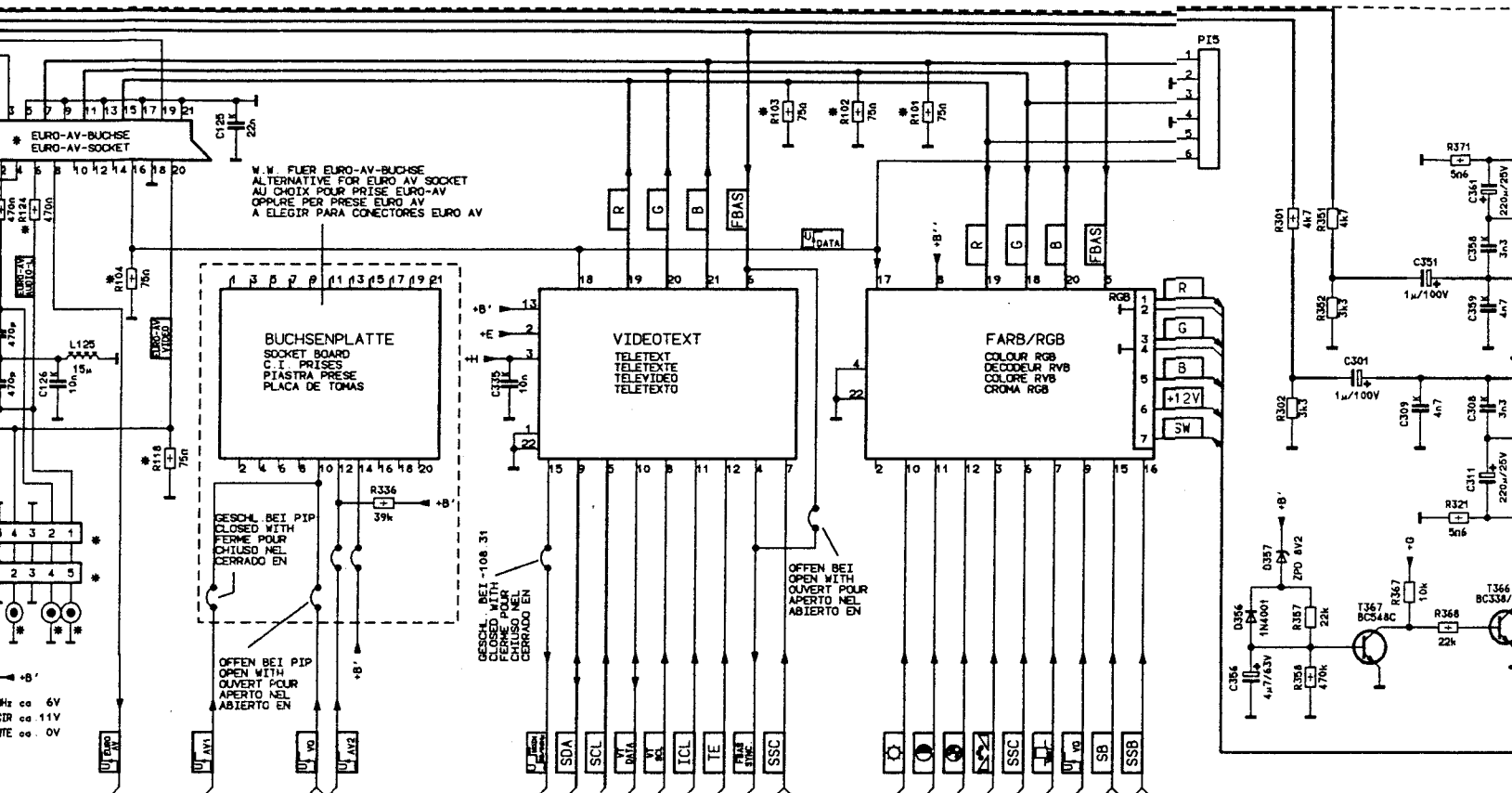
④ 500 V_{SS}
5 μs/cm



⑤ 550 V_{SS}
5 μs/cm

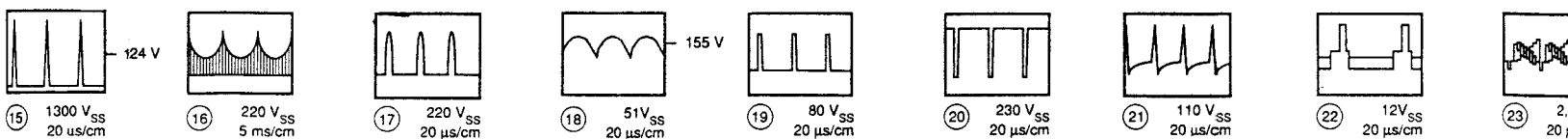


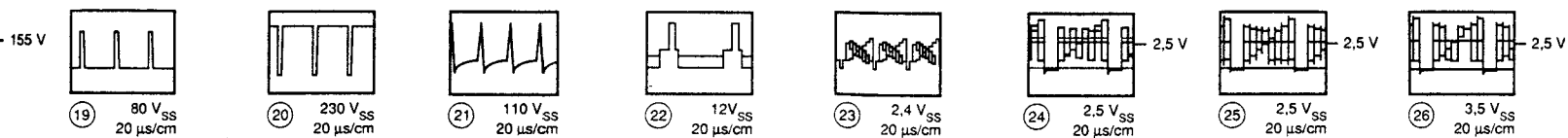


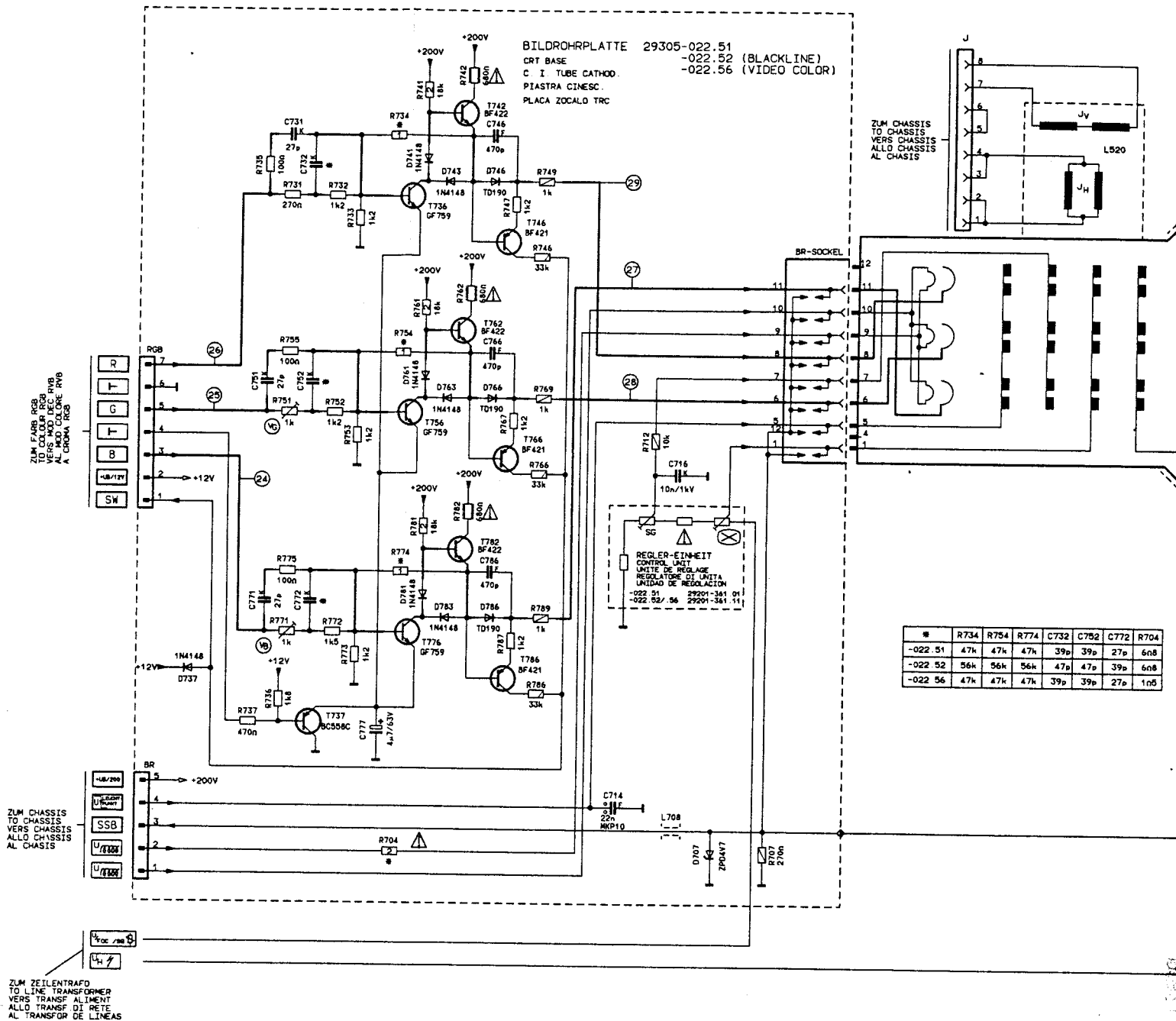


	R432	R434	R467	R468	C503	C522	C511	TR526	TR651	R552	R541	R536
21"	2n2	33k	n68	13k	μ36/MKP10	μ18	8n3/FKP1	-029.01	-316.97	1k8	270k	3k3
25"	2n2	33k	n68	13k	μ36/MKP10	μ26	9n/FKP1	-029.01	-316.97	1k8	270k	3k3
28"	2n2	33k	n68	13k	μ36/MKP10	μ33	9n/FKP1	-029.01	-316.97	1k8	270k	3k3
25" VIDEO COLOR	5n6	27k	1n5	22k	μ68/MKP10	μ33	11n/FKP1	-029.04	-317.97	1k8	270k	3k3
25" BLACKLINE	2n2	33k	n68	15k	μ36/MKP10	μ26	9n5/FKP1	-029.02	-312.97	1k2	200k	1k5
28" BLACKLINE	2n2	33k	n68	15k	μ36/MKP10	μ33	9n5/FKP1	-029.02	-312.97	1k2	200k	1k5

ÄNDERUNGEN VORBEHALTEN
SUBJECT TO ALTERATION
SOUIS RESERVE DE MODIFIC
CON RISERVA DI MODIFICA
RESERV. EL DERECH. DE MODIF







D

Weißabgleich

FuBK - Testbild einspeisen.

⊙ min., ⊙ nom., ⊙ max. einstellen.

Regler VG (R 751) und VB (R 771) so einstellen, daß keine Verfärbungen sichtbar sind.

RV Regelspannungsverzögerung (Tuner)

Normtestbild auf hohen UHF Kanal legen, die HF sollte mindestens 1,5 mV betragen (rauschfreies Bild). Regler R 341 (Kontakt 15, ZF-Verst.) in Richtung Linksanschlagdrehen bis das Bild zu rauschen beginnt, dann wieder zurückdrehen bis das Bild gerade rauschfrei wird.

ABGLEICH DER BRÜCKENSPULE L 511

Bildbreite auf Minimum, den Tastkopf eines Zweistrahloszilloskopes an den Kollektor des Transistors T 572 (BU 508 A) einhängen. Den anderen Tastkopf zwischen den Dioden D 502 und D 503 anschließen.

Mit der Spule L 511 beide Oszillogramme auf gleiche Impulsbreite abgleichen.

GB

White level adjustment

Display colour bar test pattern.

Set ⊙ to min., ⊙ to nom., ⊙ to max.

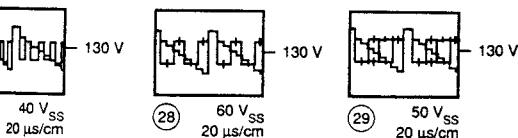
Adjust presets VG (R 751) and VB (R 771) until no colouration shows.

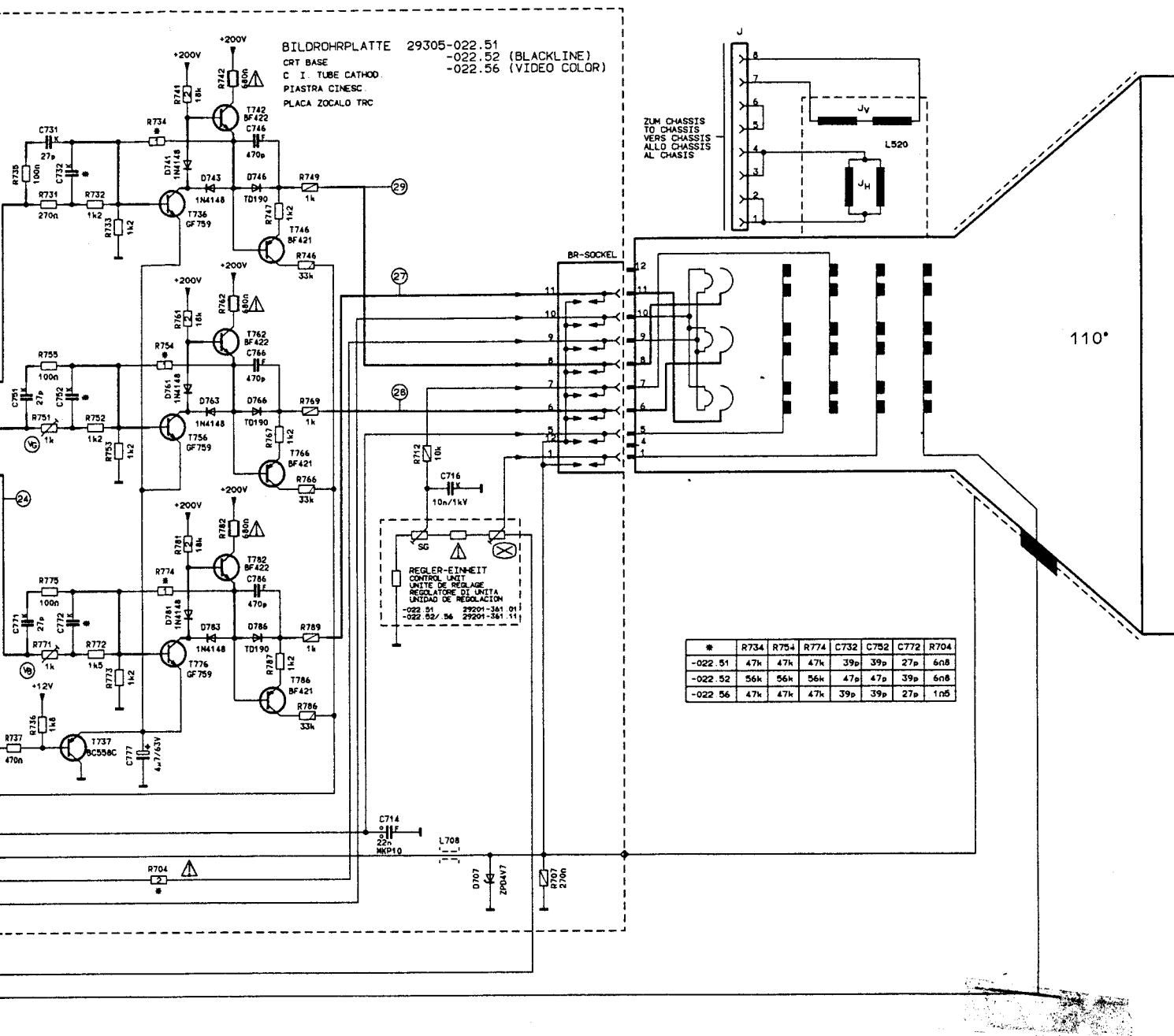
RV Delayed Automatic Gain Control

Feed in a standard test pattern at a high UHF Band. The RF should be at least 1.5 mV. Rotate the control R 341 (contact 15, ZF-Verst.) in the direction of the control until the picture begins to show noise.

ADJUSTMENT OF THE BRIDGE COIL

Picture width to minimum, then connect one test probe of the oscilloscope to the collector of transistor T 572. Connect the other test probe to the junction of diodes D 502 and D 503. Adjust the coil L 511 so that both oscillograms have the same pulse width.





D

Weiabgleich

FuBK - Testbild einspeisen.

⊙ min., ◊ nom., ● max. einstellen.

Regler VG (R 751) und VB (R 771) so einstellen, da keine Verfärbungen sichtbar sind.

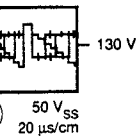
RV Regelspannungsverzögerung (Tuner)

Normtestbild auf hohen UHF Kanal legen, die HF sollte mindestens 1,5 mV betragen (rauschfreies Bild). Regler R 341 (Kontakt 15, ZF-Verst.) in Richtung Linksanschlagdrehen bis das Bild zu rauschen beginnt, dann wieder zurückdrehen bis das Bild gerade rauschfrei wird.

ABGLEICH DER BRÜCKENSPULE L 511

Bildbreite auf Minimum, den Tastkopf eines Zweistrahloszilloskopes an den Kollektor des Transistors T 572 (BU 508 A) einhängen. Den anderen Tastkopf zwischen den Dioden D 502 und D 503 anschließen.

Mit der Spule L 511 beide Oszillogramme auf gleiche Impulsbreite abgleichen.



GB

White level adjustment

Display colour bar test pattern.

Set ⊙ to min., ◊ to nom., ● to min.

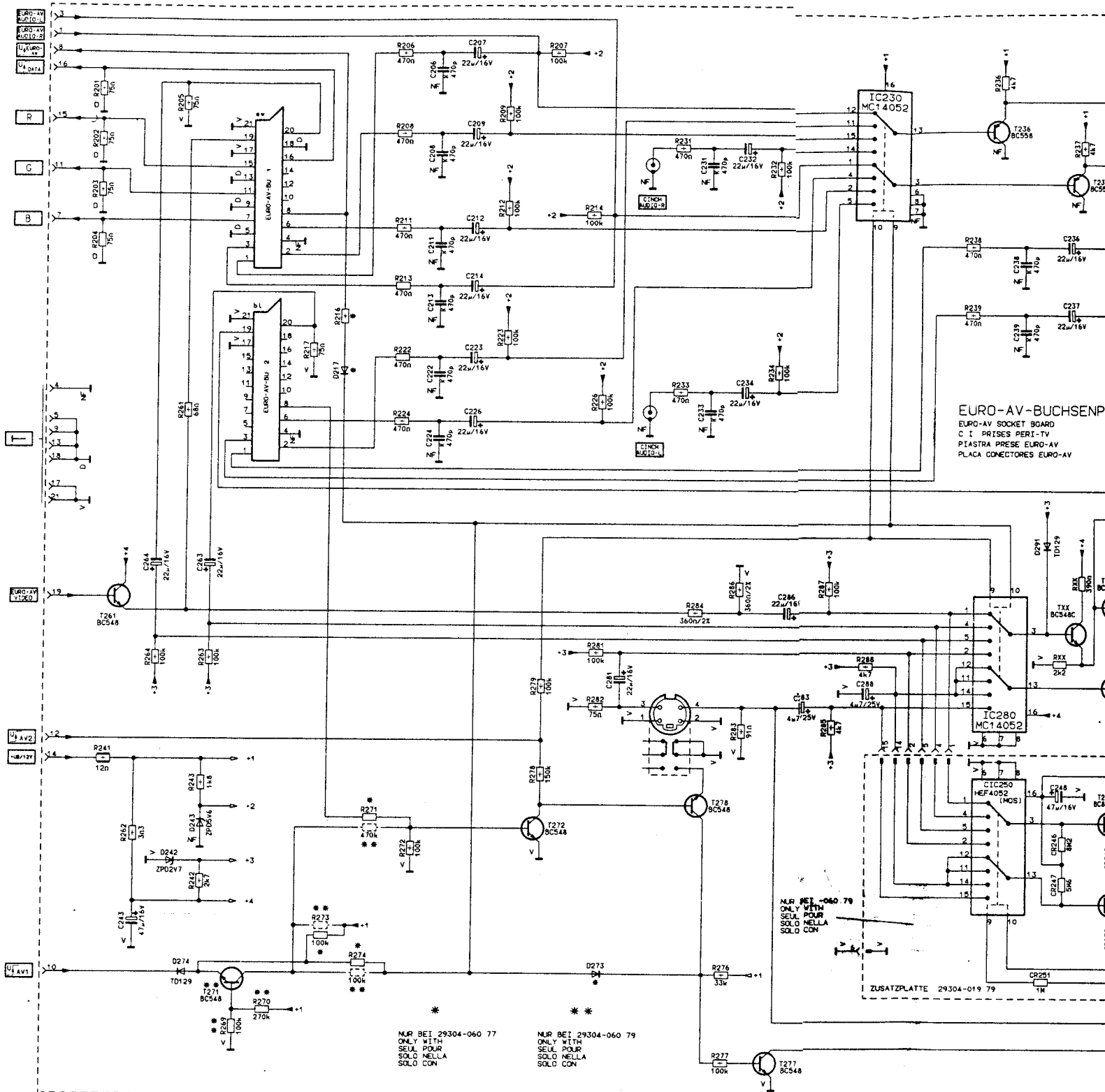
Adjust presets VG (R 751) und VB (R 771) so that the picture does not show any colouration.

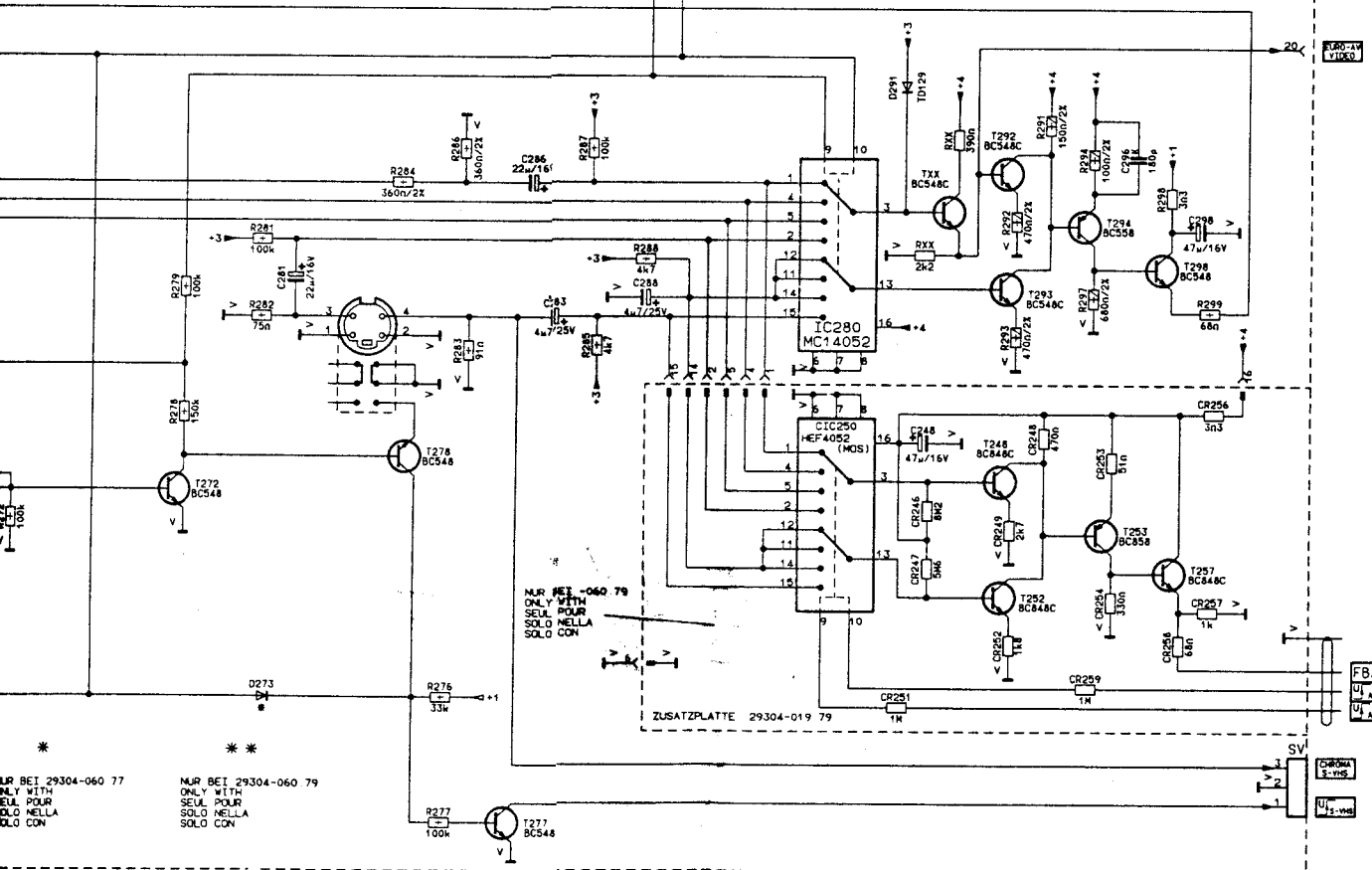
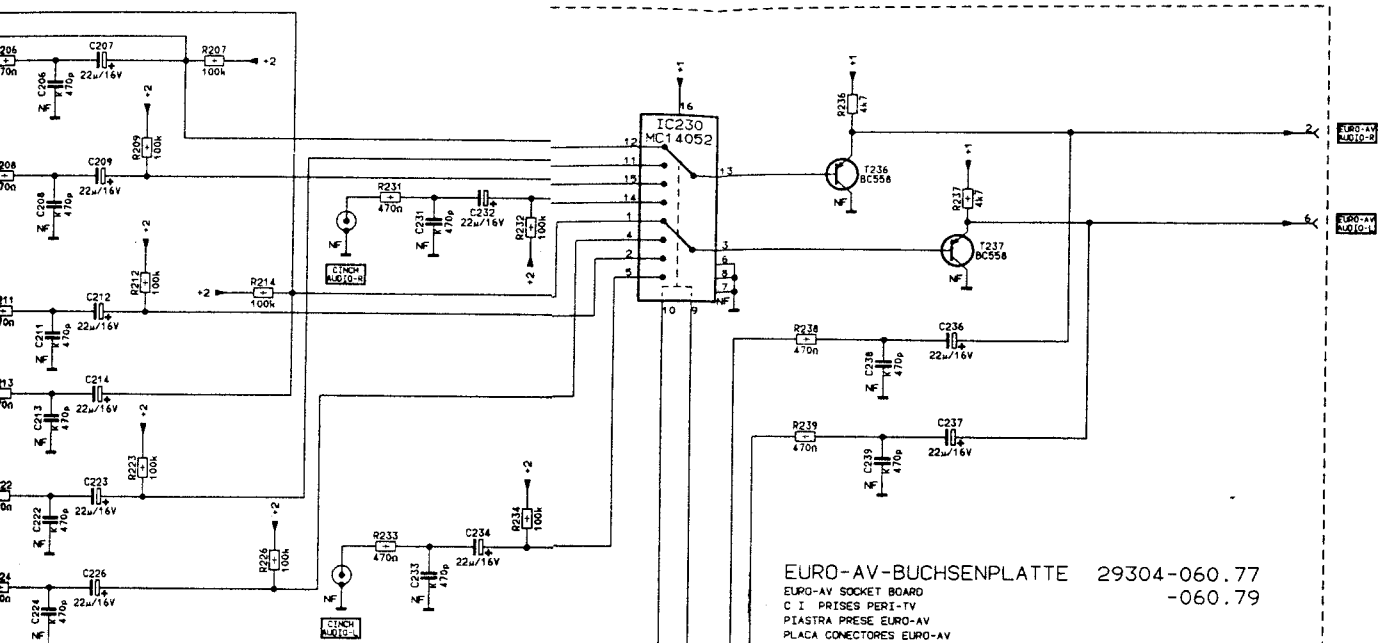
RV Delayed Automatic Gain Control Voltage (Tuner)

Feed in a standard test pattern at a channel in the upper range of the UHF Band. The RF should be at least 1,5 mV (noise free picture). Rotate the control R 341 (contact 15, IF-Ampl.) towards the left hand and stop until noise just begins to appear in the picture, then reverse the direction of the control until the picture just becomes noise free.

ADJUSTMENT OF THE BRIDGE COIL L 511

Picture width to minimum, then connect one test probe of a twin beam oscilloscope to the collector of transistor T 572 (BU 508 A). Connect the other test probe to the junction of D 502, D 503. Adjust the coil L 511 so that both oscillograms have the same pulse width.





ZUR P.I.P.-ZF-VERSTÄRKER
TO P.I.P.-IF AMPLIFIER
VERS P.I.P.-AMPLIFI-FT
AL P.I.P.-AMPLIFICATORE FI
AL P.I.P.-AMPLIFICADOR FI

BILDROHRPLATTE 29305-022.53
 CRT BASE
 CATH. TUBE CATHOD.
 PIASTRA CINESC
 PLACA ZOCALO TRC

-022.54 (BLACKLINE)
 -022.55 (WEITBEREICH)
 -022.57 (WEITB. VIDEO COLOR)

ZUM FARB-ROB
 TO COLOUR ROB
 VERS CHASSIS
 AL MOD. COLORE RGB
 A CROMA/RGB

ZUM CHASSIS
 TO CHASSIS
 VERS CHASSIS
 AL CHASSIS

ZUM ZEILENTRAF
 TO LINE TRANSFORMER
 VERS TRANSFORMER
 AL TRANSFORMER DE LINES

ZUM CHASSIS
 TO CHASSIS
 VERS CHASSIS
 AL TELAIO
 AL CHASSIS

BR-SOCKEL

REGLER-EINHEIT
 CONTROL UNIT
 UNITE DE REGULATION
 REGOLATORE DI UNITA
 UNIDAD DE REGULACION

*	R734	R754	R774	C731	C751	C771	BR1	R704	REGLE
-022.53	47k	47k	47k	22p	22p	22p	↔	5n8	29201
-022.54	56k	56k	56k	27p	27p	27p	↔	6n8	29201
-022.55	47k	47k	47k	22p	22p	22p	↔	6n8	29201
-022.57	47k	47k	47k	22p	22p	22p	↔	1n5	29201

D

Weißabgleich

FuBK - Testbild einspeisen.

⊙ min., ⊙ nom., ⊙ max. einstellen.

Regler VG (R 755) und VB (R 775) so einstellen, daß keine Verfärbungen sichtbar sind.

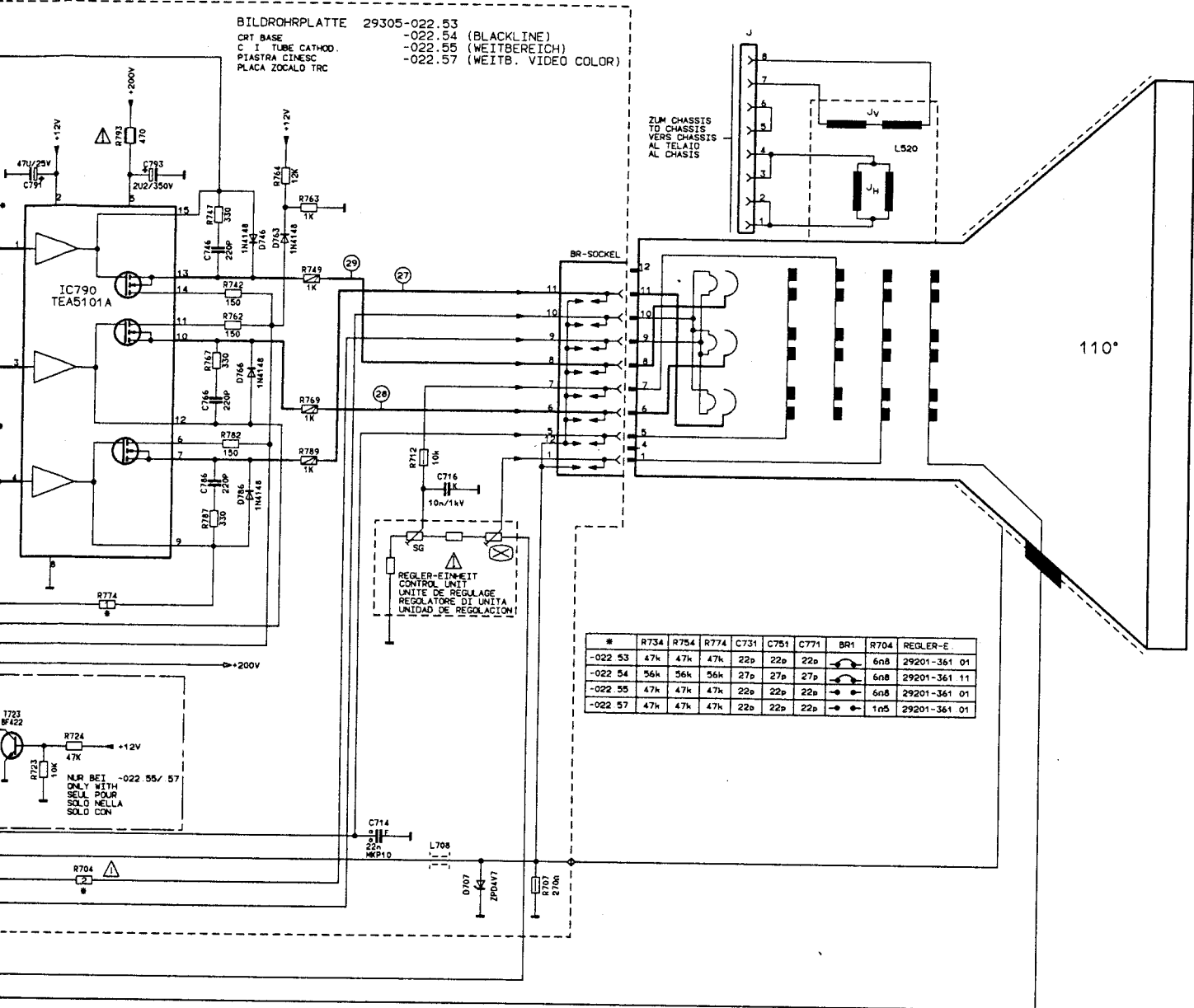
GB

White level adjustment

Display colour bar test pattern.

Set ⊙ to min., ⊙ to nom., ⊙ to max.

Adjust presets VG (R 755) und VB (R 775) so that no colouration is visible.



D

Weißabgleich

FuBK - Testbild einspeisen.

⊙ min., ⊙ nom., ⊙ max. einstellen.

Regler VG (R 755) und VB (R 775) so einstellen, daß keine Verfärbungen sichtbar sind.

GB

White level adjustment

Display colour bar test pattern.

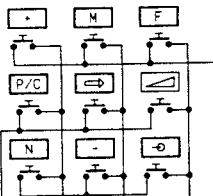
Set ⊙ to min., ⊙ to nom., ⊙ to min.

Adjust presets VG (R 755) und VB (R 775) so that the picture does not show any colouration.

CONTROL UNIT
UNITE DE COMANDE
UNITA DI COMANDO
UNIDAD DE MANDO

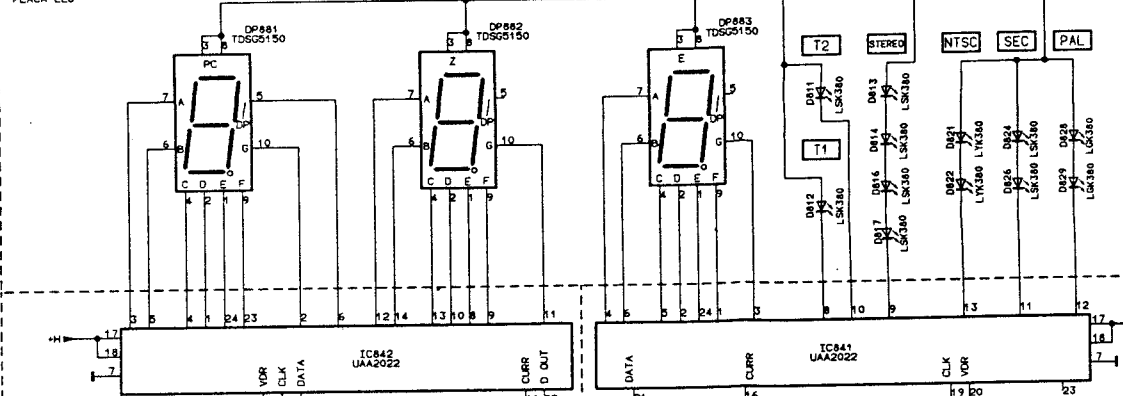
KEYBOARD 29501-706.22

KEYBOARD
CLAVIER
KEYBOARD
TECLADO



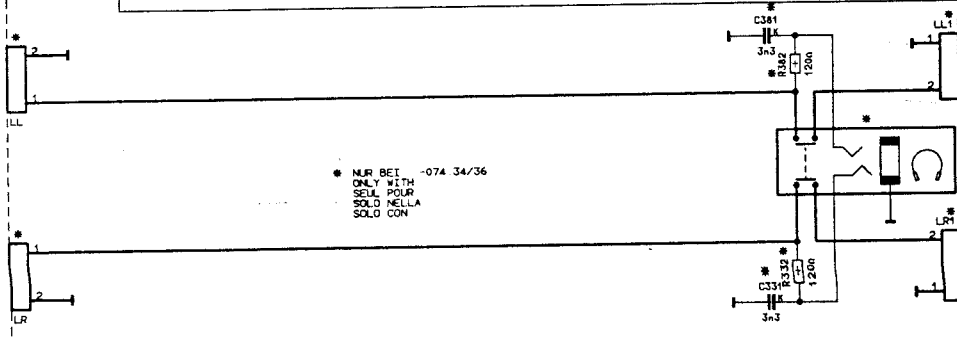
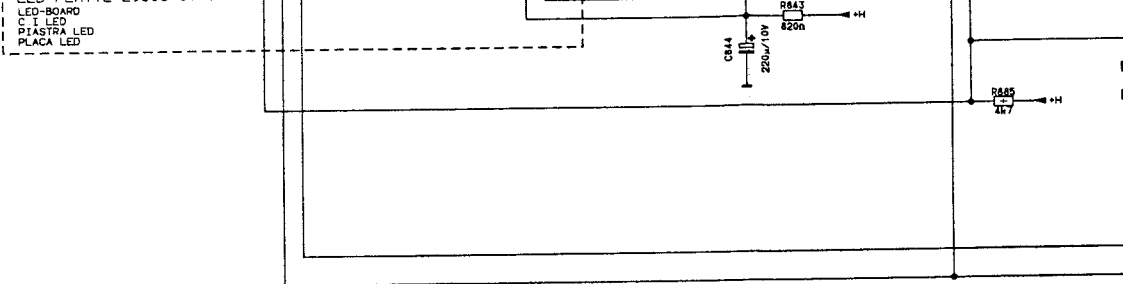
LED-PLATTE 29503-017.34

LED-BOARD
C I LED
PIASTRA LED
PLACA LED

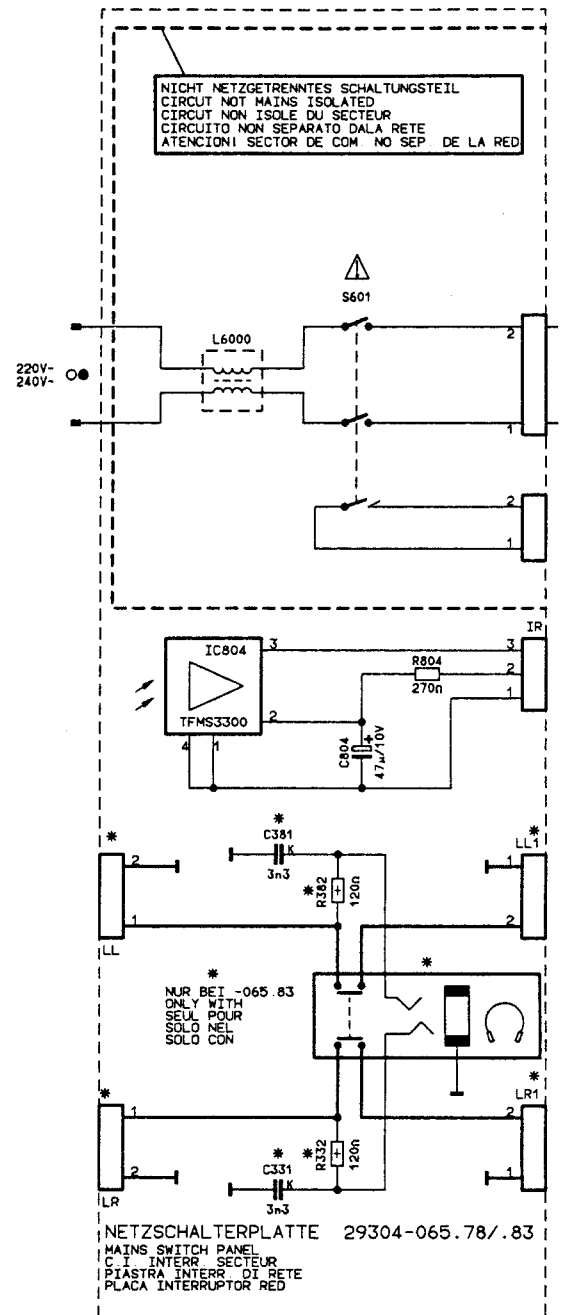


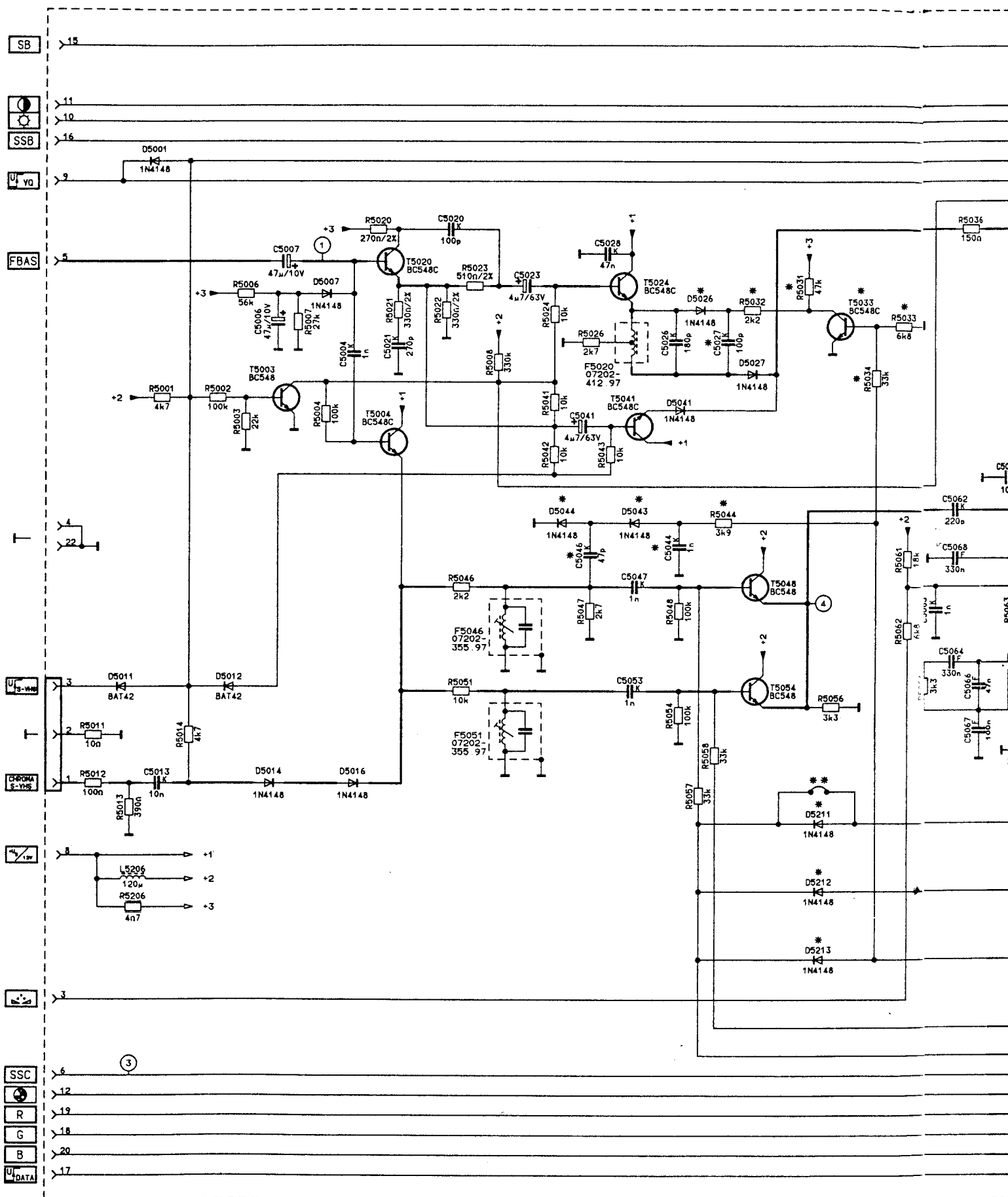
LED-PLATTE 29503-017.44

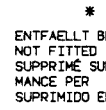
LED-BOARD
C I LED
PIASTRA LED
PLACA LED

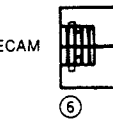
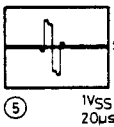
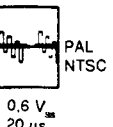
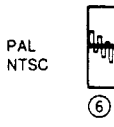
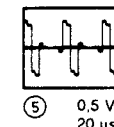
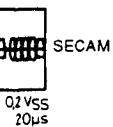
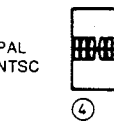
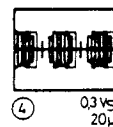
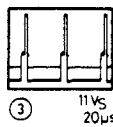
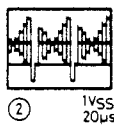
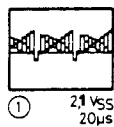
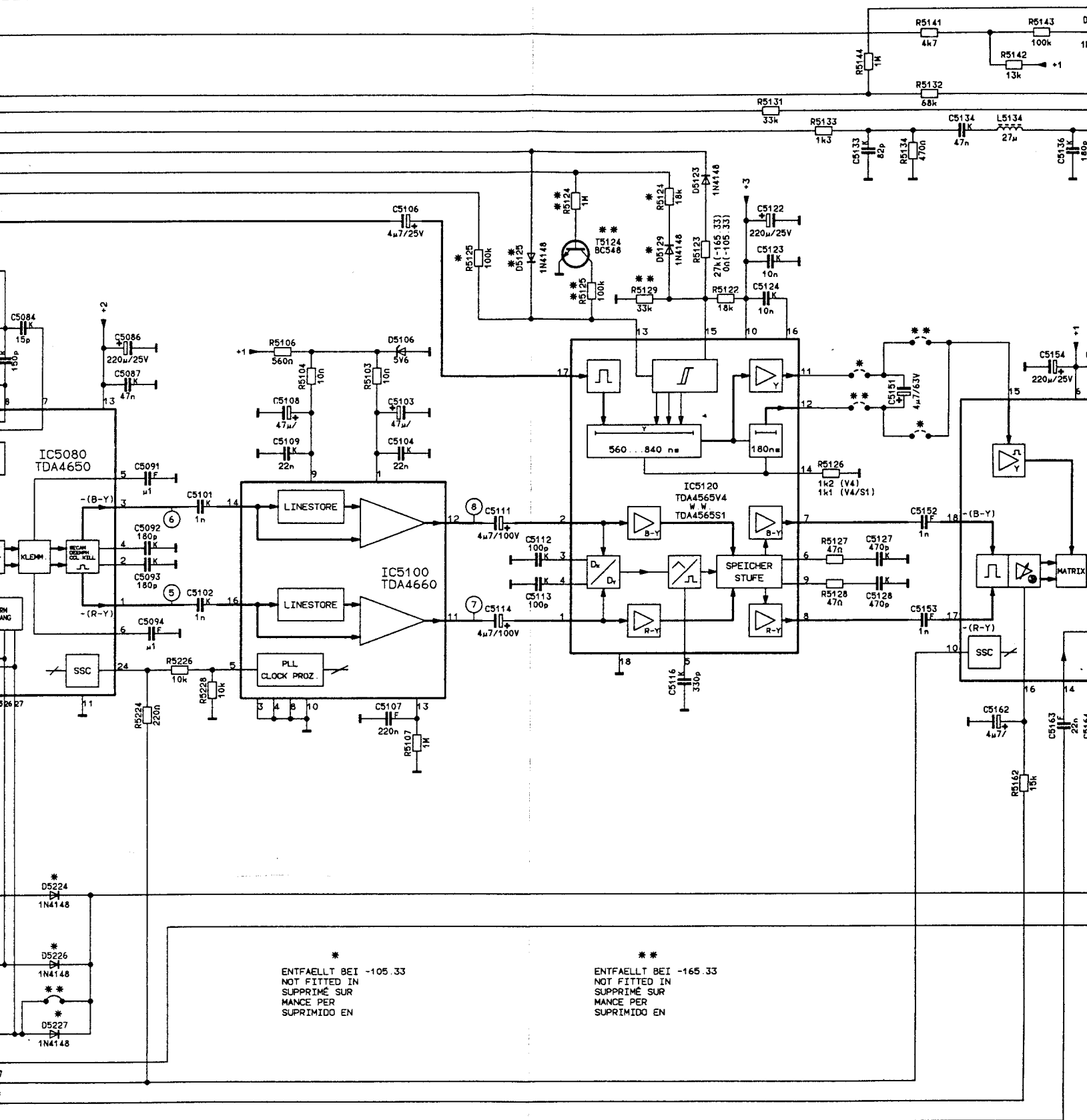


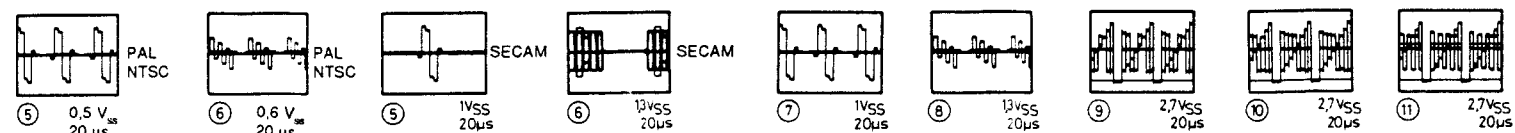
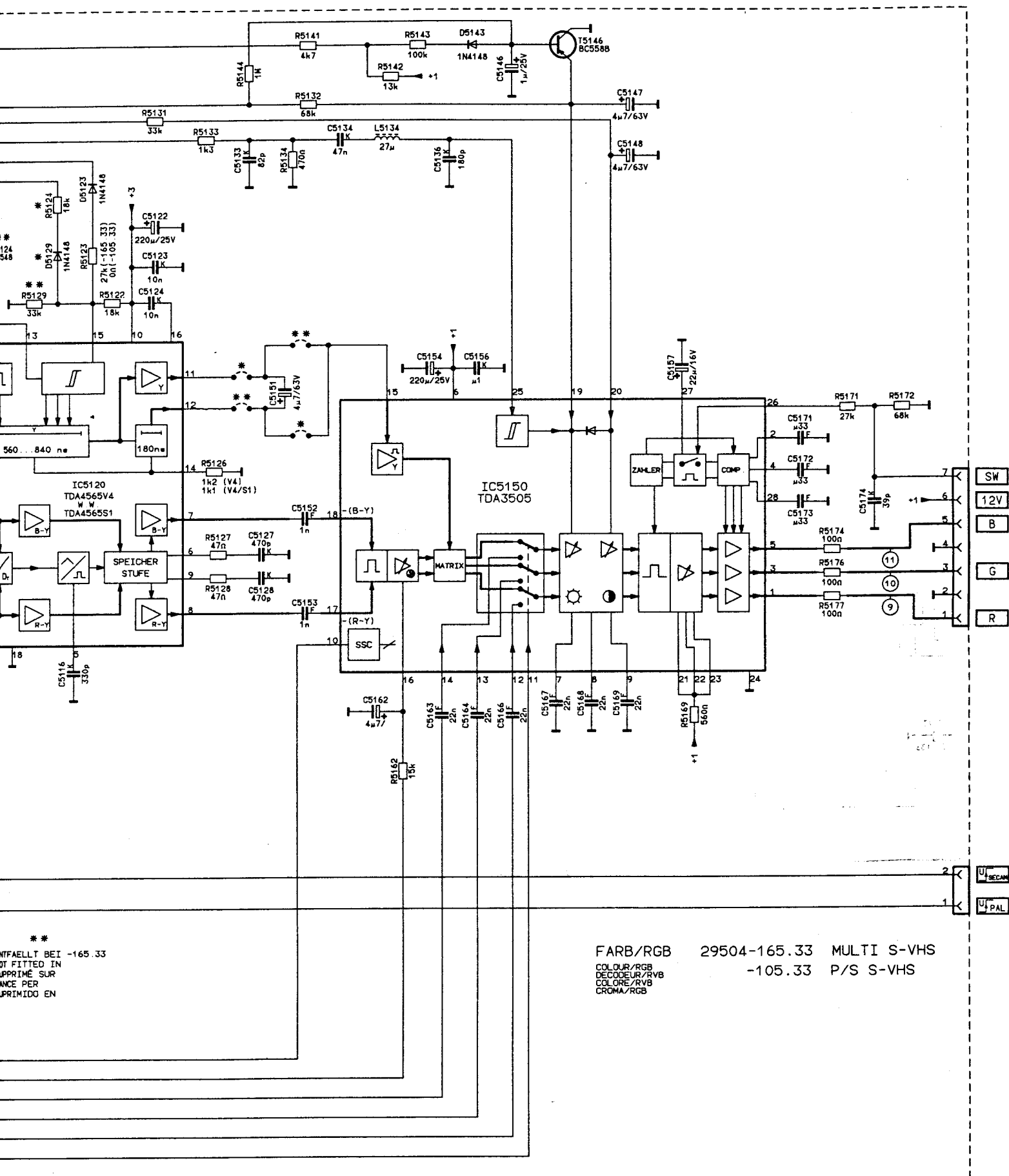
• NUR BEI -074.34/36
ONLY WITH
SEUL POLAR
SOLO NELLA
SOLO CON

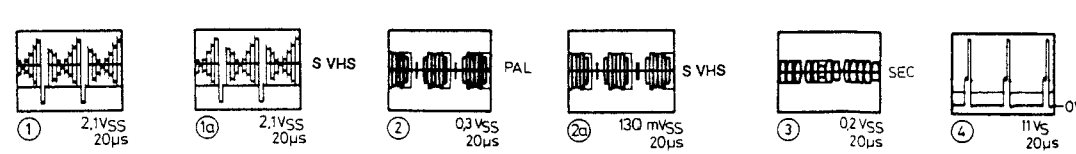
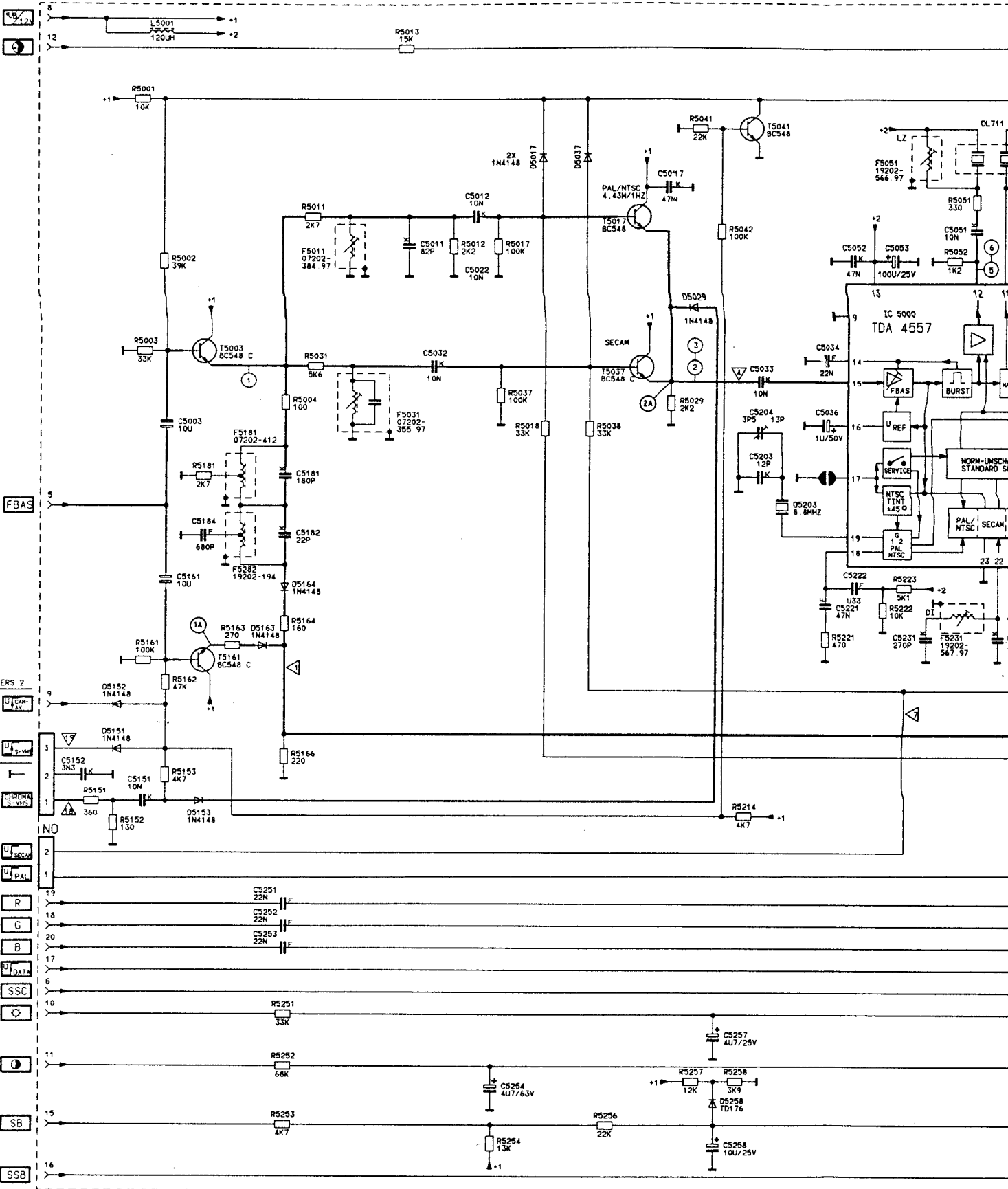


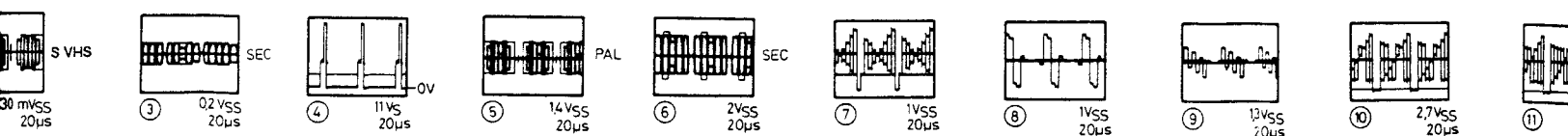




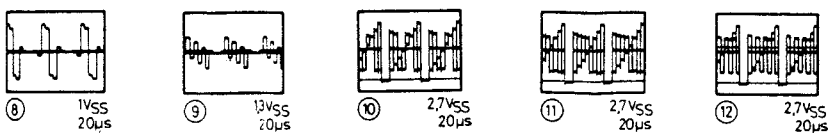
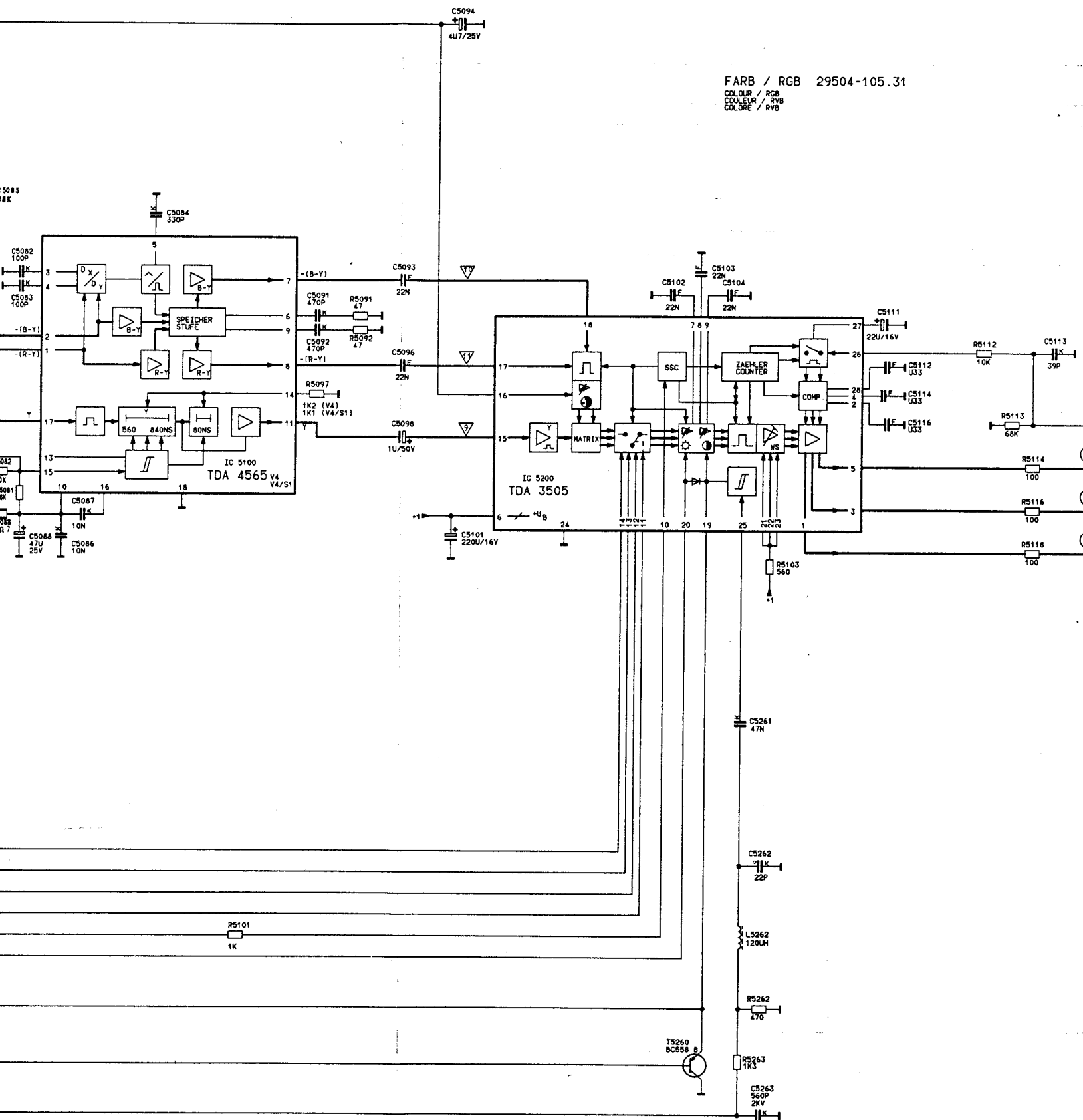


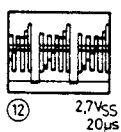
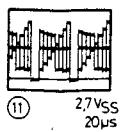
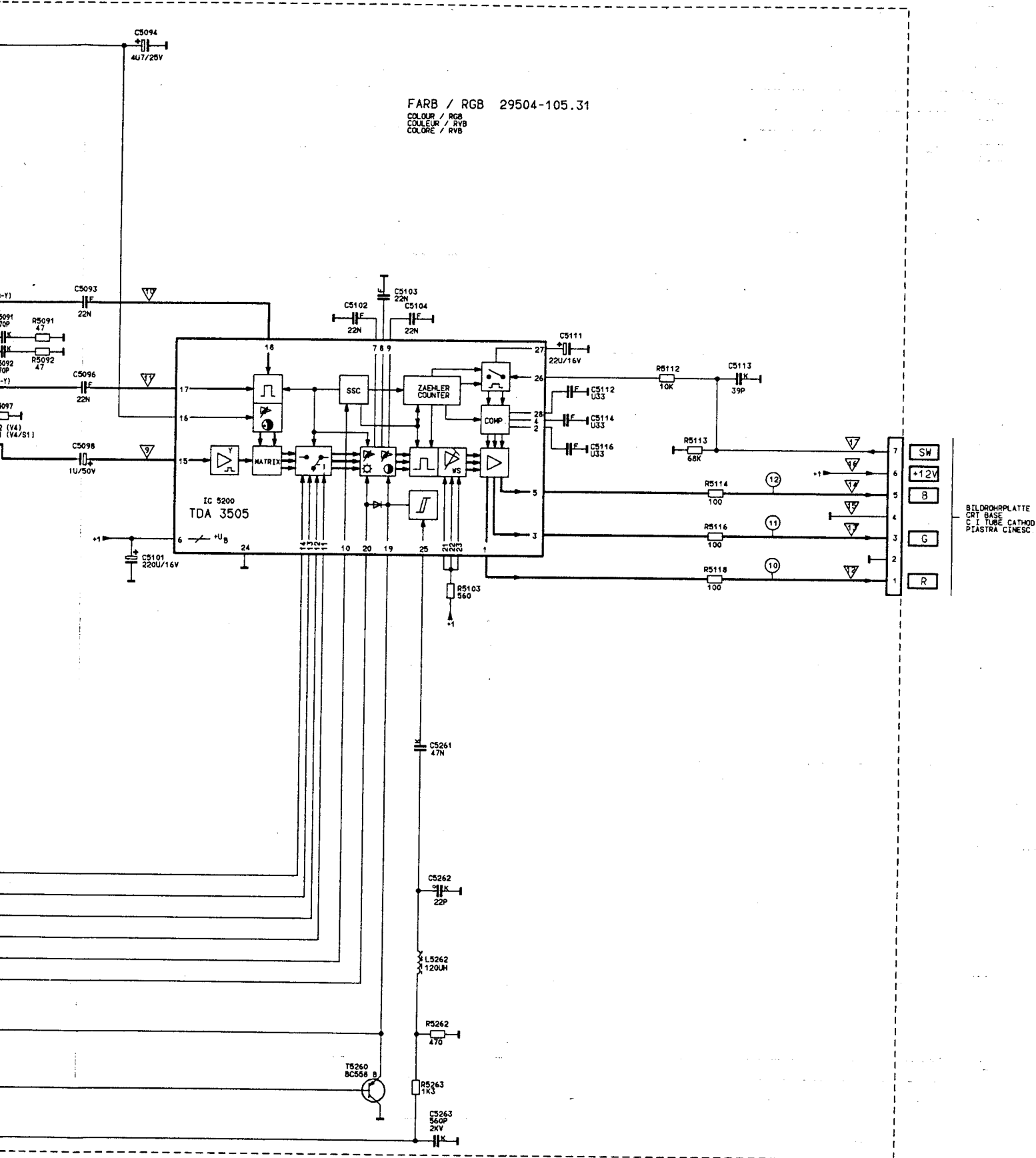


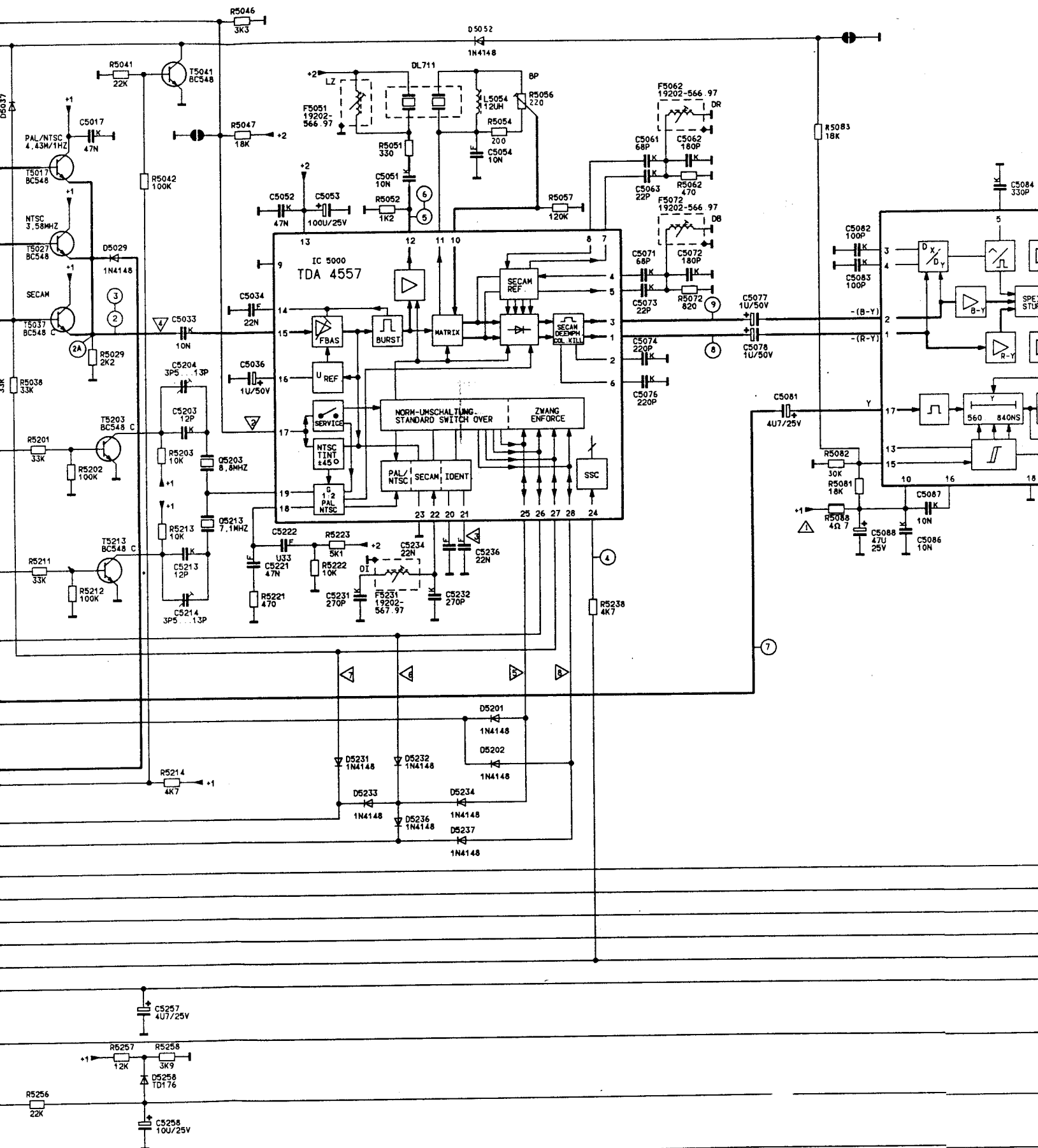




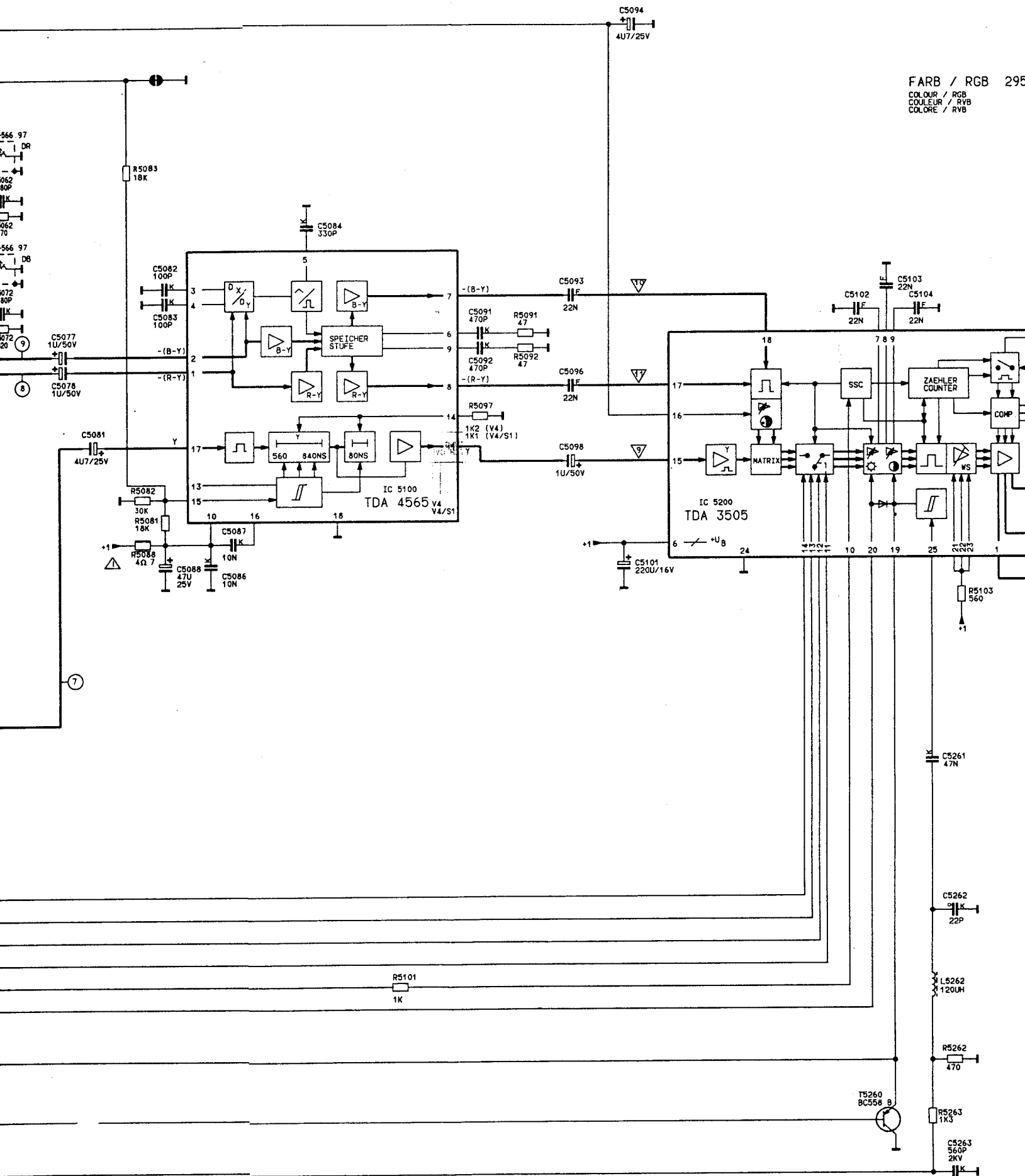
FARB / RGB 29504-105.31
 COLOUR / RGB
 COULEUR / RVB
 COLORE / RYB





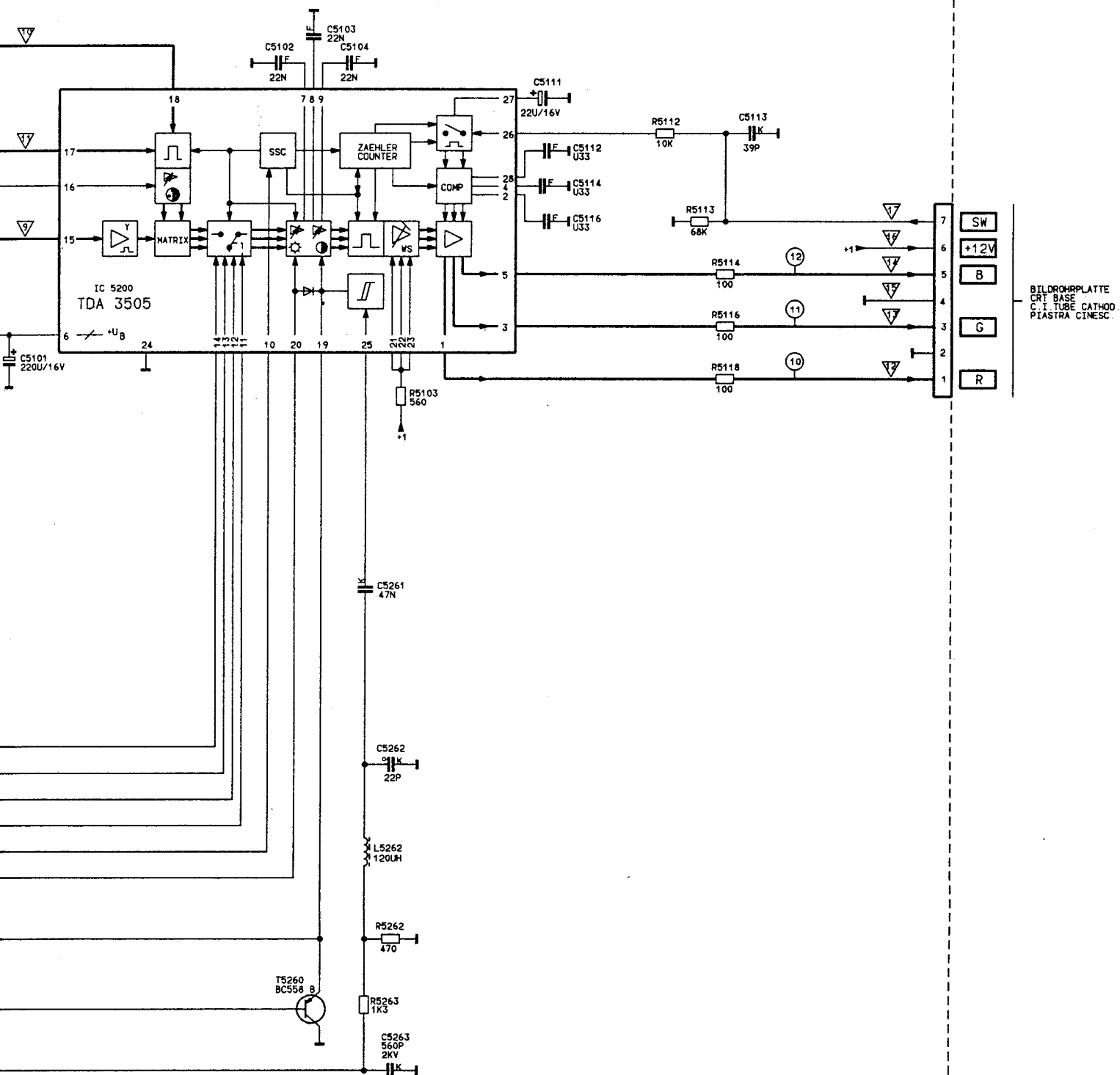


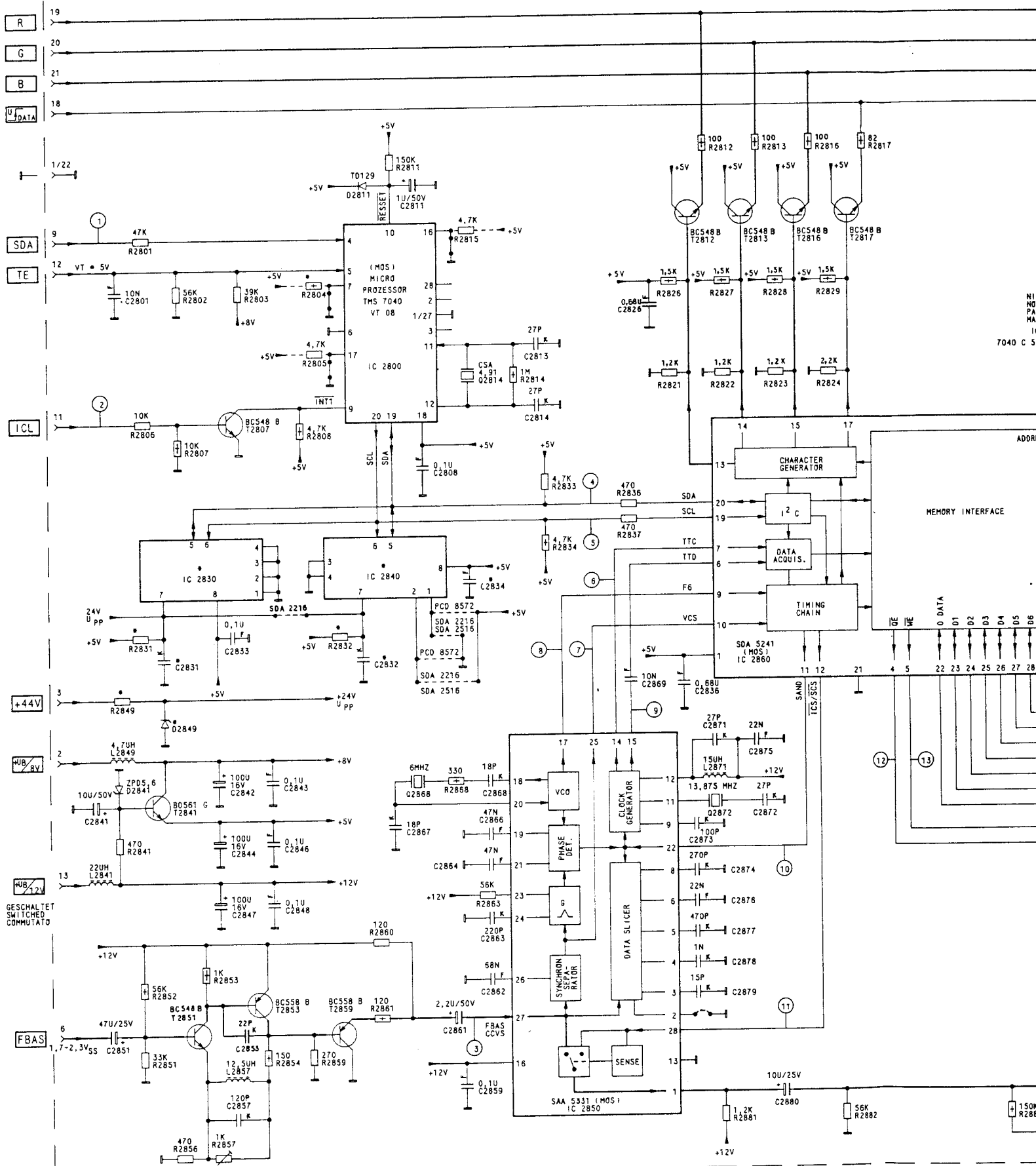
FARB / RGB 295
COLOUR / RGB
COULEUR / RVB
COLORE / RVB



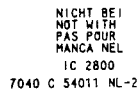
C5094
4U7/25V

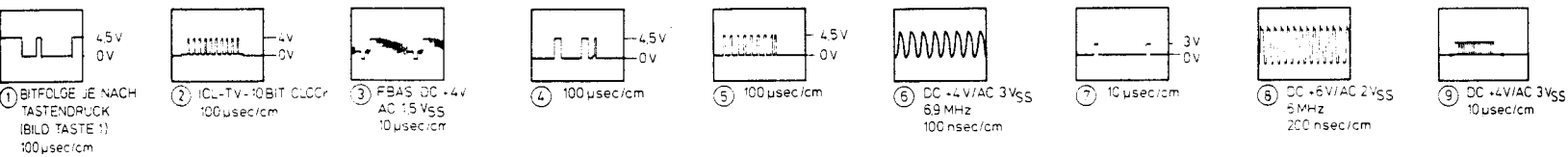
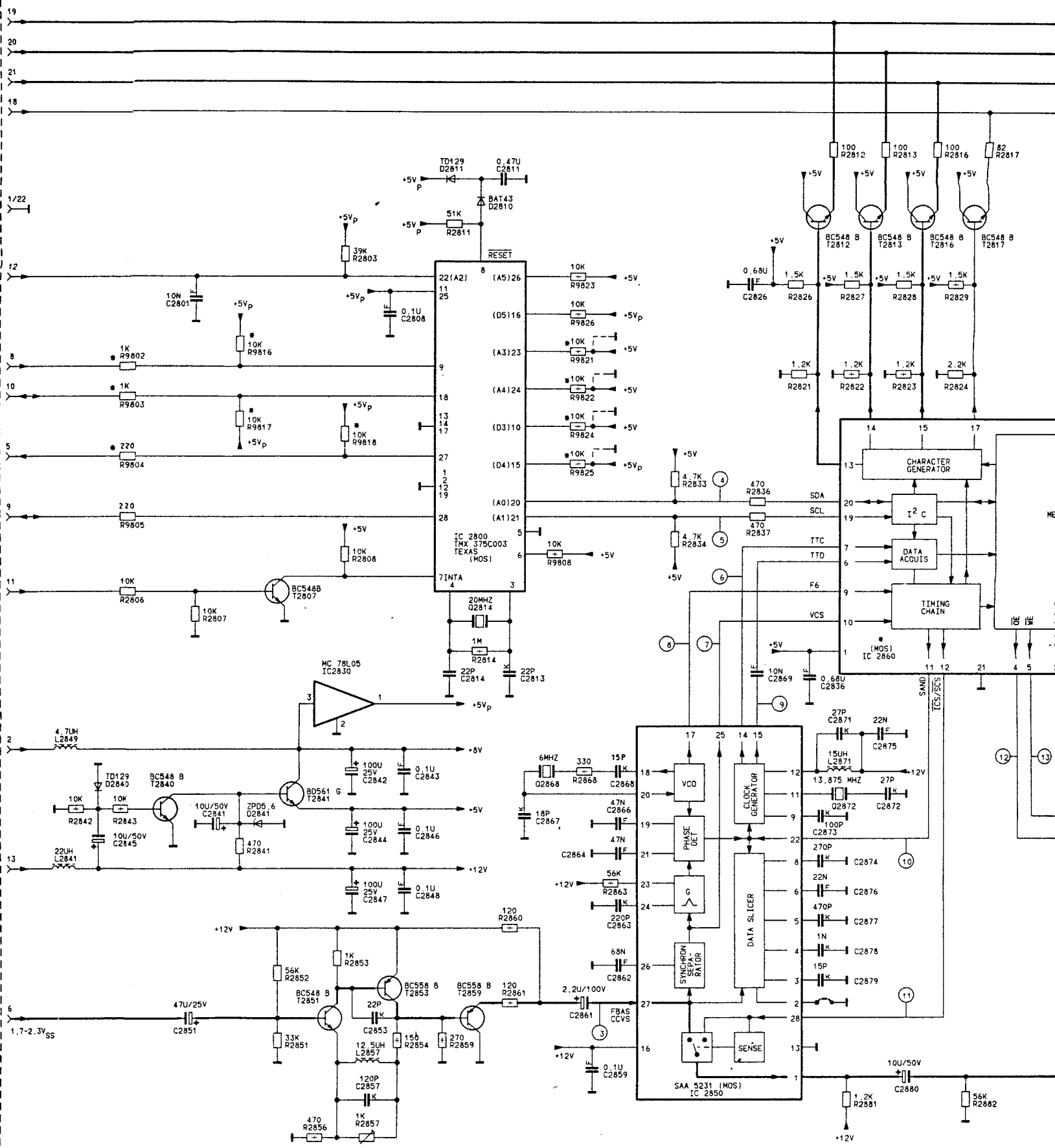
FARB / RGB 29504-165.31
COLOUR / RGB
COULEUR / RVB
COLORE / RVB

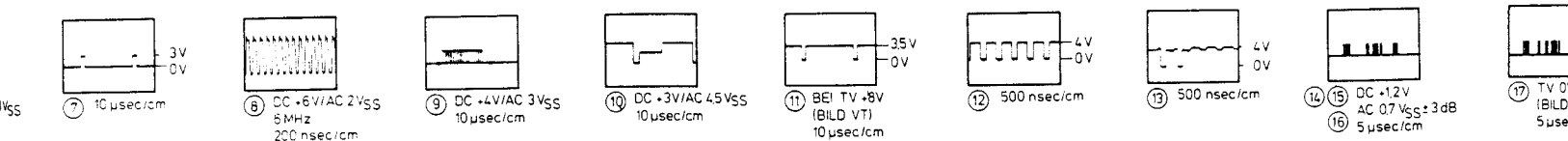
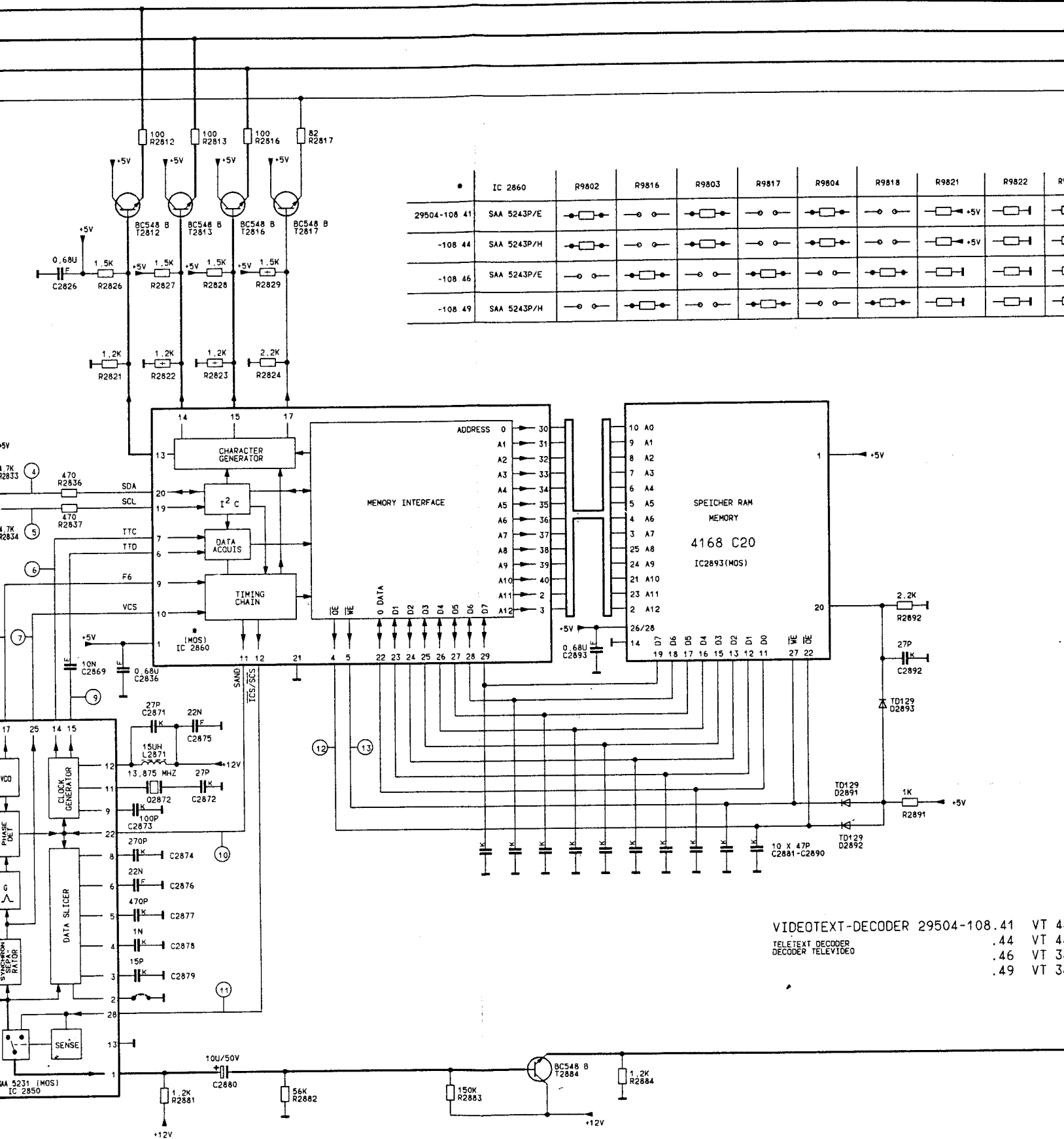


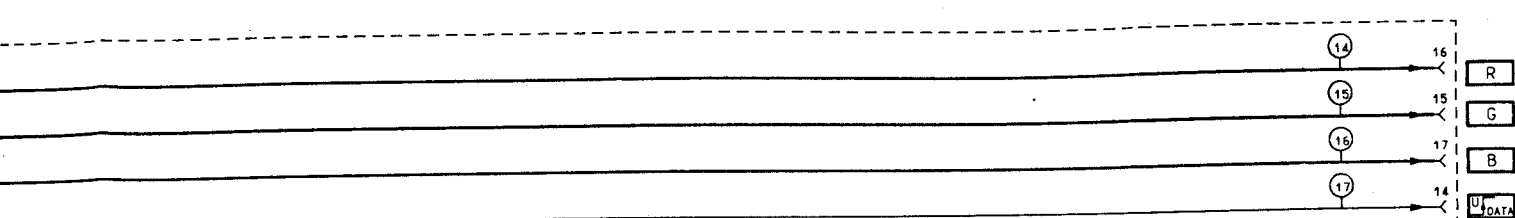


TELETEXT DECODER
DECODER TELEVIDEO

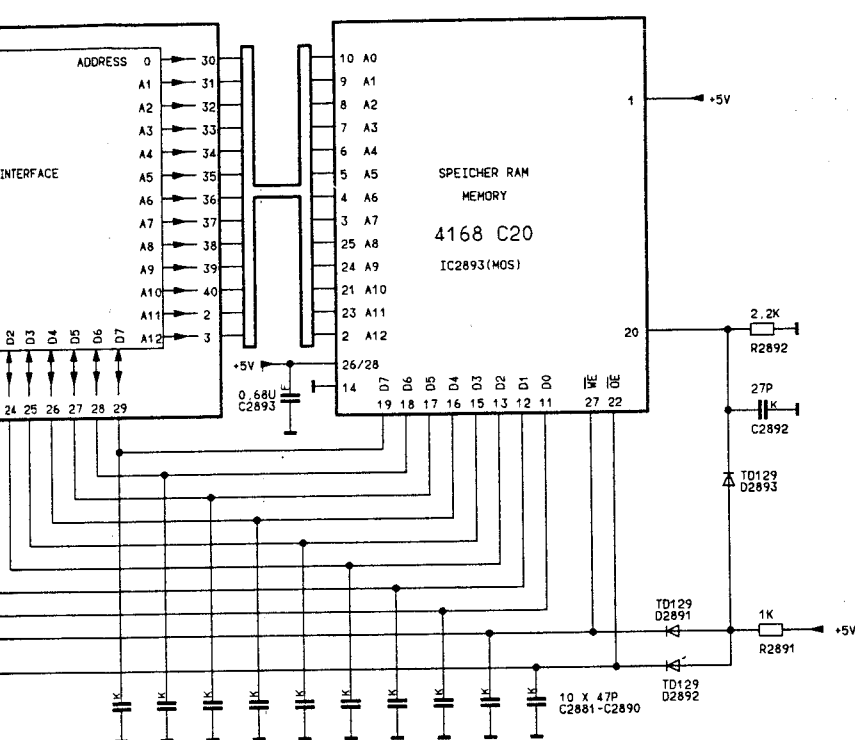
[illegible]



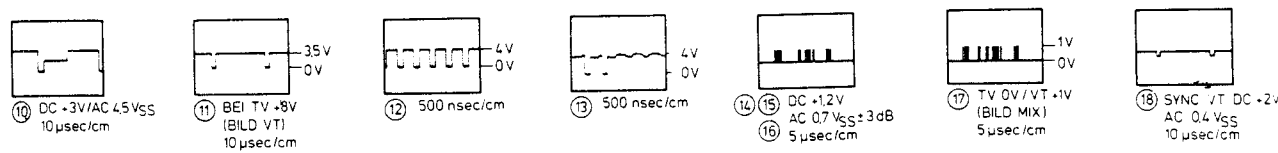
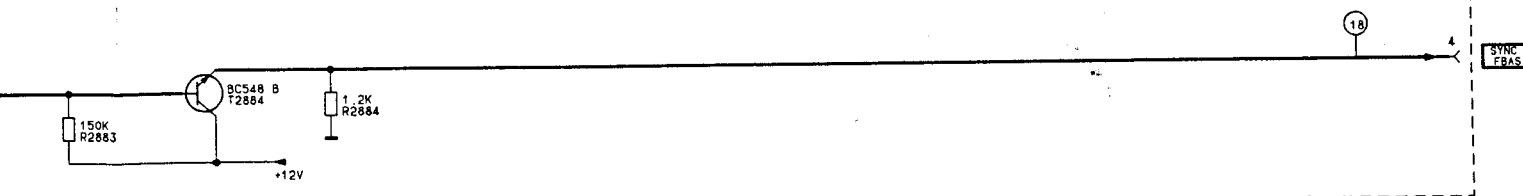




	IC 2860	R9802	R9816	R9803	R9817	R9804	R9818	R9821	R9822	R9824	R9825
29504-108 41	SAA 5243P/E										
-108 44	SAA 5243P/H										
-108 46	SAA 5243P/E										
-108 49	SAA 5243P/H										



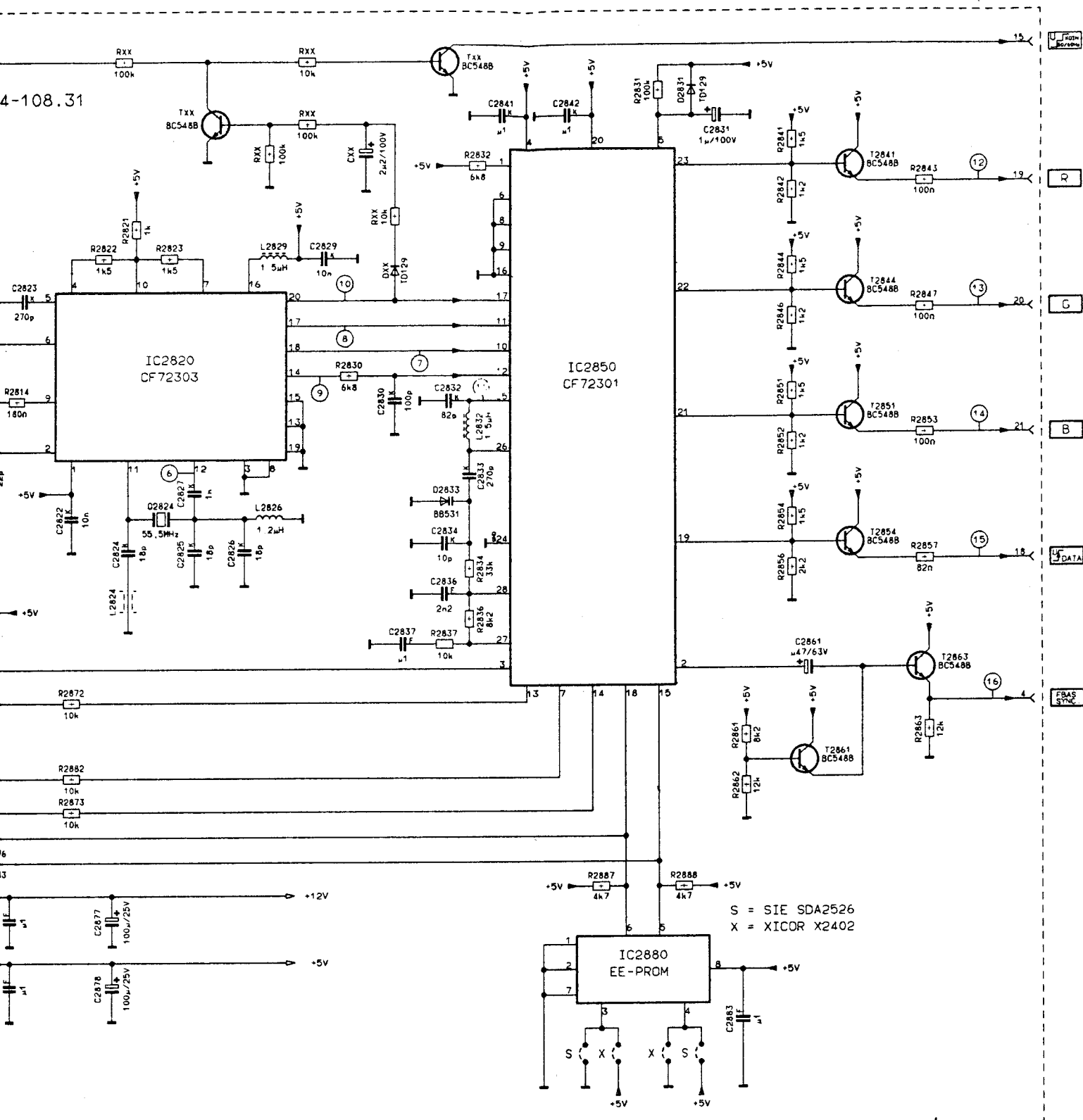
VIDEOTEXT-DECODER 29504-108.41 VT 4416
 TELETXT DECODER .44 VT 4416 CS
 DECODER TELEVIDEO .46 VT 3416
 .49 VT 3416 CS





GB

VT GB: Teletext installation and match
When fitting the Teletext plug-in board, The control R 2804 is set in the fully anti approx. 2dB). If, with a perfect aerial signal disappear. Do not turn R 2804 up any Page 199 must always be selected a new making it possible to evaluate the error



GB

VT GB: Teletext Installation and matching adjustment.

When fitting the Teletext plug-in board, the shorting plug on contact 4 and 6 has to be removed.

The control R 2804 is set in the fully anti clockwise position when the unit is delivered (smallest treble boost: approx. 2dB). If, with a perfect aerial signal character faults occur, turn R 2804 slowly clockwise until the faults disappear. Do not turn R 2804 up any further as error rate may increase again.

Page 199 must always be selected a new during the adjustment, as only this effects a new read-in of the page making it possible to evaluate the error level.

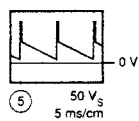
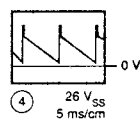
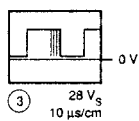
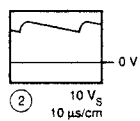
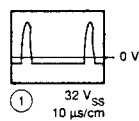
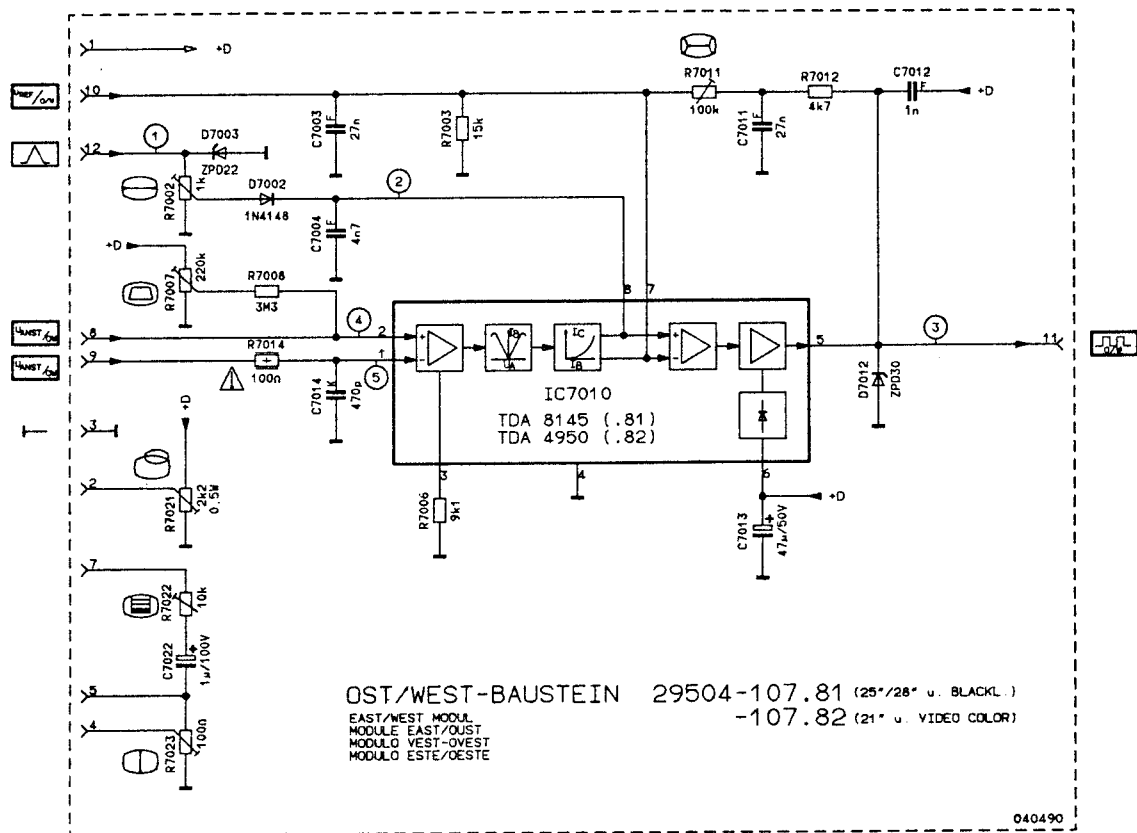
chen Kontakt 4 und 6 entfernt werden.

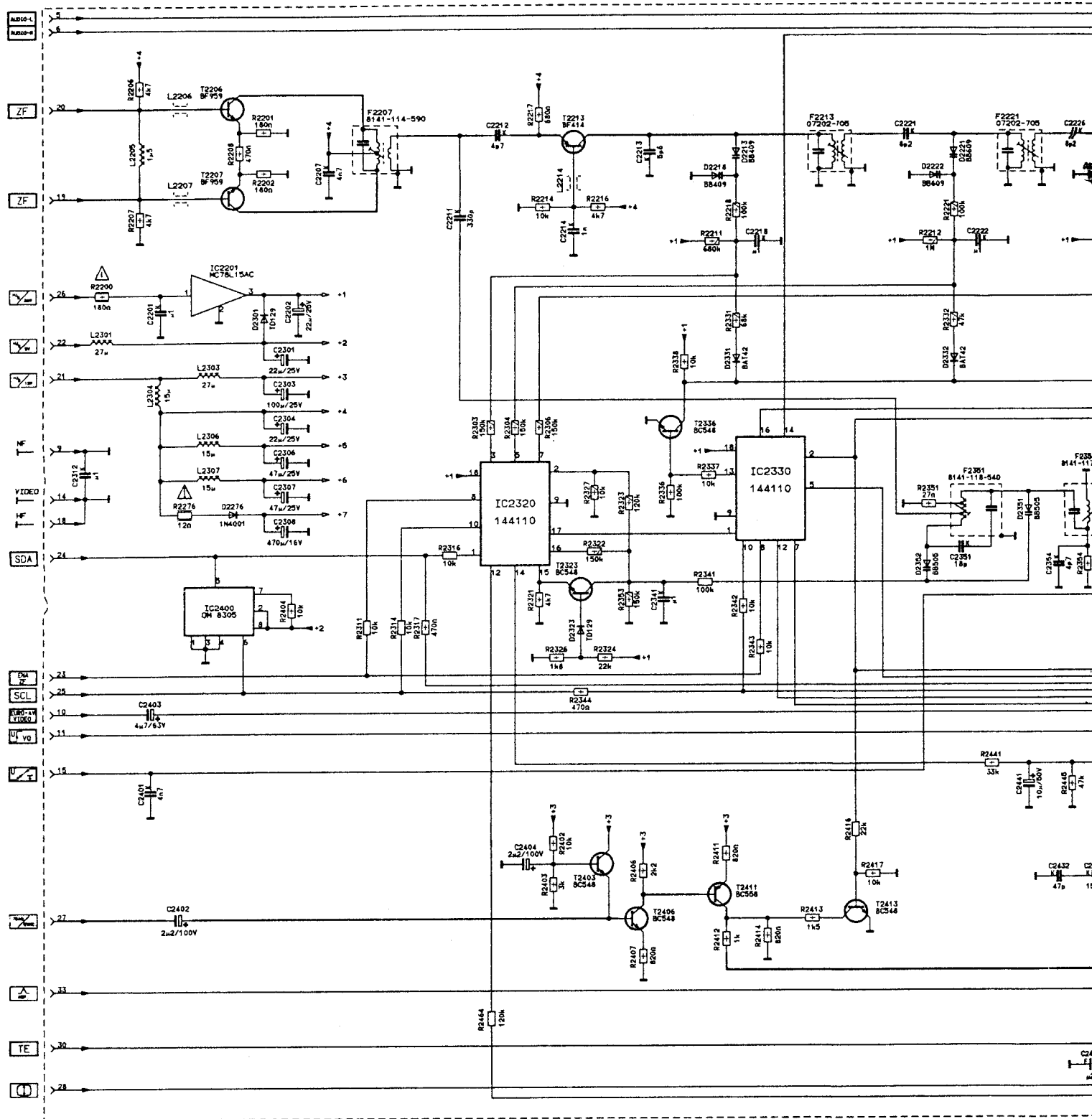
(kleinste Höhenanhebung, ca 2 dB).

langsam nach rechts verschieben, bis

er zunehmen kann.

anzuwählen, da nur so die Seite neu





D

Zeilen-Abgleich-Mode der Multi ZF

"Plus" Taste am Key-Board gedrückt halten und mit dem Netzschalter das Gerät einschalten. Im Display erscheint nach ca 2 sec. ein Punkt. Am Geber ist danach die Taste "0" zu drücken, dadurch wird die Zeilenfrequenz und -phase voreingestellt. Wenn keine Koizidenz (kein Sendersignal) "Kanalsuchlauf Plus" Taste drücken.

Die nachfolgenden Funktionen sind über den Geber auszuführen:

- Taste
- 2: Vorwahl Zeilenfrequenzabgleich
- 1: Vorwahl Phasenabgleich
- P+ / P-: Abgleich +/-
- P/C: Doppelfunktion der Taste, Norm 0 = Inland
Norm 1 = Frankreich

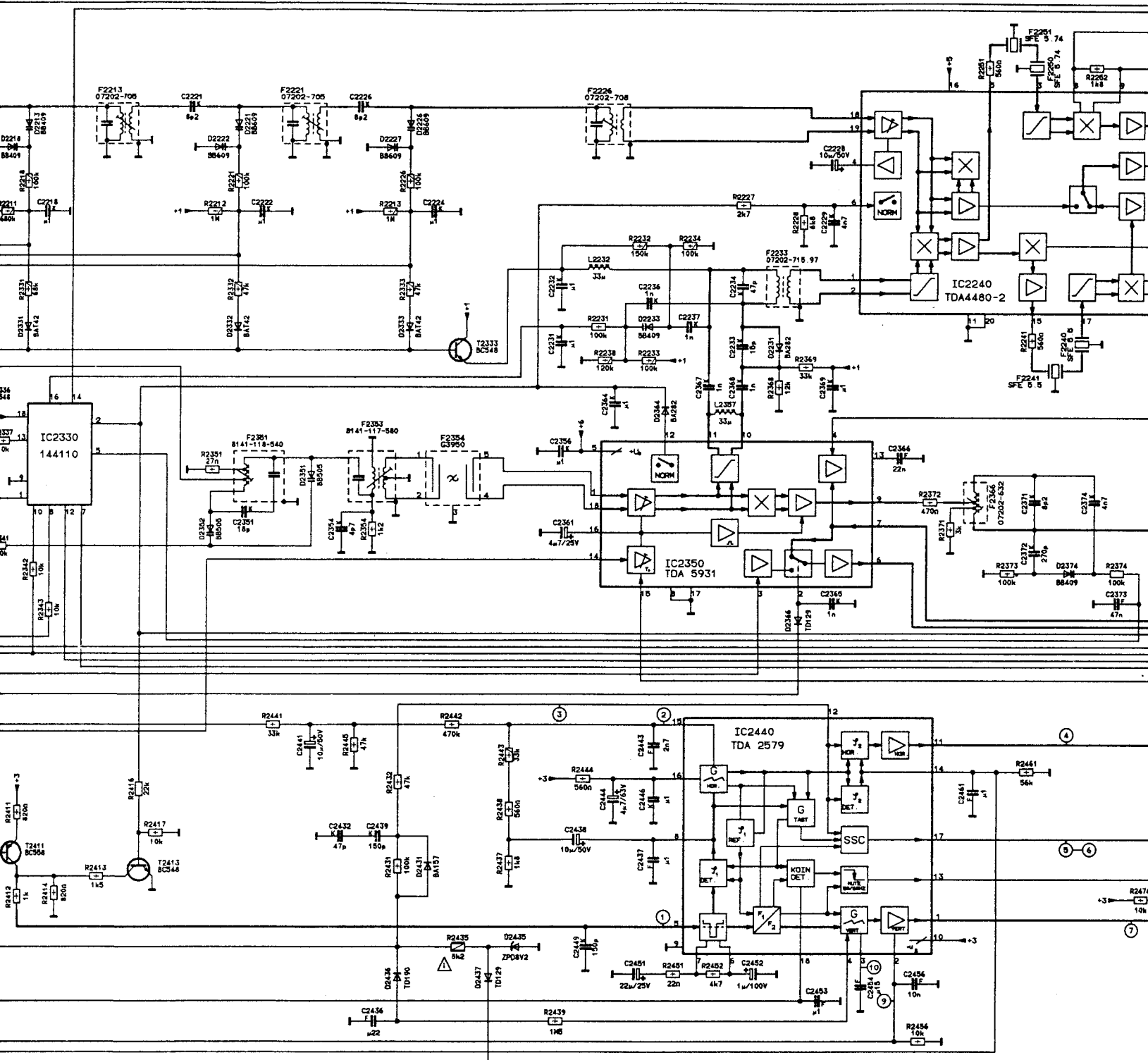
Stand-By: Abbruch ohne speichern
Speichern: VT - Speicher - Taste bewirkt die Speichervorwahl. Das Zeitfenster wird durch Blinken des Punktes angezeigt.
OK: Während des Zeitfensters Abgleichwerte speichern, sonst werden die Analogwerte aus dem NVM gelesen.

Analogfunktionen wie im Programm-Mode.

GB

Line A

Depress
receiver
shown a
that the
When t
"Channel



(GB)

onen sind über den Geber auszuführen:

nfrequenzabgleich
enabgleich

n der Taste, Norm 0 = Inland
Norm 1 = Frankreich

speichern

- Taste bewirkt die Speichervorwahl. Das
rd durch Blinken des Punktes angezeigt.
Zeitenfsters Abgleichwerte speichern,
die Analogwerte aus dem NVM gelesen.

Programm-Mode.

Line Adjustment - Mode for the Multi IF

Depress and hold the "Plus" button on the keyboard and switch the receiver on with the mains switch. In the display a decimal point is shown after approx. 2 sec. Depress the "0" button on the transmitter so that the line frequency and -phase is preadjusted. When there is no coincidence (no transmitted signal) depress the "Channel Search Plus" button.

The following functions can be called up from transmitter:

Button

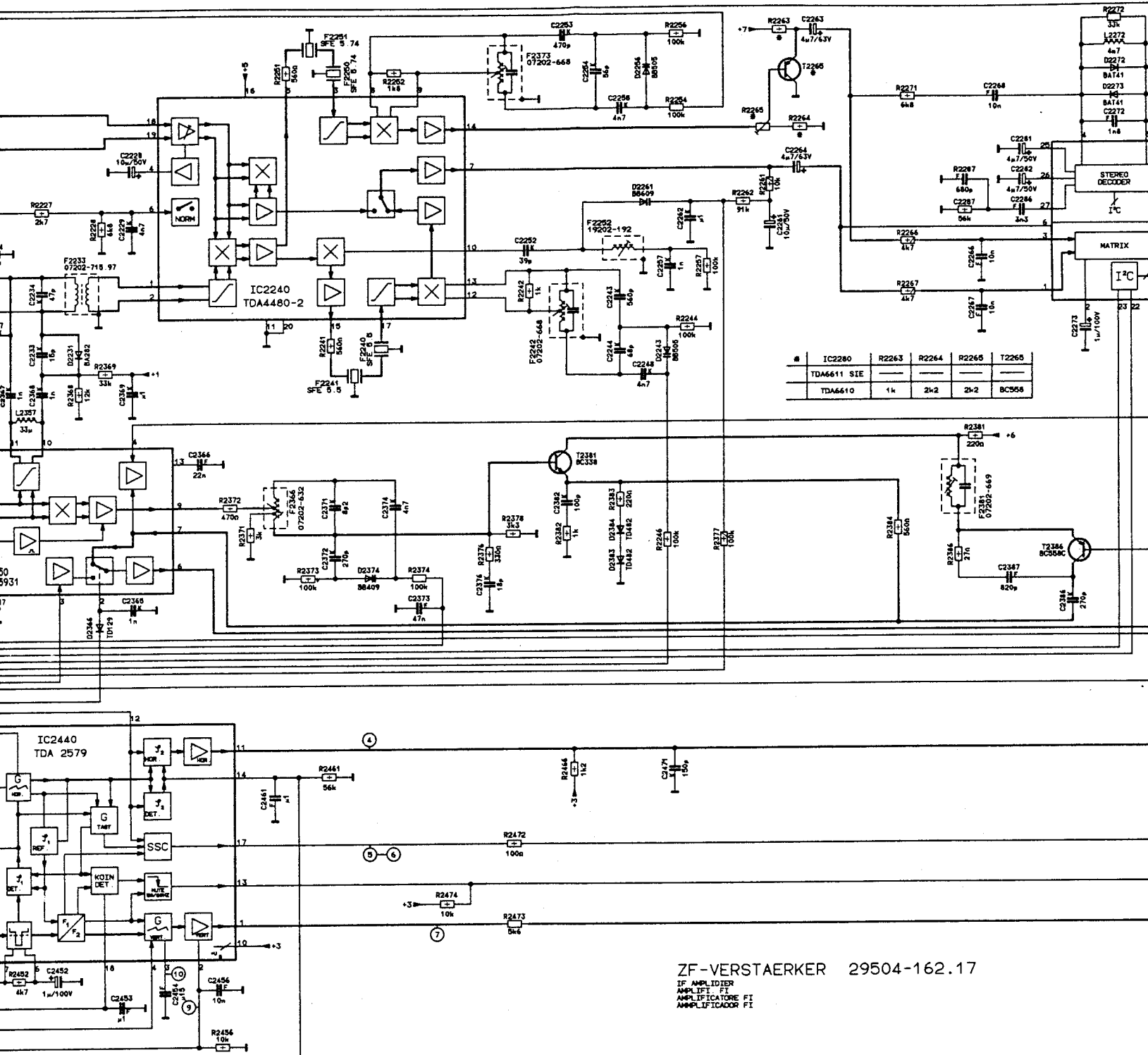
2: Selection of Line Frequency Adjustment
1: Selection of Phase Adjustment

P+ / C-: Adjustment +/-
P/C: Button with double function, standard

Standby: Break without memorising
Memory: The VT memory button operates as a
tor. The time window is indicated by the flashing
cimal point.

OK: During the period of the time window,
alignment value otherwise the analog
read out of the NVM.

Analogue functions as for programme mode.

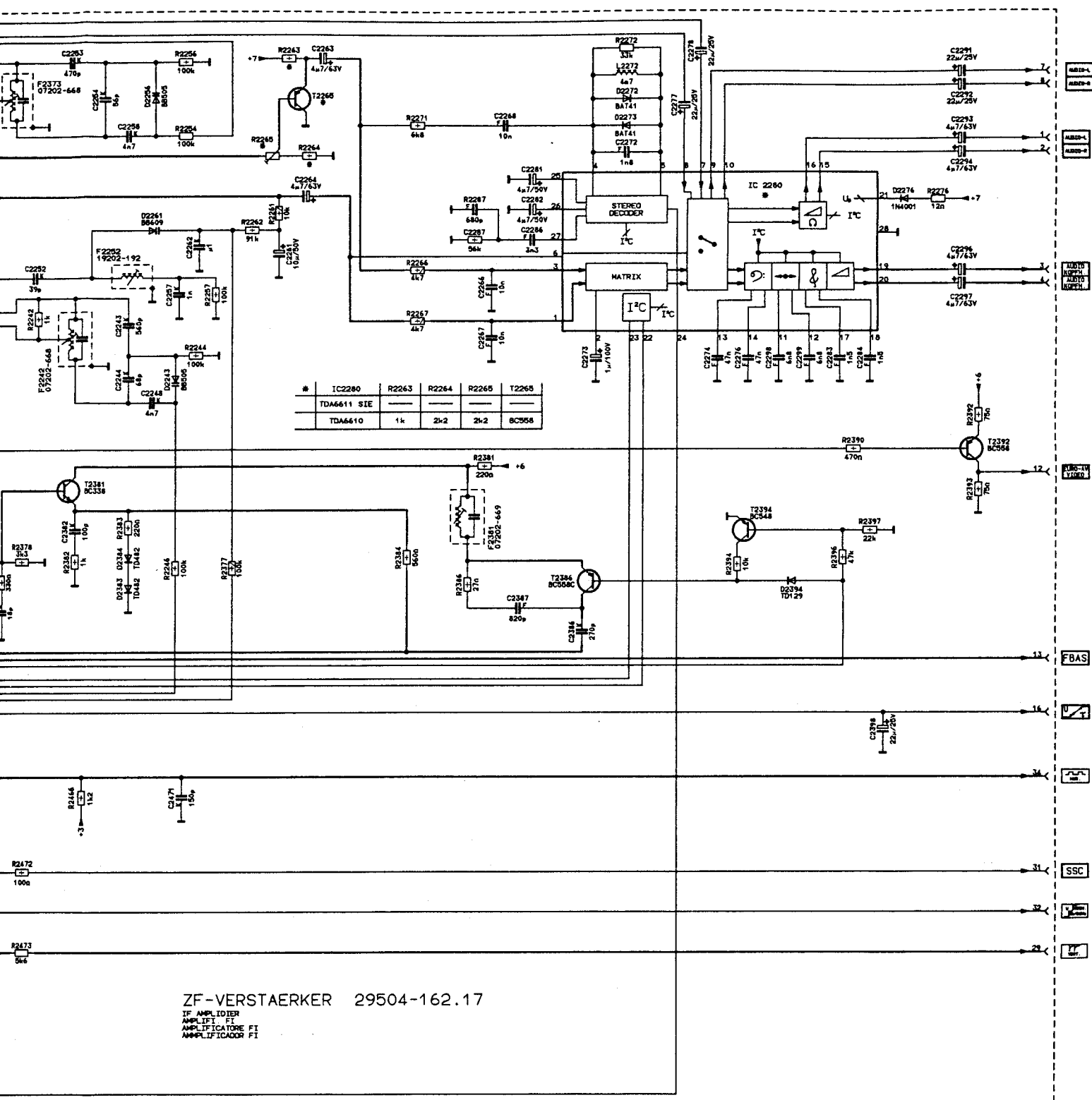


The following functions can be called up from the remote control transmitter:

- Button 2: Selection of Line Frequency Adjustment
- Button 1: Selection of Phase Adjustment
- P+ / C-: Adjustment +/-
- P/C: Button with double function, standard 0 = Inland
standard 1 = France

- Standby: Break without memorising
- Memory: The VT memory button operates as the memory selector. The time window is indicated by the flashing of the decimal point.
- OK: During the period of the time window, memorising of the alignment value otherwise the analogue values will be read out of the NVM.

Analogue functions as for programme mode.

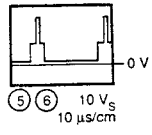
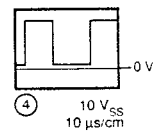
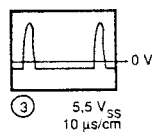
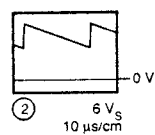
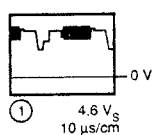
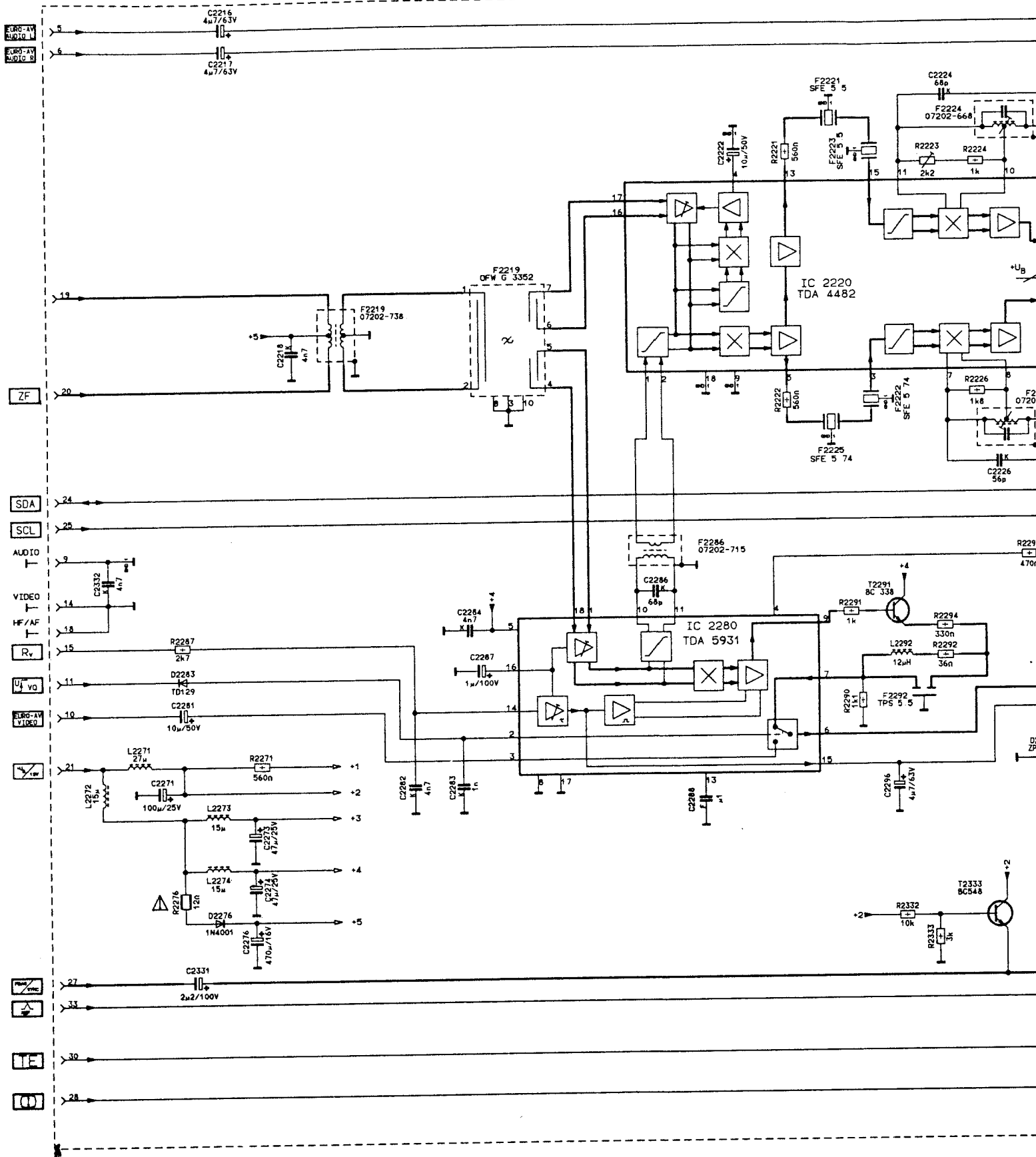


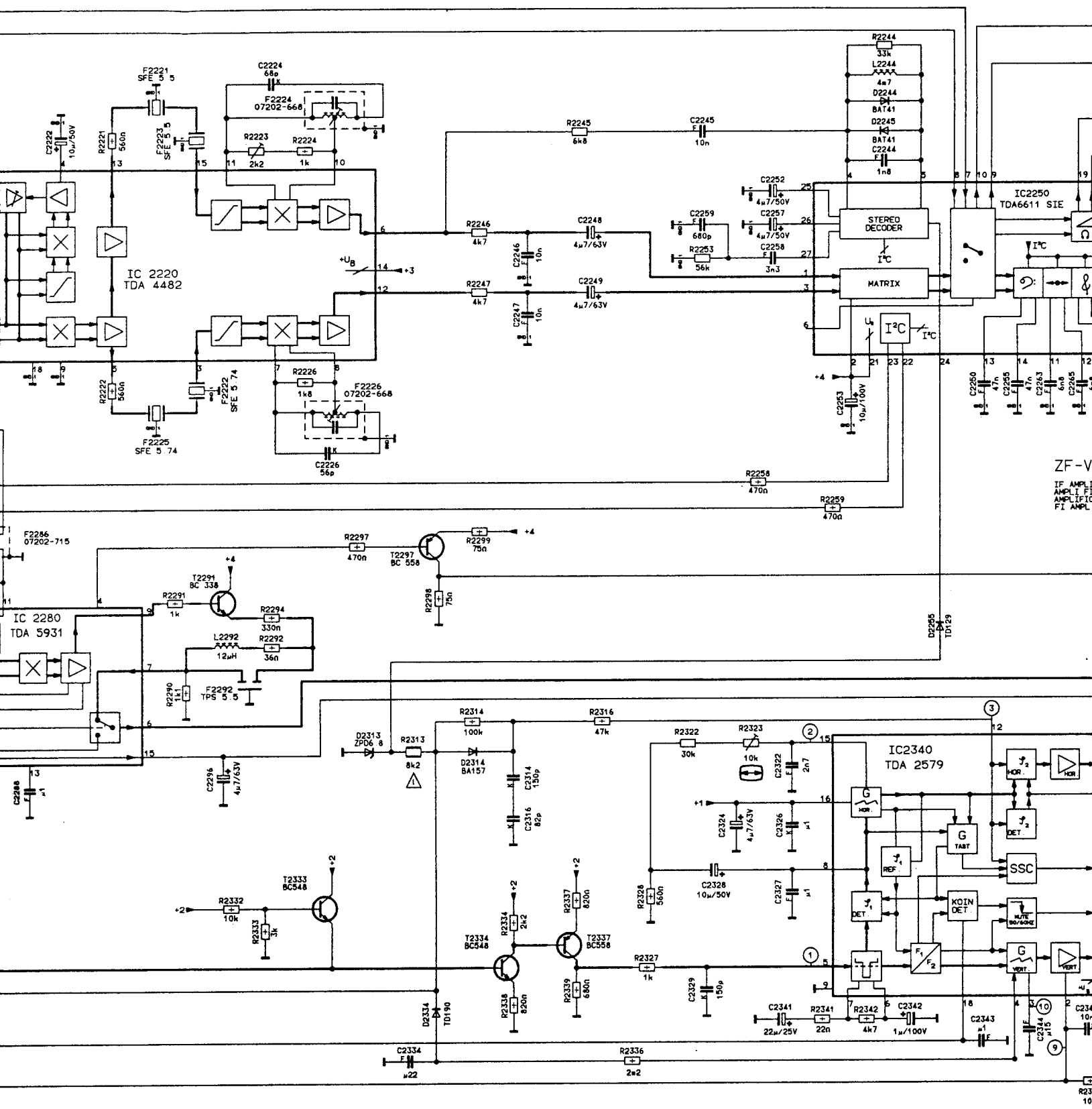
remote control

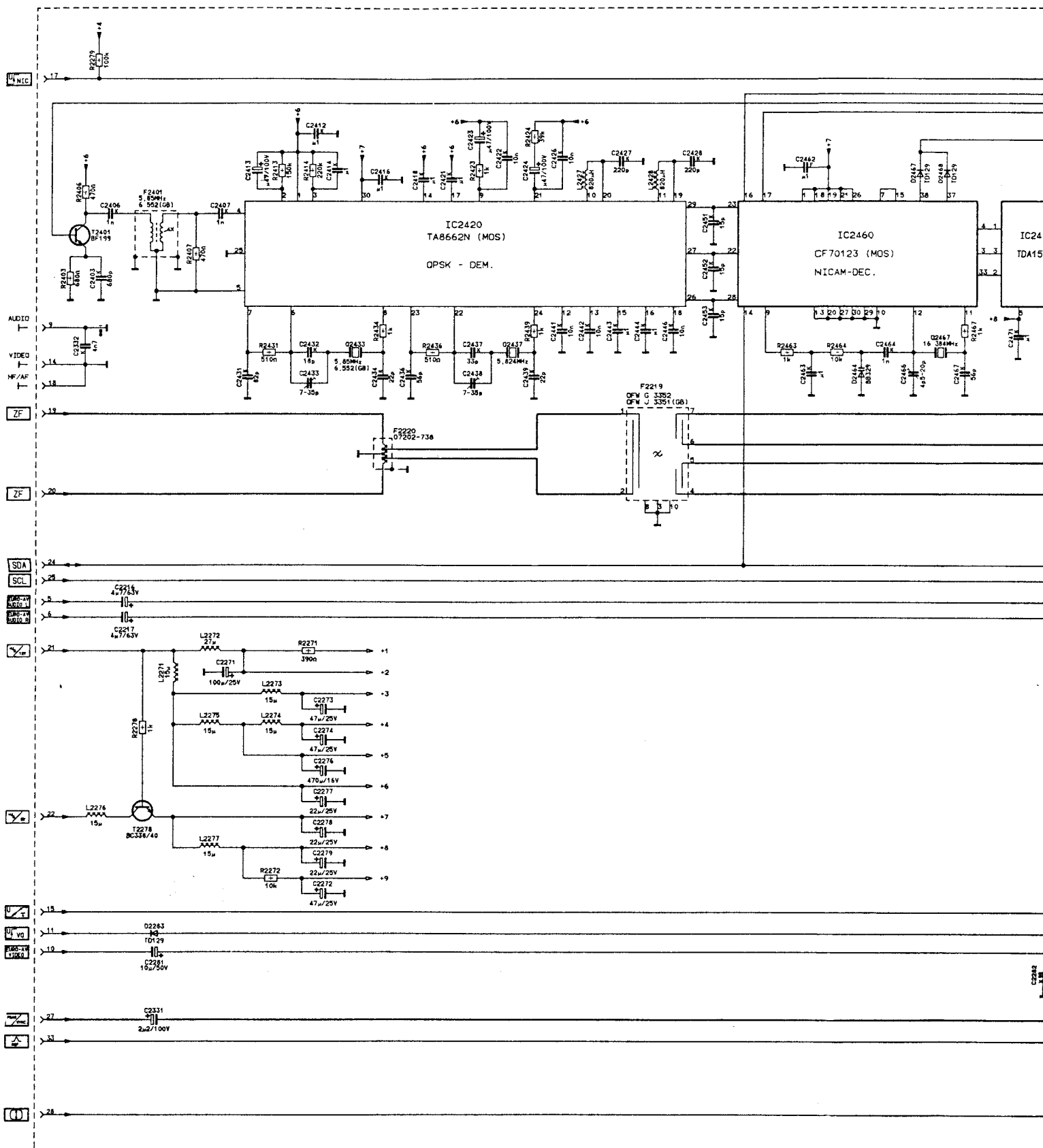
Inland
France

memory selec-
de

omising of the
values will be







D

ABGLEICH DER ZEILENFREQUENZ UND -PHASE

Zeilenfrequenz:

1. Pin 5, IC 2340 (TDA 2579) nach Masse kurzschließen.
2. Mit Einstellregler R 2323 Bild auf langsames Durchlaufen einstellen.
3. Kursor entfernt.

ZeilenPhase:

1. Den Bildbreitenregler R 7002 auf Minimum stellen.
2. Mit dem Trimmer R 2353 den grauen Bildrand symmetrisch zum rechten und linken Bildraster einstellen.
3. Die Bildbreitenpule wieder nach Testbild einstellen.

GB

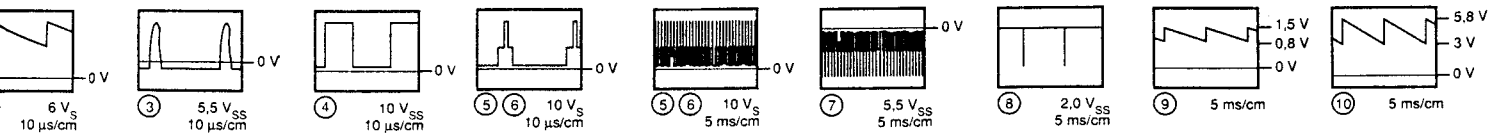
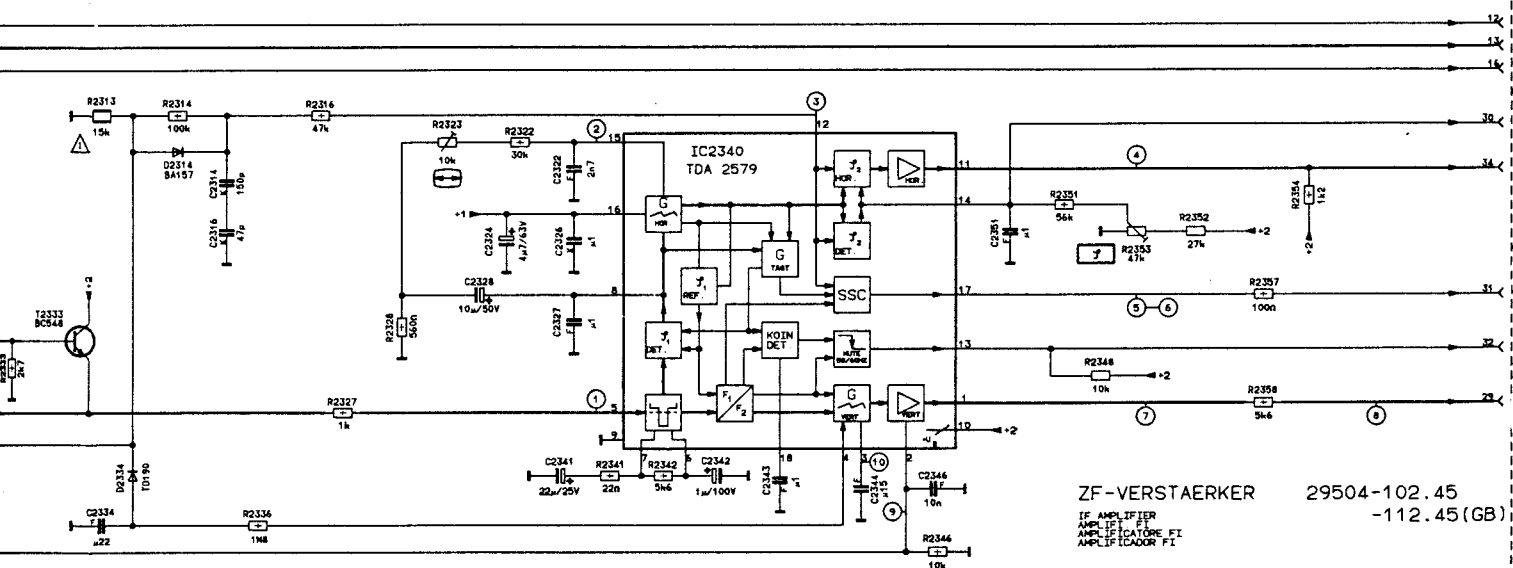
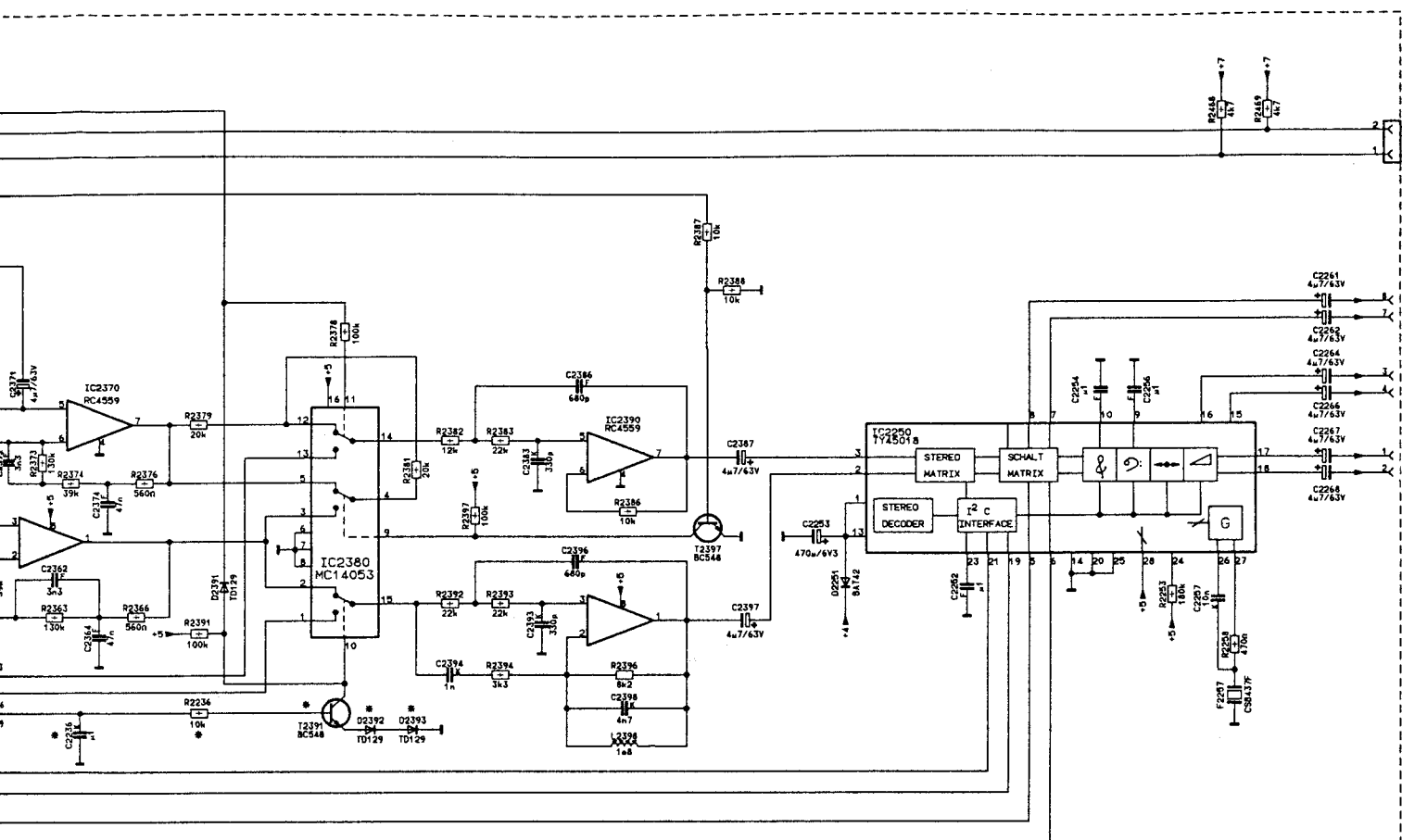
ADJUSTMENT OF LINE FREQUENCY AND PHASE

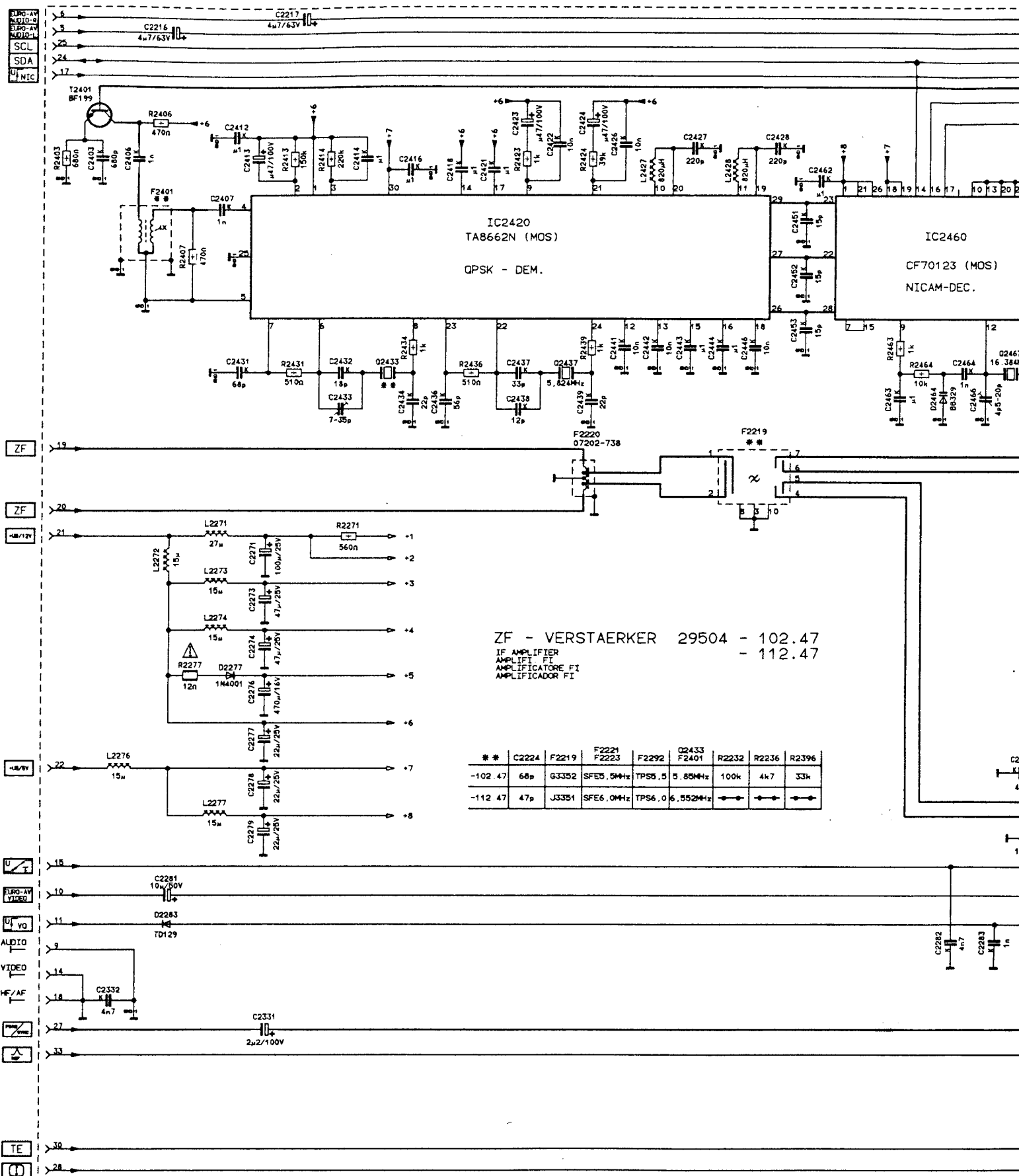
Line Frequency:

1. Short circuit Pin 5, IC 2340 (TDA 2579) to chassis.
2. With the adjustment control R 2323, adjust so that the picture runs through slowly.
3. Remove the short circuit.

Line Phase:

1. Set the picture width control R 7002 to minimum.
2. With the adjustment control R 2353 set the grey picture edges to be symmetrical within the right and left picture frame.
3. Reset the picture width control to conform with the test pattern.





(D)

ABGLEICH DER ZEILENFREQUENZ UND -PHASE

Zeilenfrequenz:

1. Pin 5, IC 2340 (TDA 2579) nach Masse kurzschließen.
2. Mit Einstellregler R 2323 Bild auf langsames Durchlaufen einstellen.
3. Kurzschluß entfernen.

ZeilenPhase:

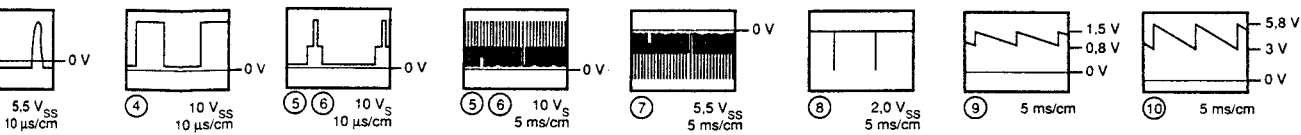
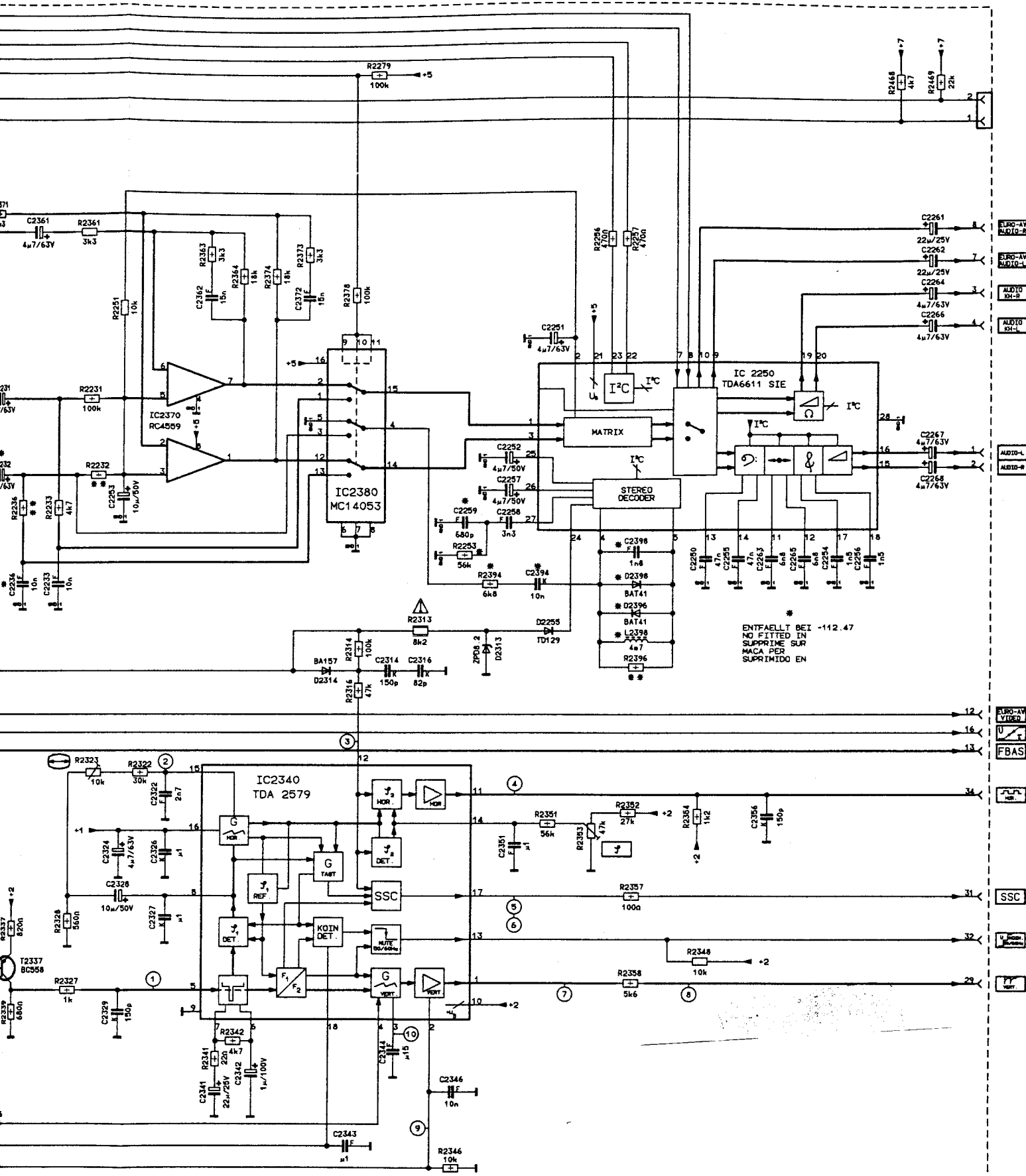
1. Den Bildbreitenregler R 7002 auf Minimum stellen.
2. Mit dem Einstellregler R 2353 den grauen Bildrand symmetrisch zum rechten und linken Bildraster einstellen.
3. Den Bildbreitenregler wieder nach Testbild einstellen.

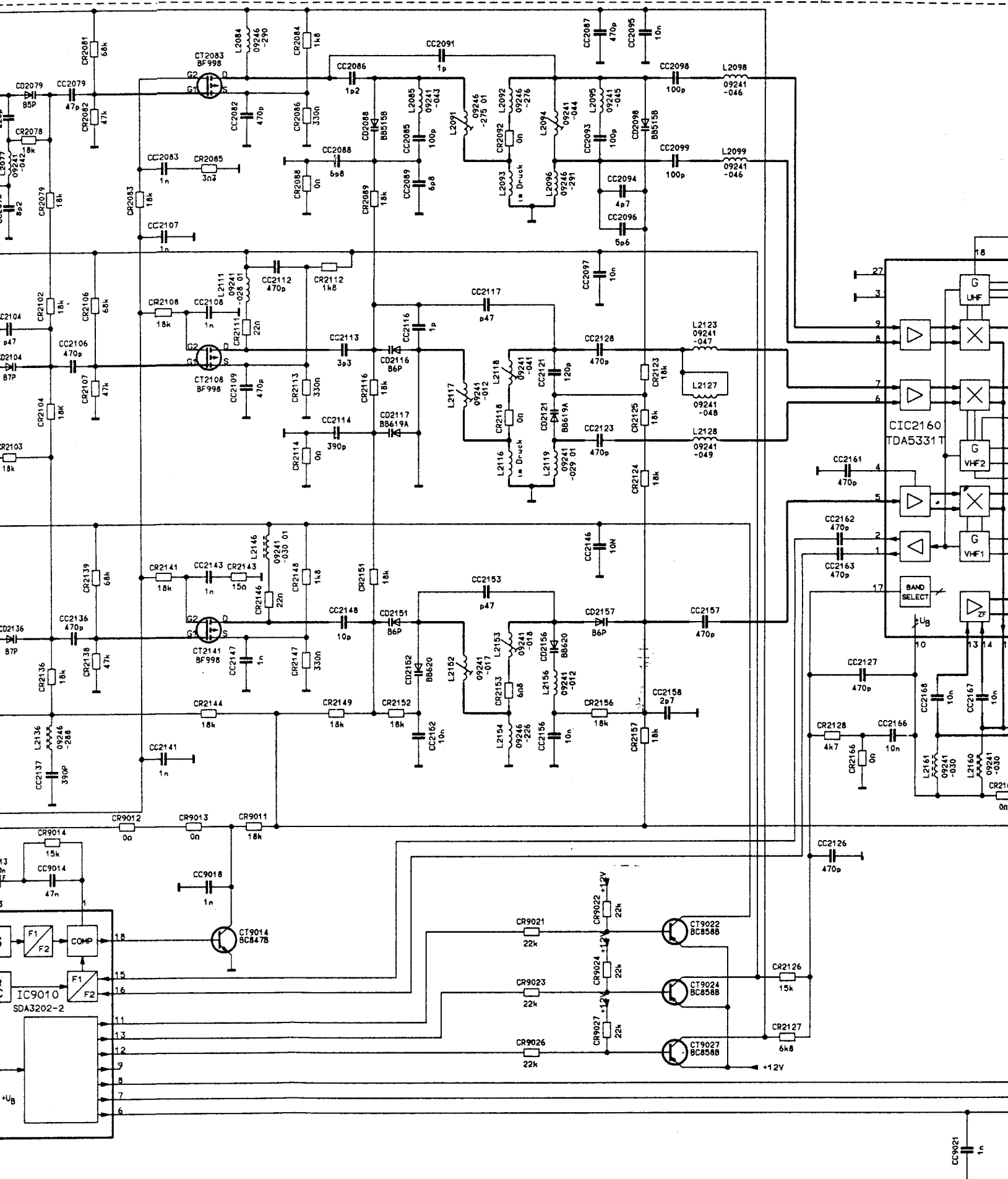
(GB)

ADJUSTMENT OF LINE FREQUENCY AND -PHASE

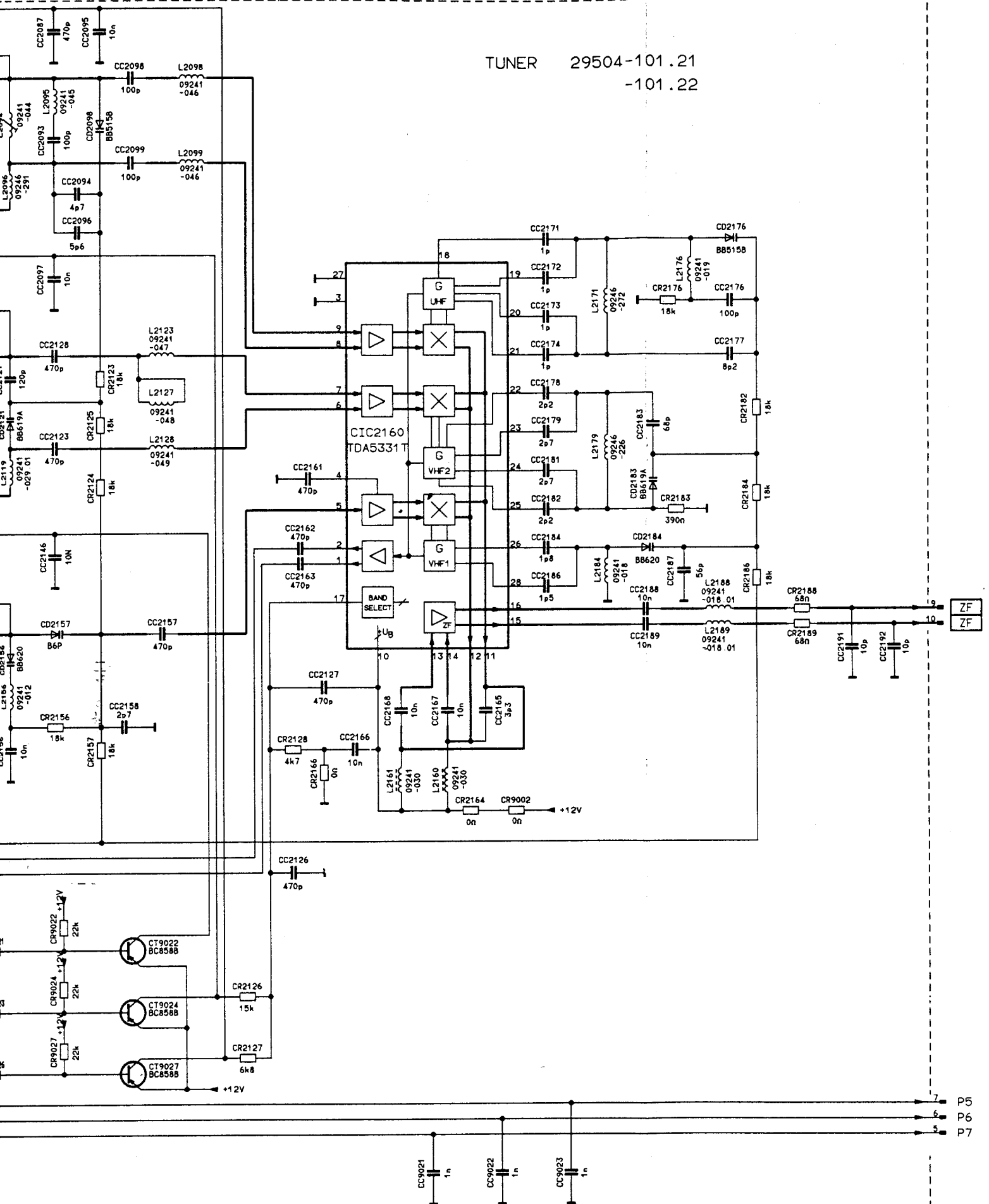
Line Frequency:

1. Short circuit Pin 5, IC 2340 (TDA 2579) to chassis.
2. With the adjustment control R 2323, adjust so that the picture runs through slowly.
3. Remove the short circuit.

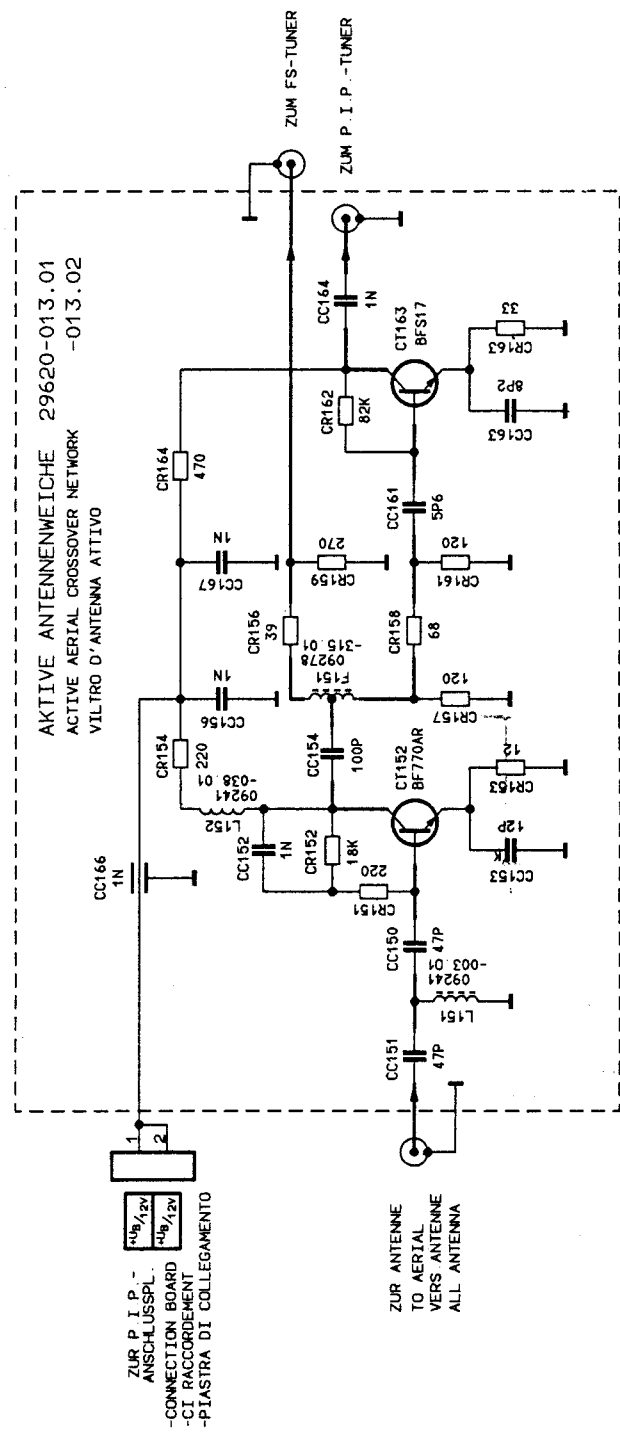
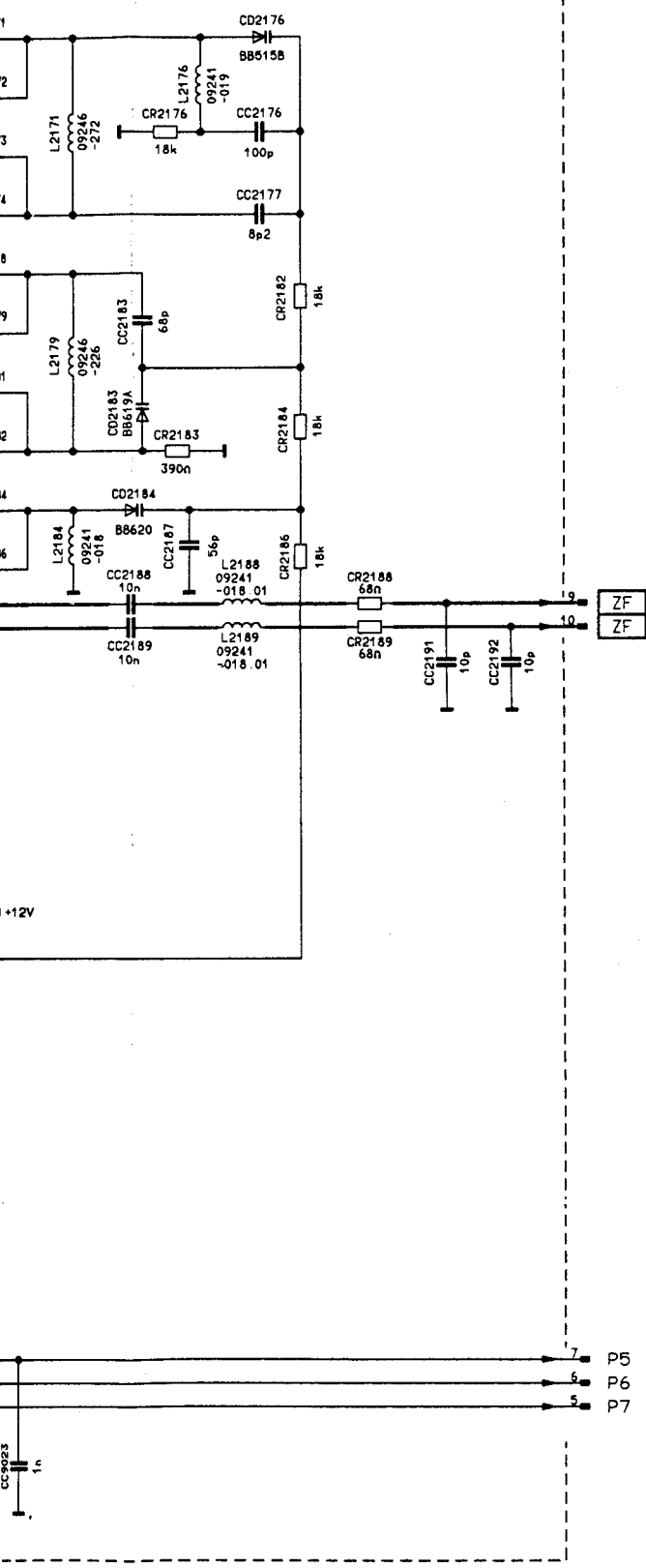




TUNER 29504-101.21 -101.22



29504-101.21
-101.22



ZUM FS TUNER
TO TV SET
AL TUNER TV
VERS TUNER TV

AKTIVE ANTENNENWEICHE 29620-013.01
ACTIVE AERIAL CROSSOVER NETWORK
FILTRO D'ANTENNA ATTIVO
FILTRE D'ANTENNE ACTIF

P.I.P.-ANSCHLUSSPLATTE 29305-015.26

P I P CONN BOARD
PIASTRA DI COLLEGAM P I P
PLAQUE DE RACCORDEMENT P I P

+30V 7
+12V 10
+5V 9

P.I.P./ZF 29504-162.93
P I P IF AMPLIFIER .94
P I P AMPL FI
P I P IF-AMPLIFICATEUR

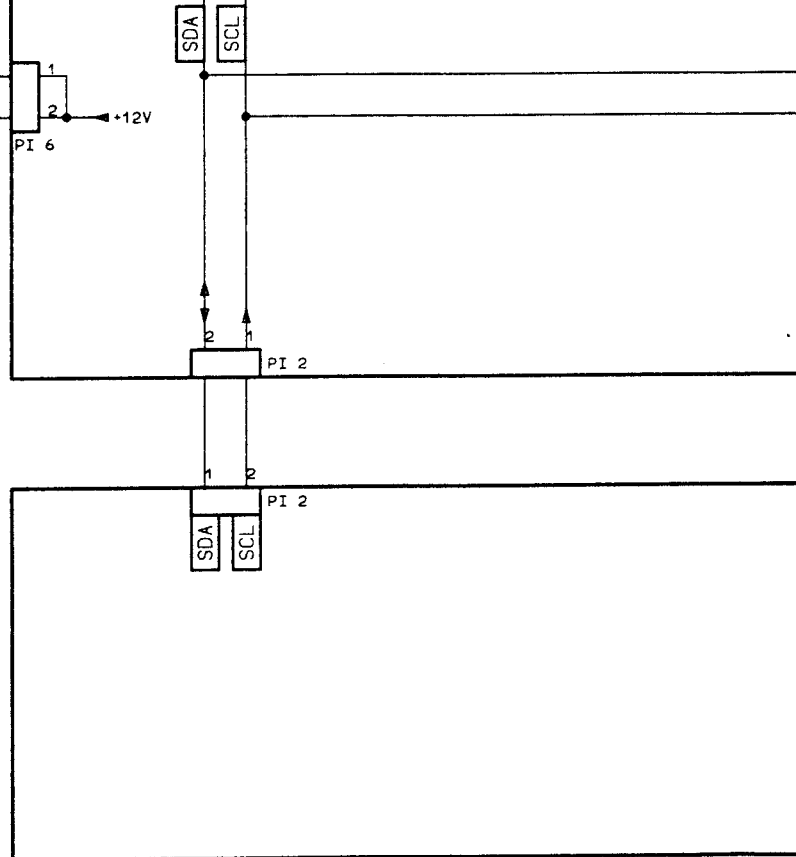
P I P TUNER 29504-101.22

SVHS4

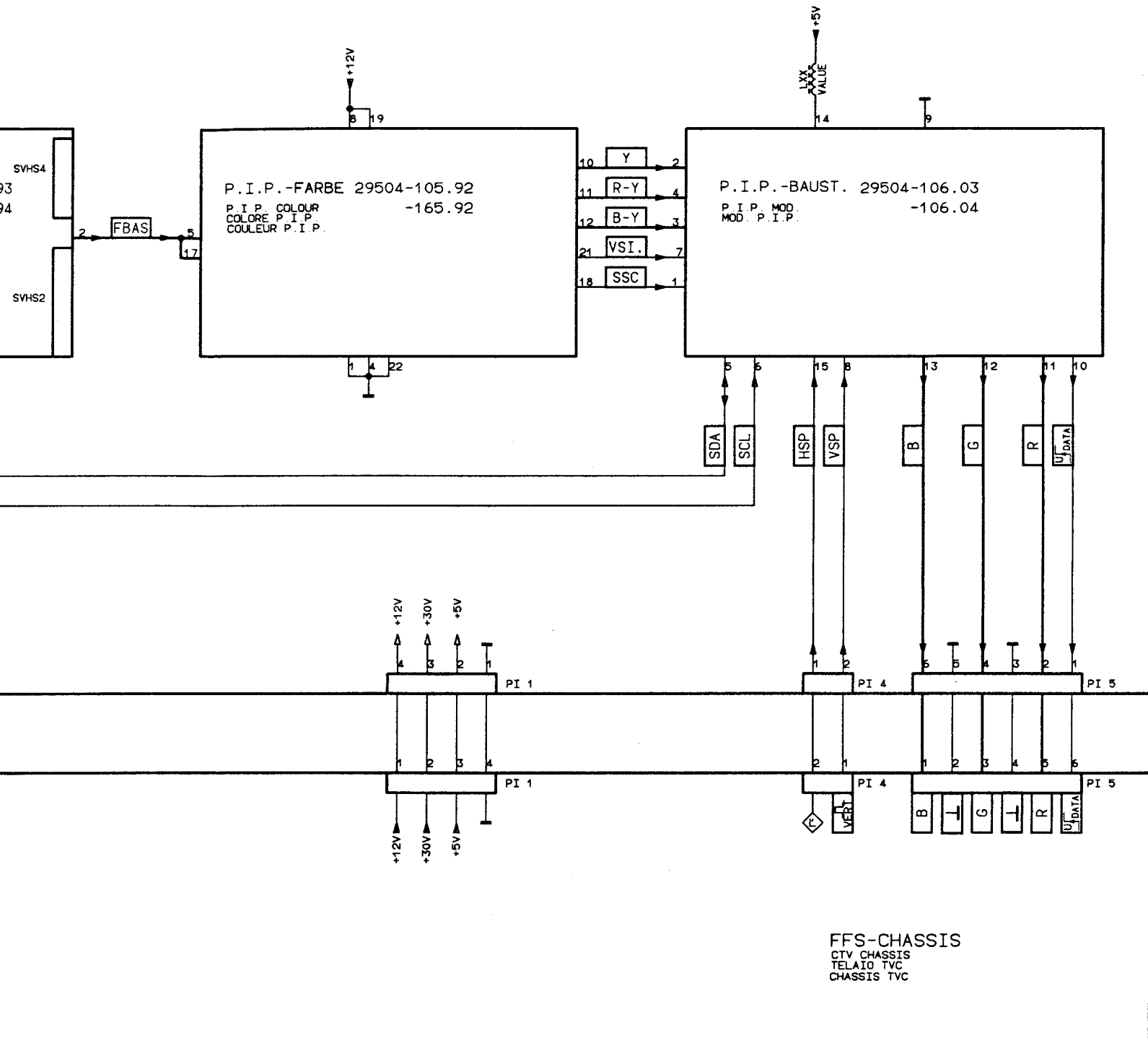
SVHS2

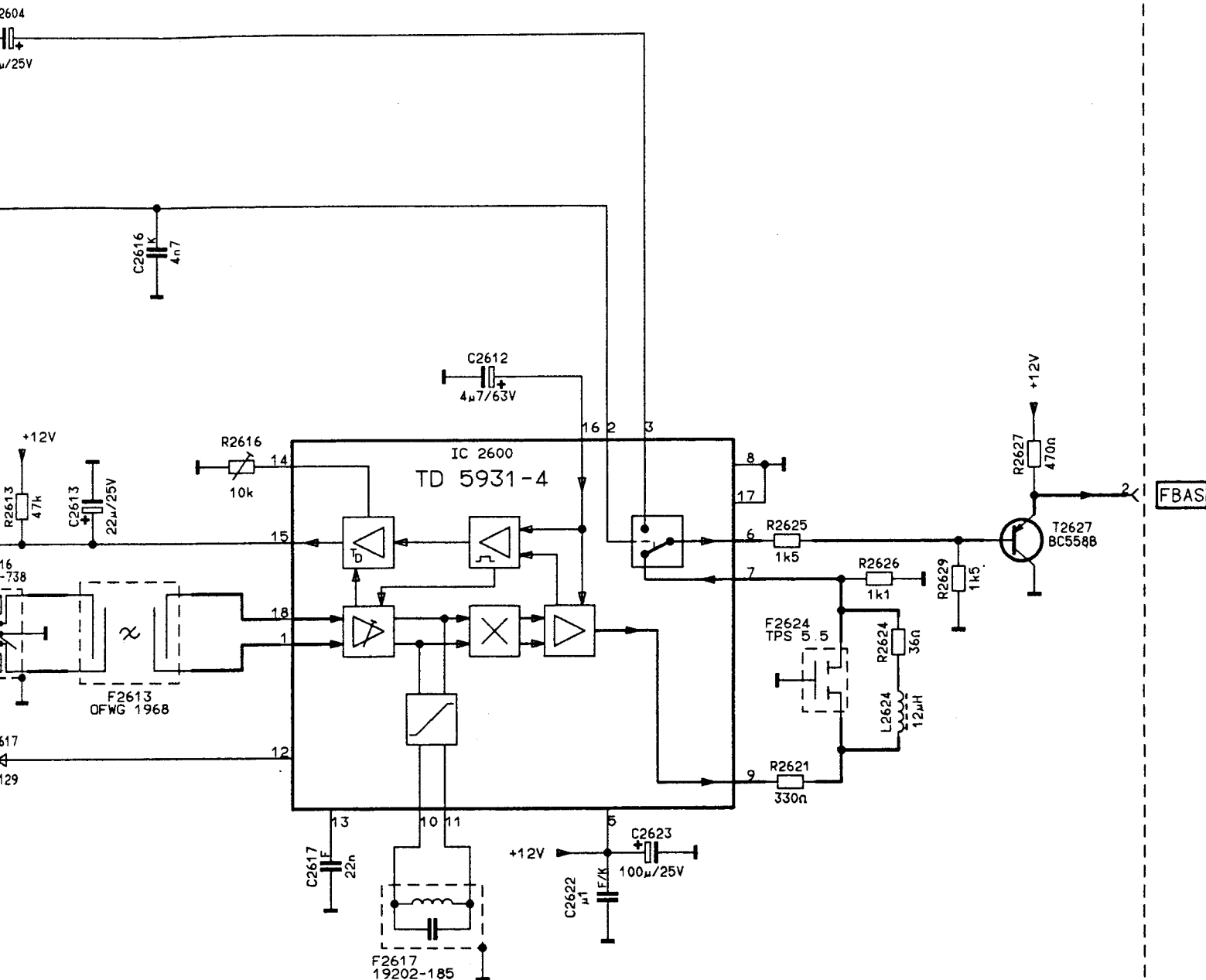
F8AS

P.I.
P.I. COLO
COUL



P.I.P.-EINHEIT 29305-020.07
P I P UNIT -020.08
UNITA P I P -020.09
UNITE P I P





04-162.94

I

(GB)

RV Delayed Automatic Gain Control Voltage (Tuner)

uner)
en, die HF sollte
ies Bild). Regler
chtung Linksan-
n beginnt, dann
rauschfrei wird.

Feed in a standard test pattern at a channel in the upper range of the UHF Band. The RF should be at least 1,5 mV (noise free picture). Rotate the control R 2616 (contact 14, IF-Ampl.) towards the left hand and stop until noise just begins to appear in the picture, then reverse the direction of the control until the picture just becomes noise free.

Einstellungen im Farbkanal

PAL-Testbild einspeisen.

- FK nom., H nom. K max. einstellen.
- IC-Pin 28 des TDA 4557 mit +12V verbinden.
- IC-Pin 17 des TDA 4557 mit Masse verbinden.
- Mit Trimmer C 5783 die durchlaufenden Farbbalken zum Stehen bringen.
- Kurzschlußbrücken entfernen.
- Tastkopf an Pin 3 des IC TDA 4557 anschließen.
- Mit Regler BP und Spule LZ die Doppelbilder des B-Signals zur Deckung bringen.

NTSC-Testbild 3.5 MHz einspeisen.

- IC-Pin 26 des TDA 4557 mit +12V verbinden.
- IC-Pin 17 des TDA 4557 mit Masse verbinden.
- Mit Trimmer C 5788 die durchlaufenden Farbbalken zum Stehen bringen.
- Kurzschlußbrücken entfernen.

SECAM-Testbild einspeisen.

- hochohmiges Voltmeter an Pin 21 des TDA 4557 anschließen,
- Spannung mit F 5794 auf Maximum abgleichen.
- Tastkopf an Pin 1 des TDA 4557 anschließen, mit Spule DR Nulllinie des (R-Y)-Signals auf Zeilenniveau bringen.
- Tastkopf an Pin 3 des TDA 4557 anschließen, mit Spule DB Nulllinie des (B-Y)-Signals auf Zeilenaustastniveau bringen.
- Spule F 5767 so einstellen, daß das (B-Y)-Signal keine Überschwinger hat.

ABGLEICH DER ZEILENFREQUENZ

1. Pin 9, IC 5810 (TDA 2593) nach Masse kurzschließen.
2. Oszilloskop Tastkopf an Bausteinkontakt 18 des PIP Farbbau- steines einhängen (Zeitbasis 20 µsec / cm).
3. Am externen Triggereingang des Oszilloskops das FBAS Signal von Bausteinkontakt 17 anschließen.
4. Mit Einstellregler R 5818 HA Impuls zum Stehen bringen.

Adjustments in chroma channel

Display PAL test pattern.

- Adjust colour level and brightness to nominal value, contrast to maximum.
- Connect pin 28 of IC TDA 4557 to +12V supply.
- Connect pin 17 to IC TDA 4557 to chassis.
- Adjust trimmer C5783 for stationary pattern in colour bars.
- Remove wire links.
- Connect test probe to pin 3 of IC TDA 4557.
- Bring the double image produced by the B-signal to coincidence by adjusting the preset BP and the coil LZ.

Display 3.5 MHz NTSC test pattern.

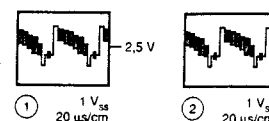
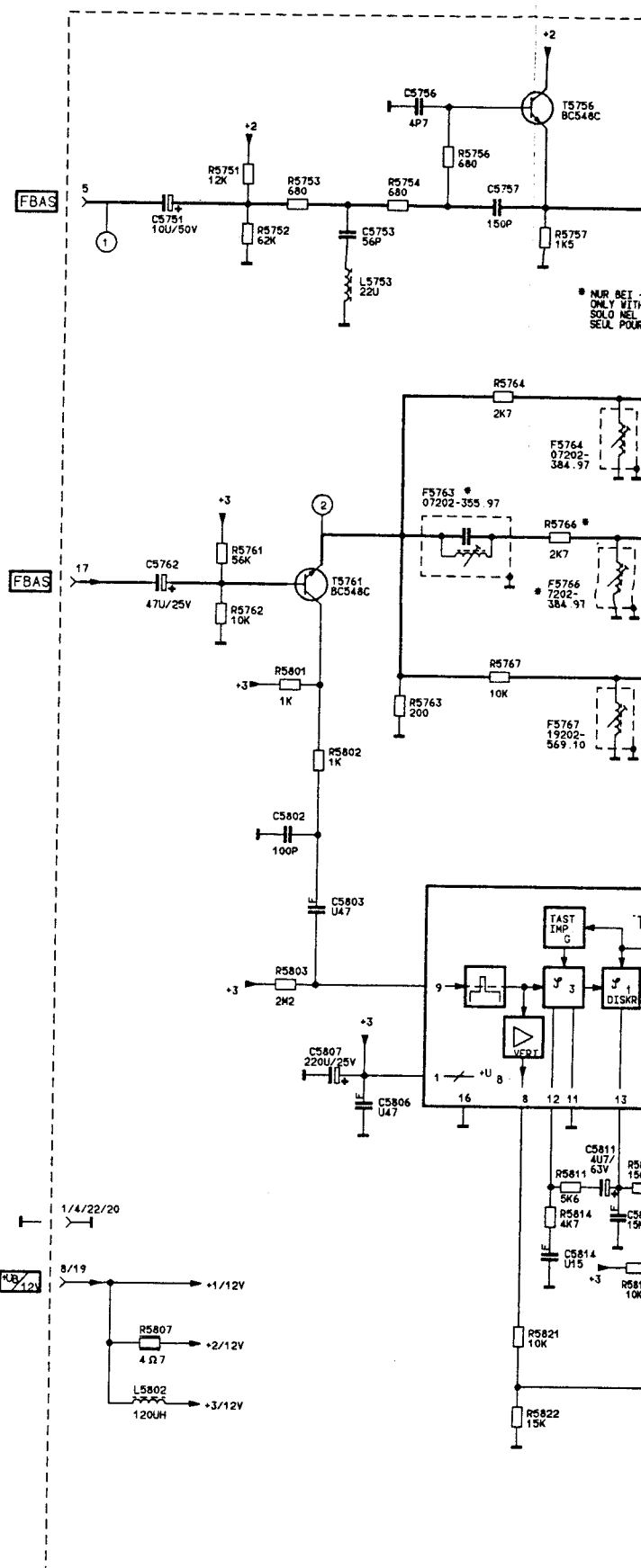
- Connect pin 26 of IC TDA 4557 to +12V supply.
- Connect pin 17 of IC TDA 4557 to chassis.
- Adjust trimmer C5788 for stationary pattern in colour bars.
- Remove wire links.

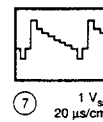
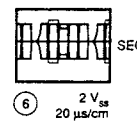
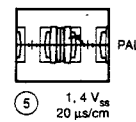
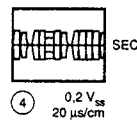
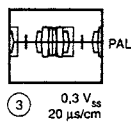
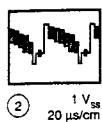
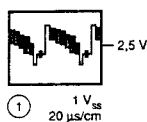
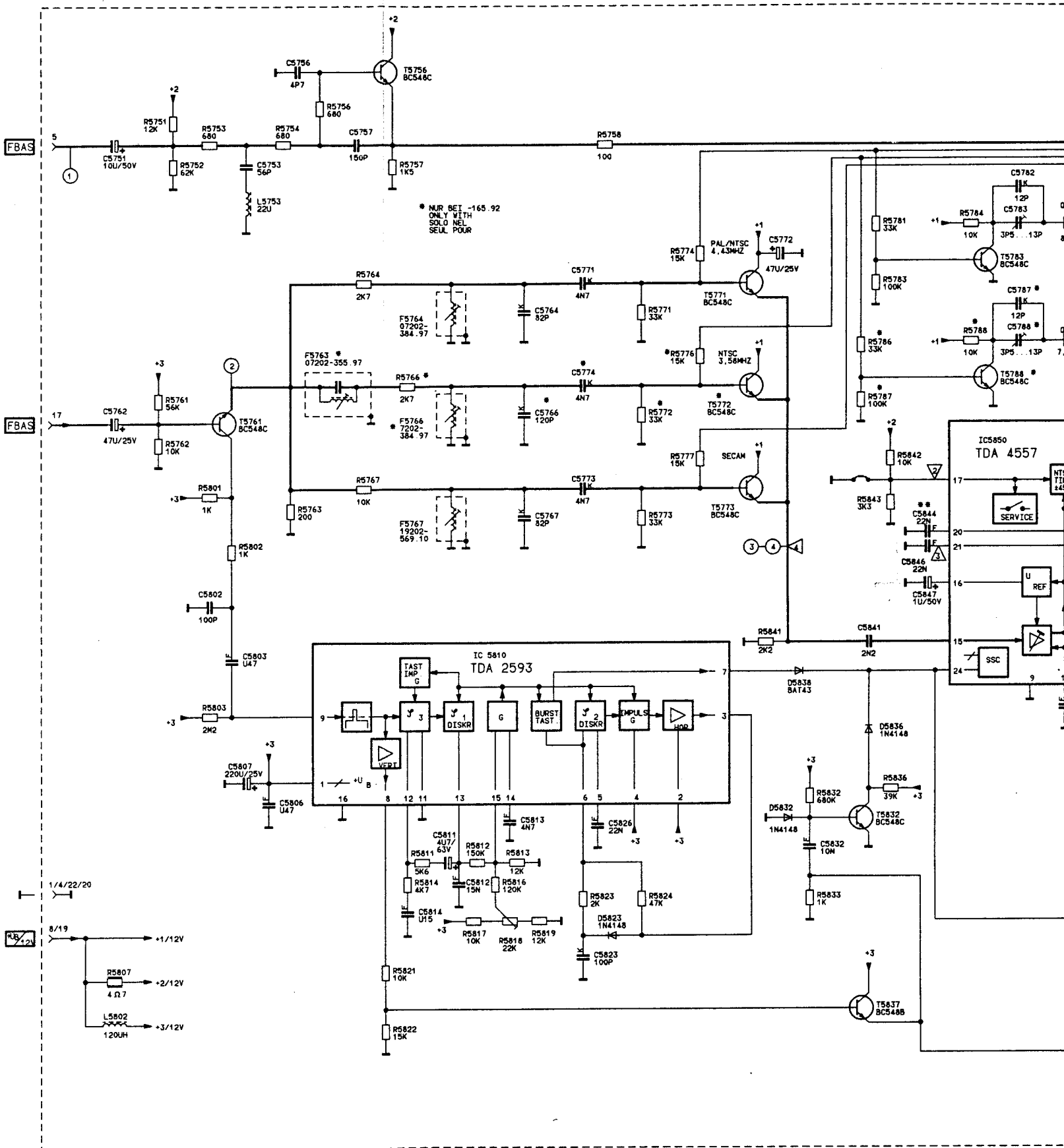
Display SECAM test pattern.

- Connect high-impedance voltmeter to pin 21 of IC TDA 4557
- Use coil F 5794 to align voltage to maximum.
- Connect test probe to pin 1 of IC TDA 4557.
- Use coil DR to align zero level of the (R-Y) signal with the line black level.
- Connect test probe to pin 3 of IC TDA 4557.
- Use coil DB to align zero level of the (B-Y) signal with the line black level.
- Adjust coil F 5767 so that the (B-Y) signal is free of over shooting.

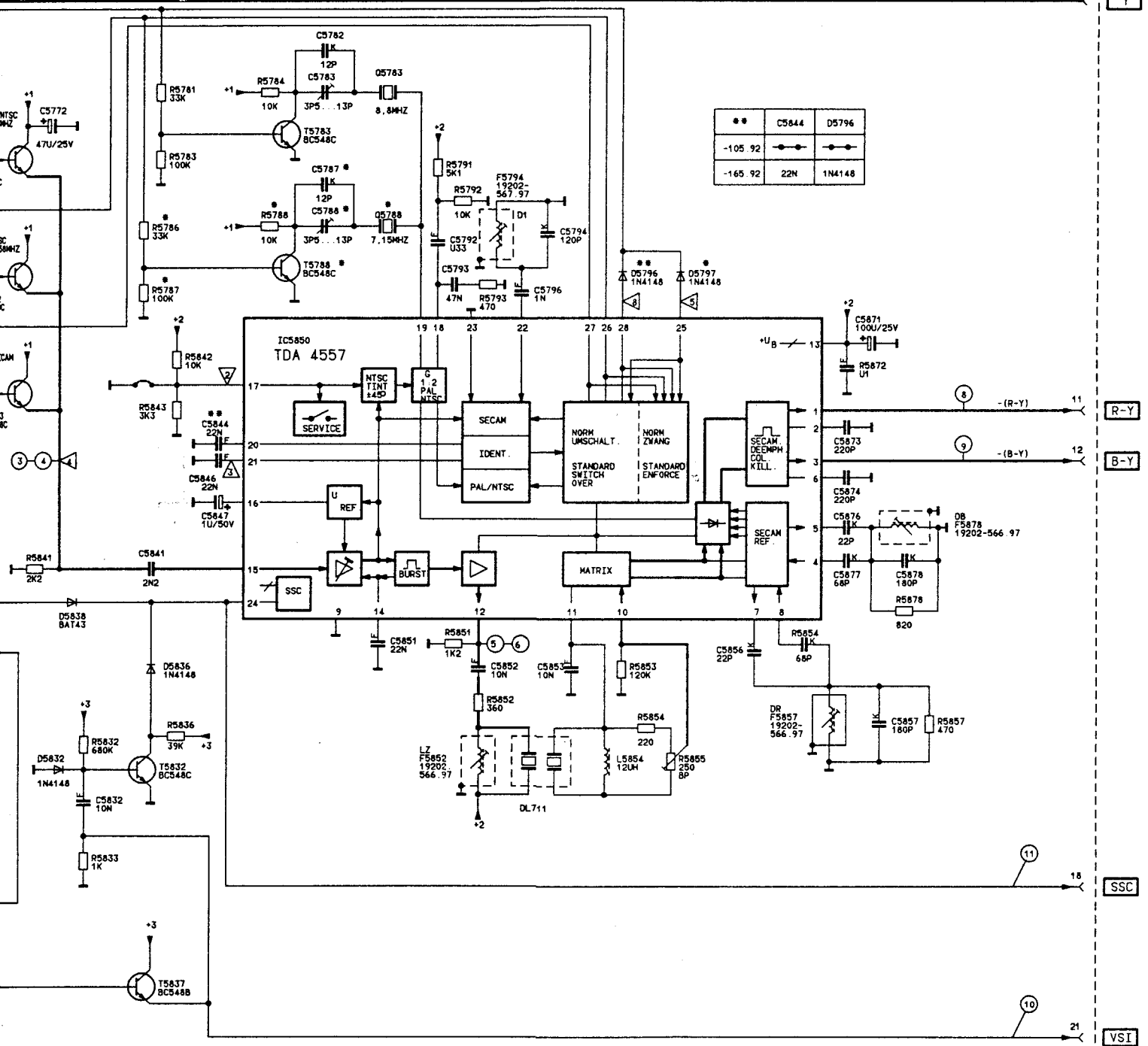
ADJUSTMENT OF LINE FREQUENCY

1. Short circuit Pin 9, IC 5810 (TDA 2593) to chassis.
2. Connect a test probe to contact 18 of colour pip module (set to 20 µsec / div).
3. Connect the external trigger input to contact 17 (FBAS signal). Set R 5818 so that HA - pulse is no longer sweeping horizontally.
4. Remove the short circuit.



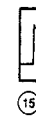
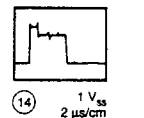
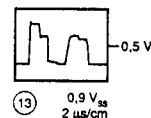
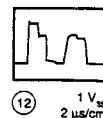
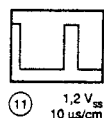
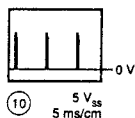
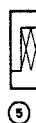
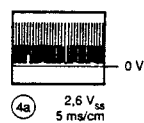
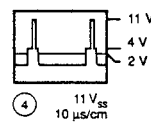
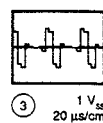
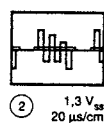
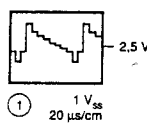
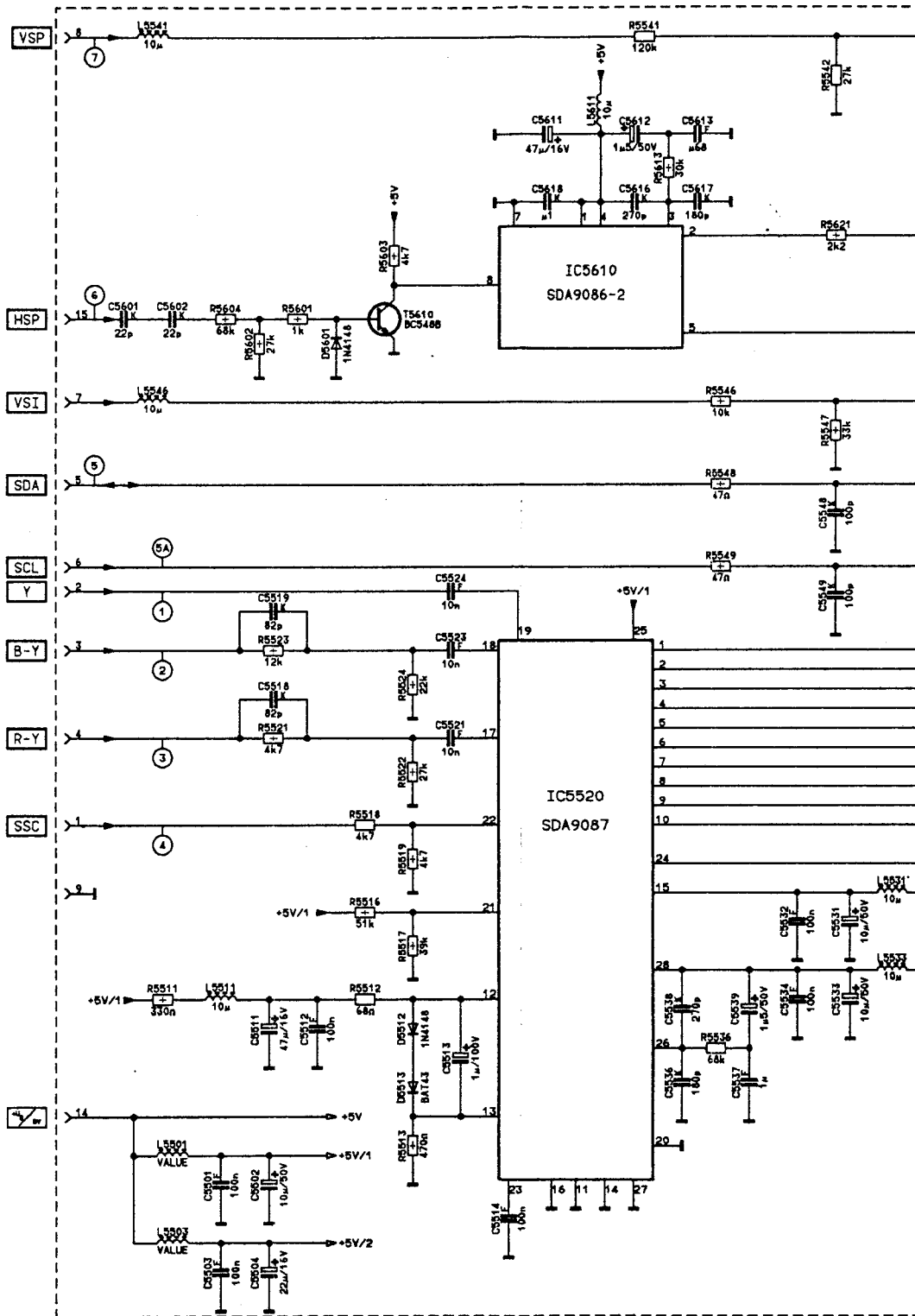
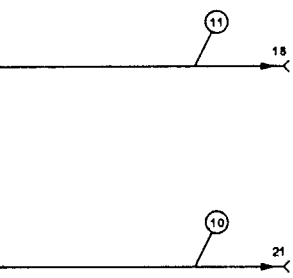
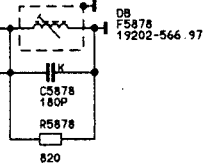


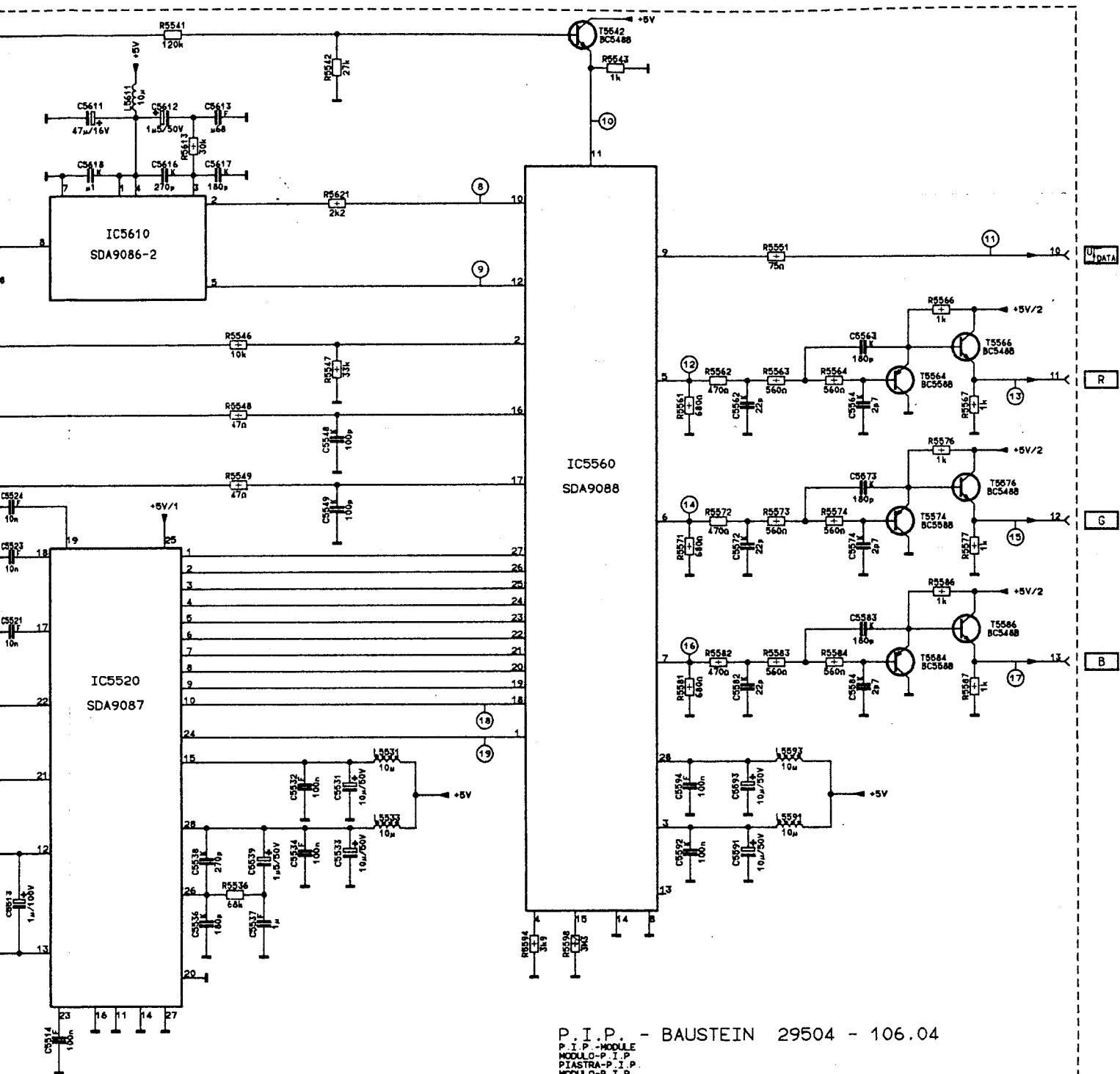
P.I.P.-FARBE 29504-105.92
P.I.P. COLOUR
P.I.P. COLORE
P.I.P. COLEUR
P.P.P. CHROMA



4-105.92
-165.92

5871
000/25V
5872
H





P.I.P. - BAUSTEIN 29504 - 106.04
P.I.P. - MODULE
MODULO-P.I.P.
PIASTRA-P.I.P.
MODULO-P.I.P.

100490

