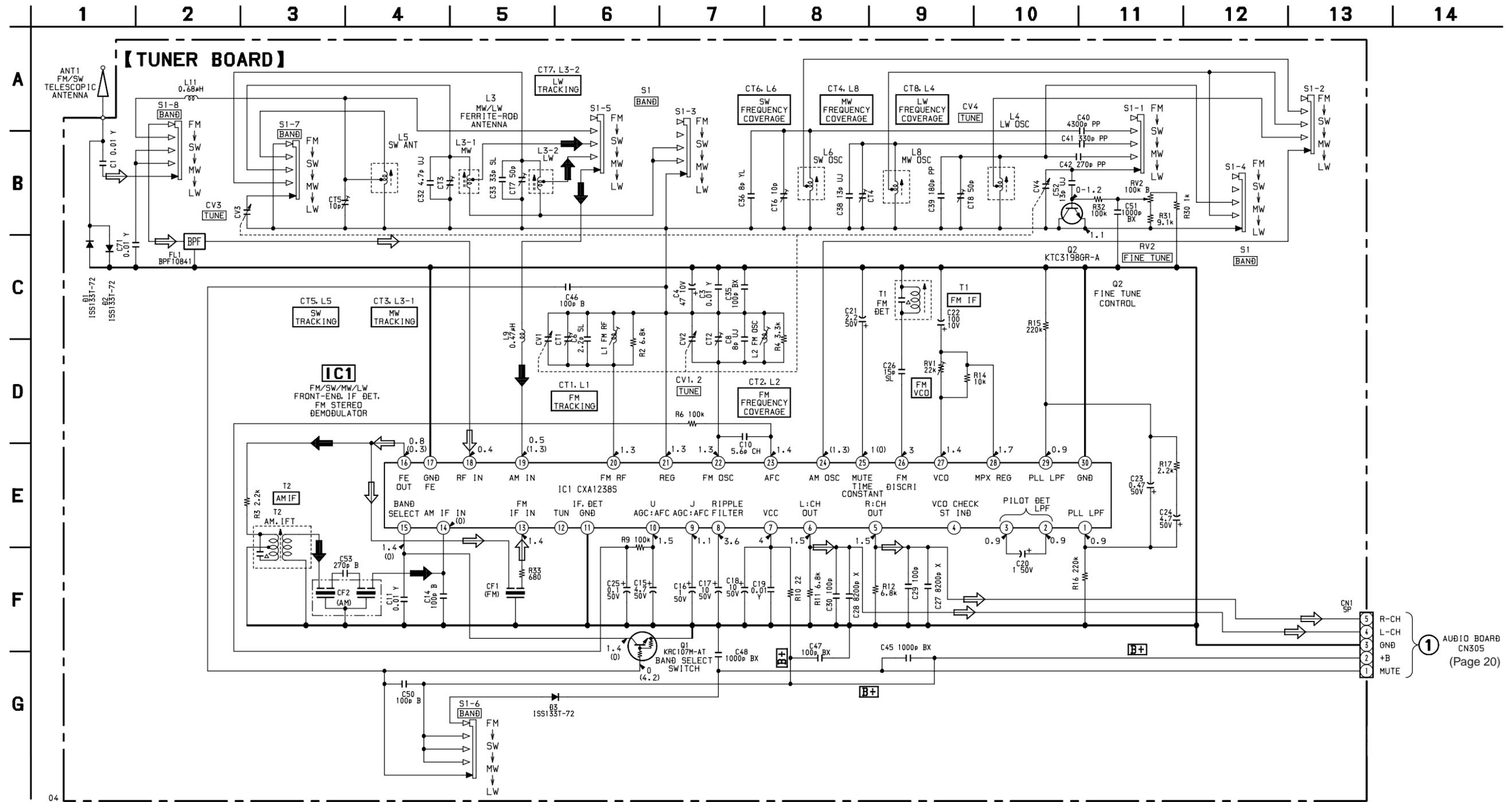


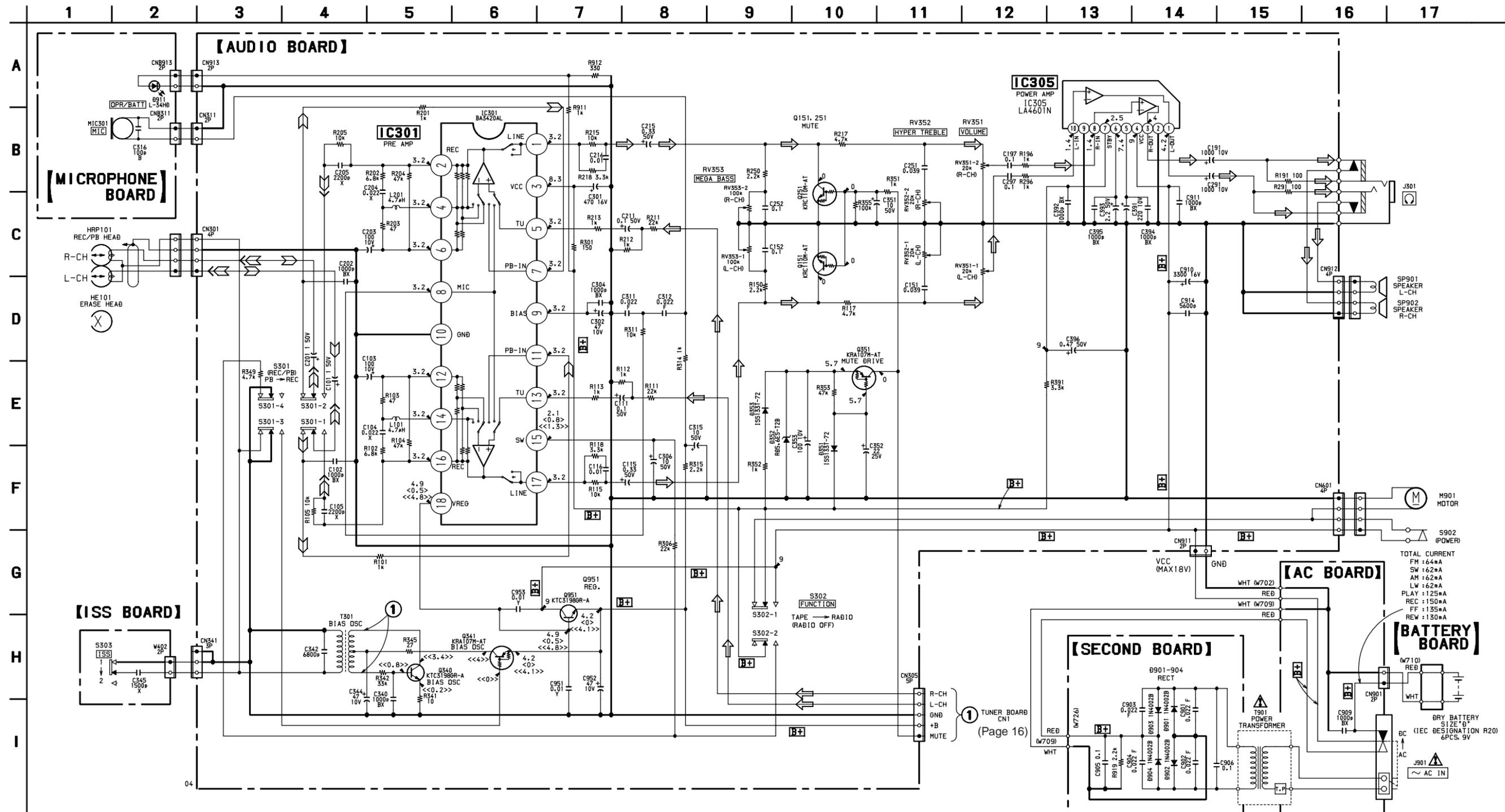
5-4. SCHEMATIC DIAGRAM — TUNER SECTION — • Refer to page 13 for IC Block Diagram.



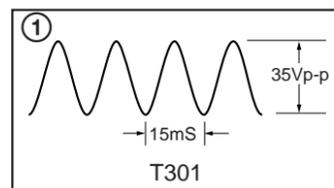
Note:

- All capacitors are in μF unless otherwise noted. pF : μF 50 WV or less are not indicated except for electrolytics and tantalums.
- All resistors are in Ω and $1/4$ W or less unless otherwise specified.
- Δ : internal component.
- \square : panel designation.
- B+ : B+ Line.
- \square : adjustment for repair.
- Power voltage is dc 9 V and fed with regulated dc power supply from battery terminal.
- Voltage is dc with respect to ground under no-signal (detuned) condition.
no mark : FM
() : MW/LW, SW
- Voltages are taken with a VOM (Input impedance 10 $\text{M}\Omega$). Voltage variations may be noted due to normal production tolerances.
- Signal path.
 \rightarrow : FM/SW
 \rightarrow : MW/LW

5-6. SCHEMATIC DIAGRAM — MAIN SECTION —



• Waveform (MODE:REC)



Note:

- All capacitors are in μF unless otherwise noted. pF : μpF 50 WV or less are not indicated except for electrolytics and tantalums.
- \square : panel designation.

Note: The components identified by mark Δ or dotted line with mark Δ are critical for safety. Replace only with part number specified.

- \square : B+ Line.
- Total current is measured with no cassette installed.
- Power voltage is dc 9 V and fed with regulated dc power supply from battery terminal.
- Voltage and waveforms are dc with respect to ground under no-signal (detuned) conditions.
- no mark : FM
 < > : PB
 << >> : REC

- Voltages are taken with a VOM (Input impedance 10 M Ω). Voltage variations may be noted due to normal production tolerances.
- Waveforms are taken with an oscilloscope. Voltage variations may be noted due to normal production tolerances.
- Circled numbers refer to waveforms.
- Signal path.
 \square : FM
 \square : PB
 \square : REC