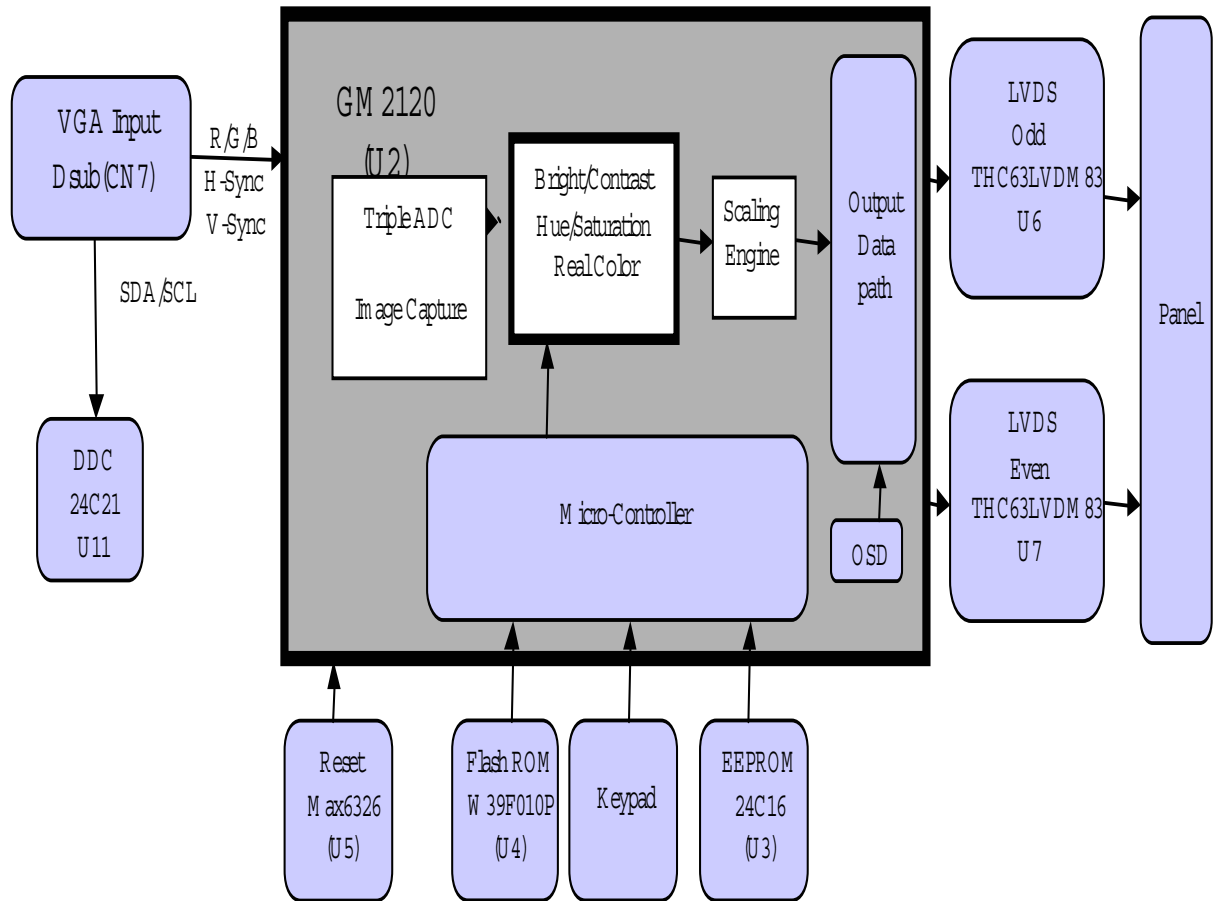


BLOCK DIAGRAM

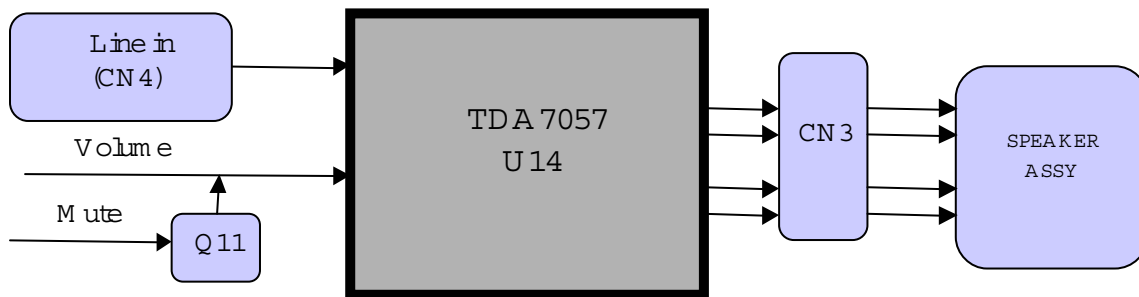
1.VIDEO

Inputs

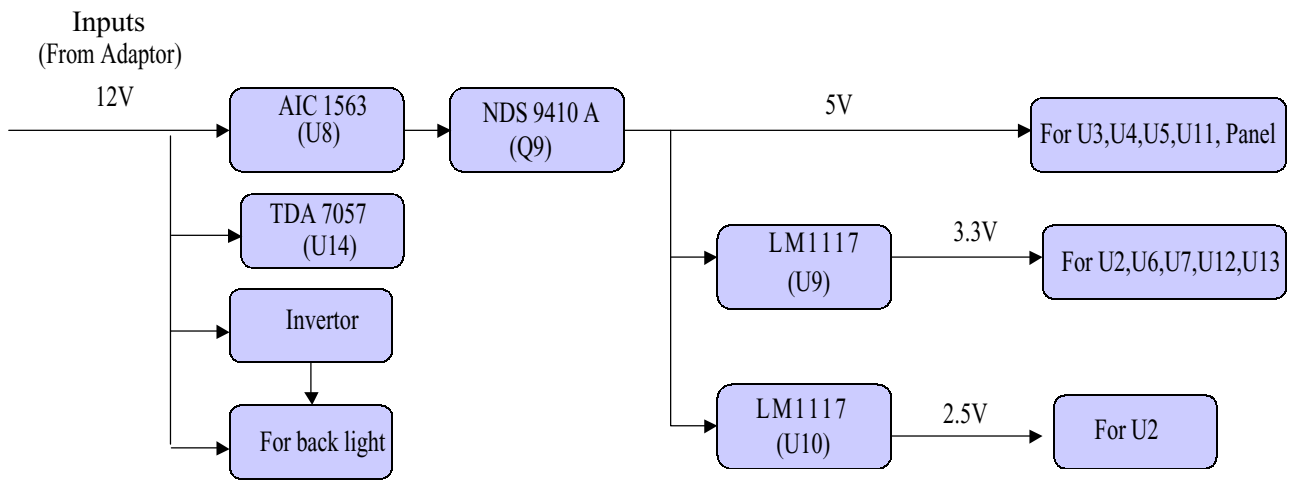


2 AUDIO

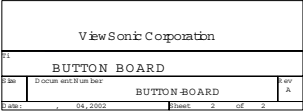
Inputs



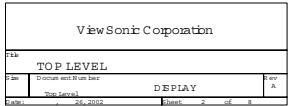
3.POWER

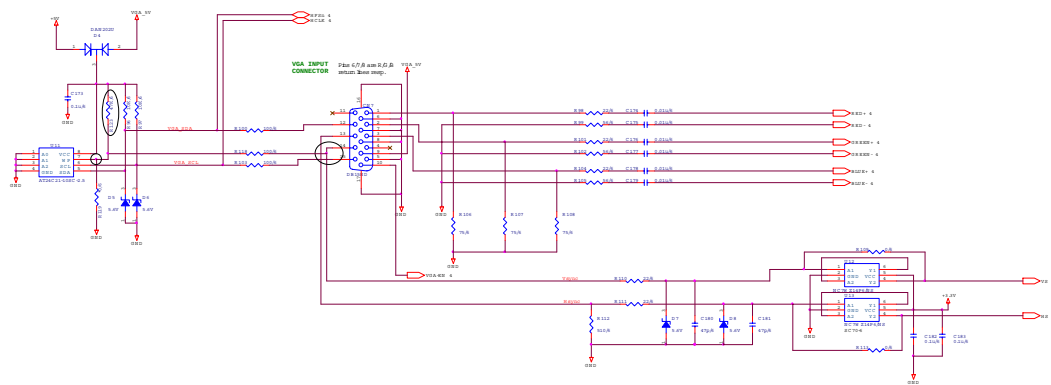


11. SCHEMATIC DIAGRAM

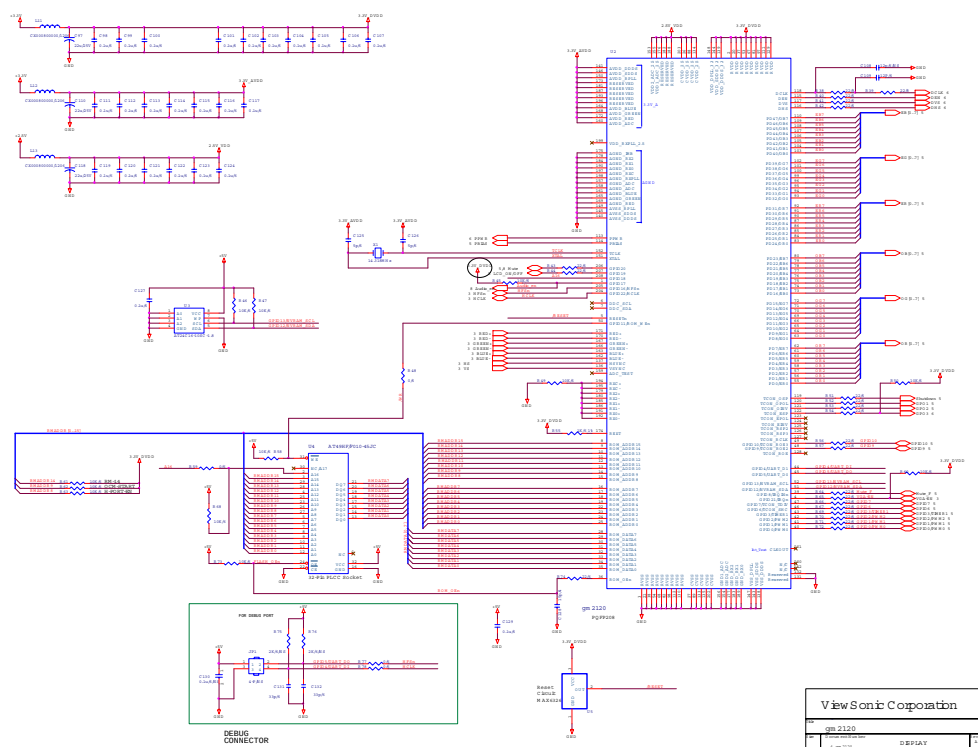


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Date:	04, 2002	Sheet	2 of 2

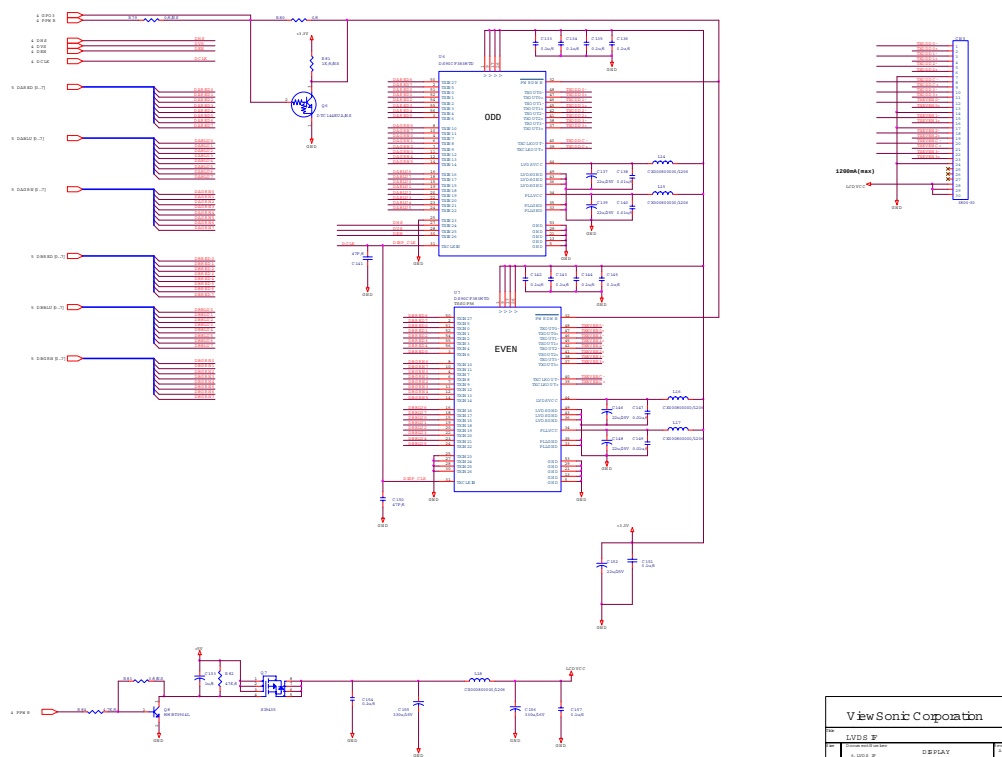


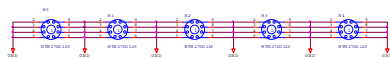
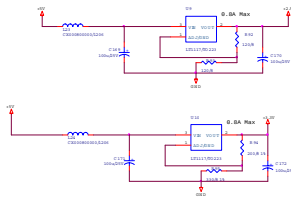
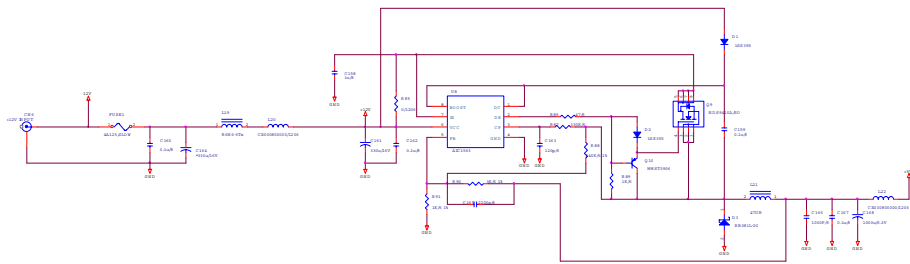


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INPUT CONNECTOR			
REV	1.0	DATE	01/01/01
REV	1.0	DATE	01/01/01









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Part	POWER	1
Rev	0.001	0.001
Rev	0.001	0.001

