

JVC

SCHEMATIC DIAGRAMS

PLASMA DISPLAY MONITOR

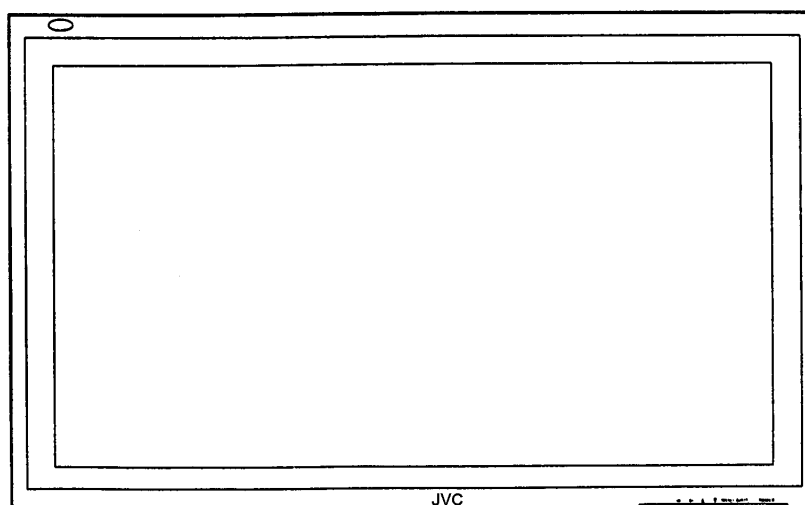
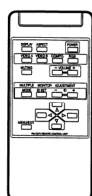
BASIC CHASSIS

P1

NV-PD4200

NV-PD4200/A

CD-ROM No.SML200012



NV-PD4200 / NV-PD4200/A STANDARD CIRCUIT DIAGRAM

1. SAFETY

The components identified by the  symbol and shading are critical for safety. For continued safety replace safety critical components only with manufactures recommended parts.

2.SPECIFIED VOLTAGE AND WAVEFORM VALUES

The voltage and waveform values have been measured under the following conditions.

(1) Input signal	: Colour bar signal
(2) Setting positions of each knob/button and variable resistor	: Original setting position when shipped
(3) Internal resistance of tester	: DC 20k Ω /V
(4) Oscilloscope sweeping time	: H $\Rightarrow$ 20 μ S/div
	: V $\Rightarrow$ 5mS/div
	: Others $\Rightarrow$ Sweeping time is specified
(5) Voltage values	: All DC voltage values

* Since the voltage values of signal circuit vary to some extent according to adjustments, use them as reference values.

3.INDICATION OF PARTS SYMBOL [EXAMPLE]

●In the PW board :R1209→R209

4.INDICATIONS ON THE CIRCUIT DIAGRAM

(1)Resistors

● Resistance value

No unit	: [Ω]
K	: [$\text{K}\Omega$]
M	: [$\text{M}\Omega$]

● Rated allowable power

No indication	:1/10[W]
Others	:As specified

●Type

No indication	:Carbon resistor
OMR	:Oxide metal film resistor
MFR	:Metal film resistor
MPR	:Metal plate resistor
UNFR	:Uninflammable resistor
FR	:Fusible resistor

*Composition resistor 1/2 [W] is specified as 1/2S or Comp.

(2)Capacitors

- Capacitance value
 - 1 or higher :[pF]
 - less than 1 :[μF]

●Withstand voltage

No indication	:DC50[V]
AC indicated	:AC withstand voltage [V]
Others	:DC withstand voltage [V]

*Electrolytic Capacitors

47/50[Example]:Capacitance value [μF]/withstand voltage[V]

- Type
- No indication :Ceramic capacitor
- MY :Mylar capacitor
- MM :Metalized mylar capacitor
- PP :Polypropylene capacitor
- MPP :Metalized polypropylene capacitor
- MF :Metalized film capacitor
- TF :Thin film capacitor
- BP :Bipolar electrolytic capacitor
- TAN :Tantalum capacitor

(3) Coils

No unit	: $[\mu\text{H}]$
Others	:As specified

(4)Power Supply



:B1
:B2(12V)
:9V
:5V

*Respective voltage values are indicated

(5) Test point

 :Test point
 :Only test point display

(6) Connecting method

 :Connector
 :Wrapping or soldering
 :Receptacle

(7)Ground symbol

	:LIVE side ground
	:ISOLATED(NEUTRAL) side ground
	:EARTH ground
	:DIGITAL ground

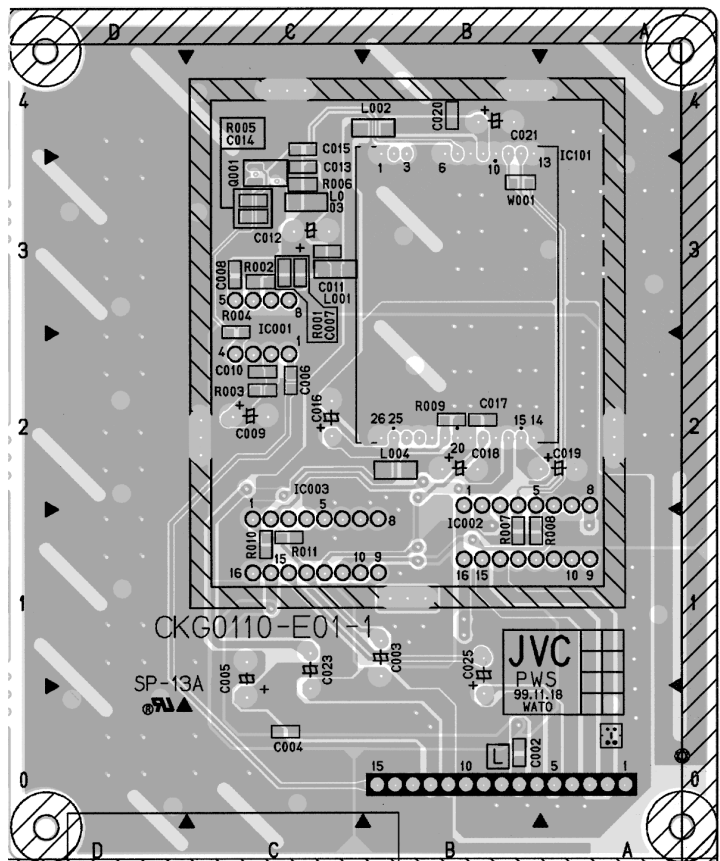
5.NOTE FOR REPAIRING SERVICE

This model's power circuit is partly different in the GND. The difference of the GND is shown by the LIVE : (⊥) side GND and the ISOLATED(NEUTRAL) : (⌋) side GND. Therefore, care must be taken for the following points.

(1) Do not touch the LIVE side GND or the LIVE side GND and the ISOLATED(NEUTRAL) side GND simultaneously. If the above caution is not respected, an electric shock may be caused. Therefore, make sure that the power cord is surely removed from the receptacle when, for example, the chassis is pulled out.

(2) Do not short between the LIVE side GND and ISOLATED(NEUTRAL) side GND or never measure with a measuring apparatus (oscilloscope, etc.) the LIVE side GND and ISOLATED(NEUTRAL) side GND at the same time. If the above precaution is not respected , a fuse or any parts will be broken.

◇ Since the circuit diagram is a standard one, the circuit and circuit constants may be subject to change for improvement without any notice.



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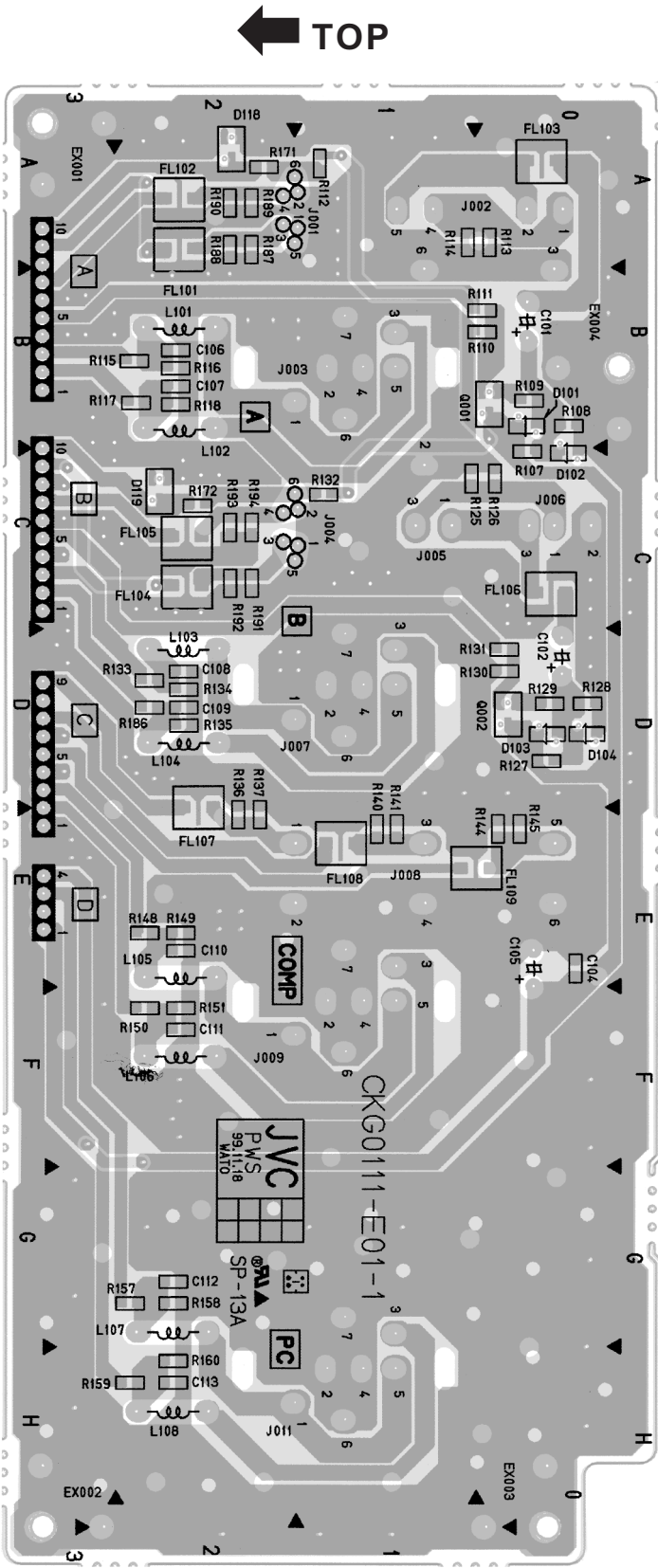
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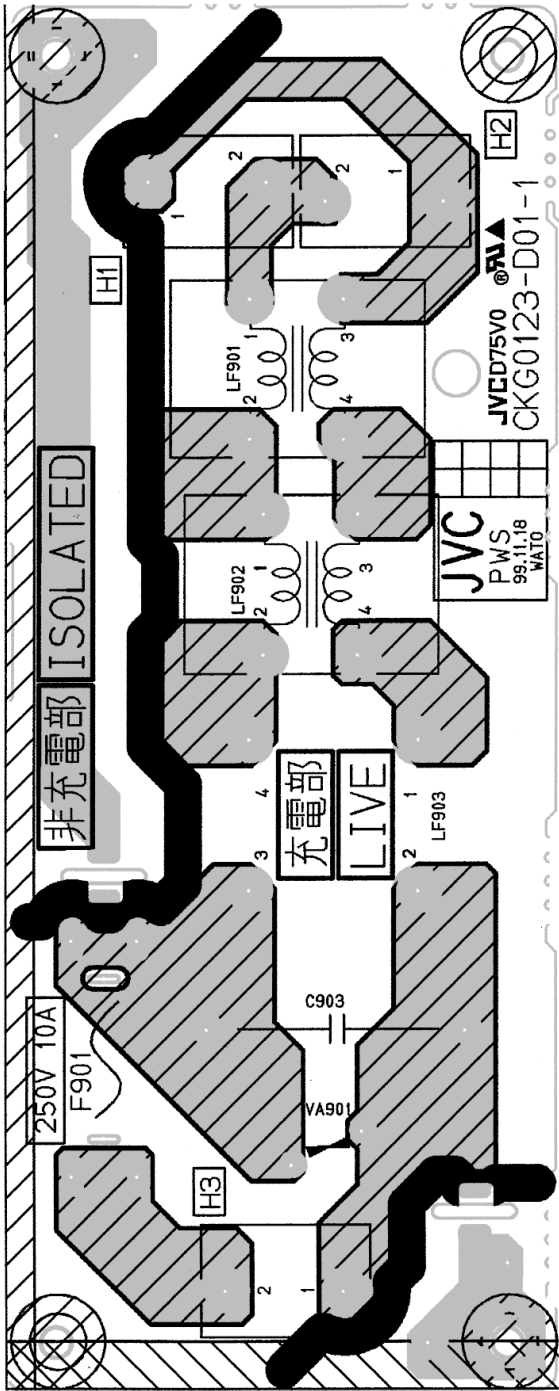
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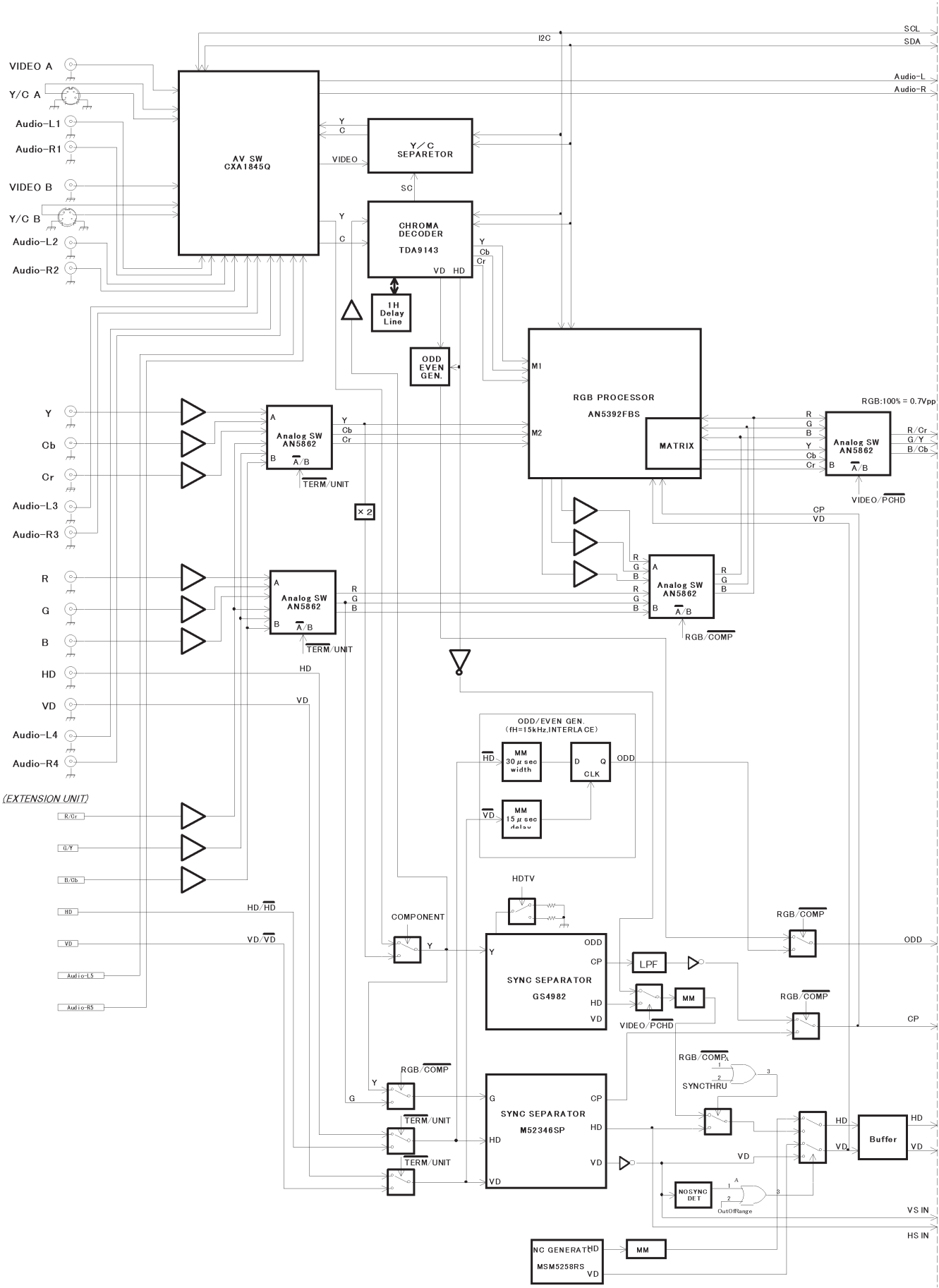
[TERMINAL PWB PATTERN]



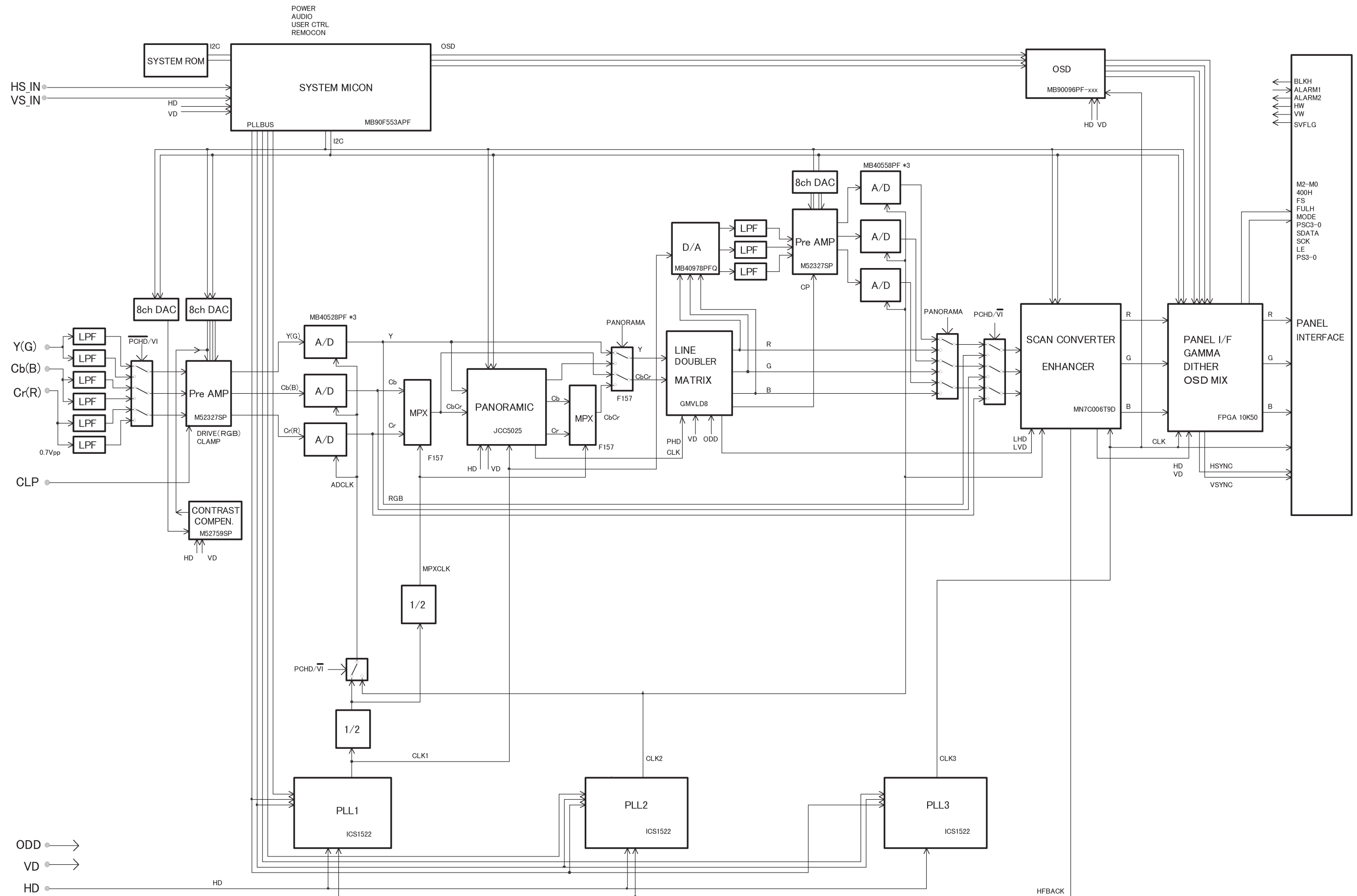
[FILTER PWB PATTERN]



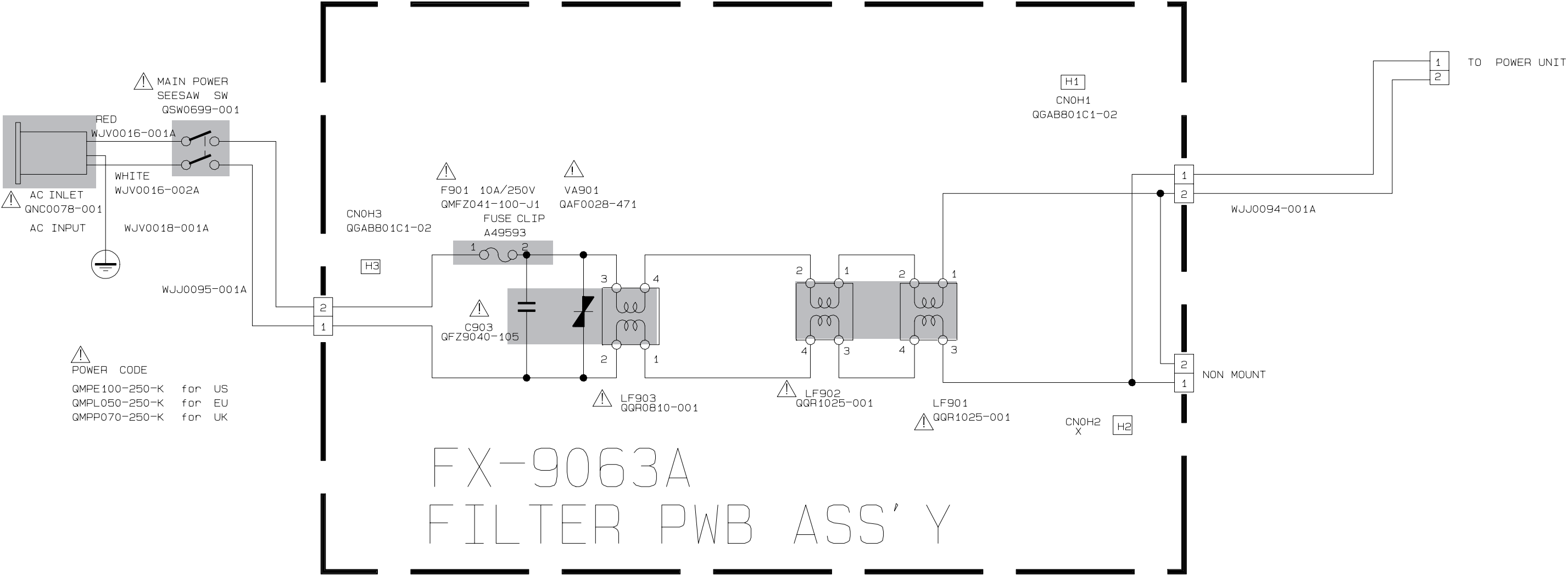
BLOCK DIAGRAM (ANALOG)



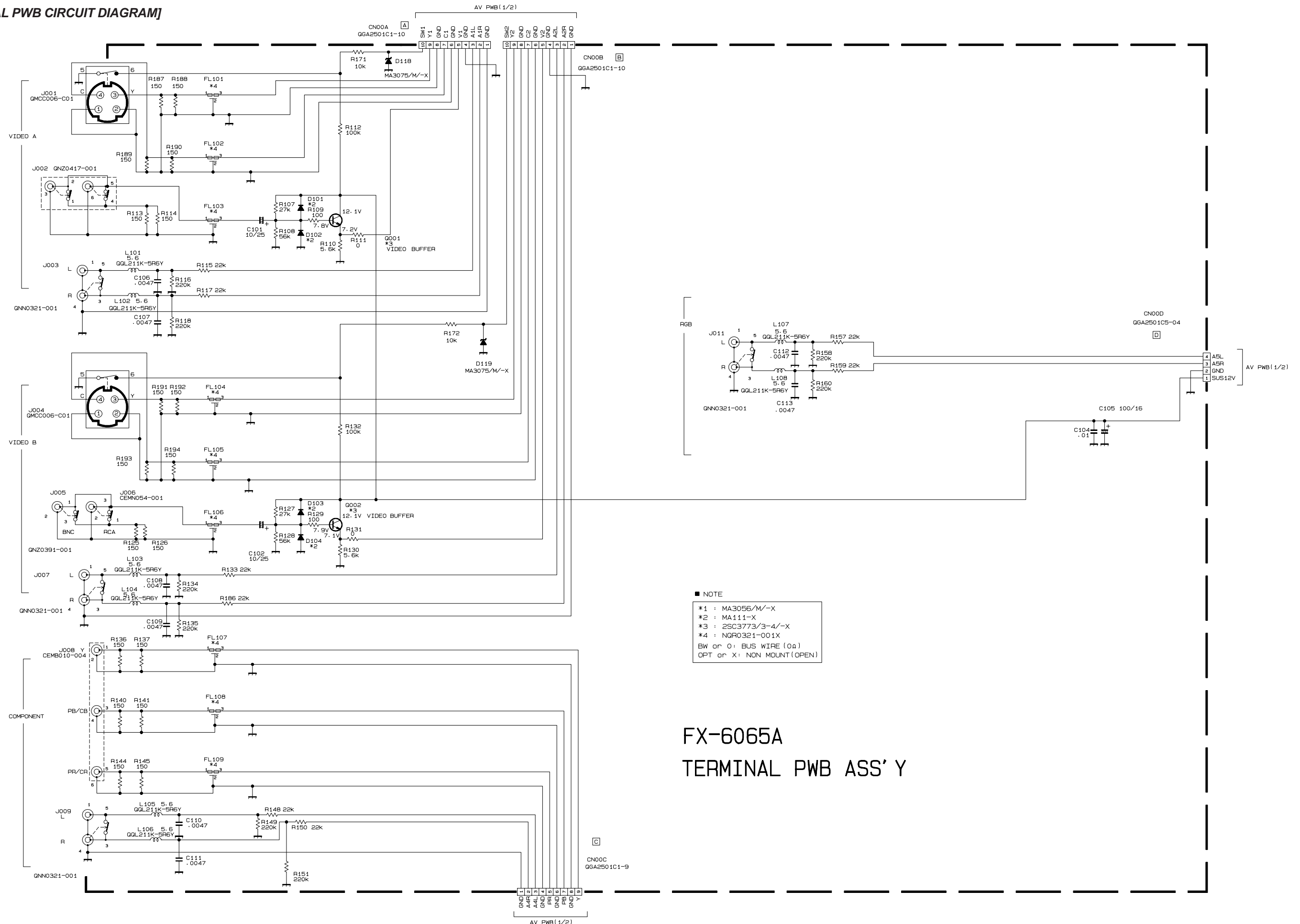
BLOCK DIAGRAM (DIGITAL)



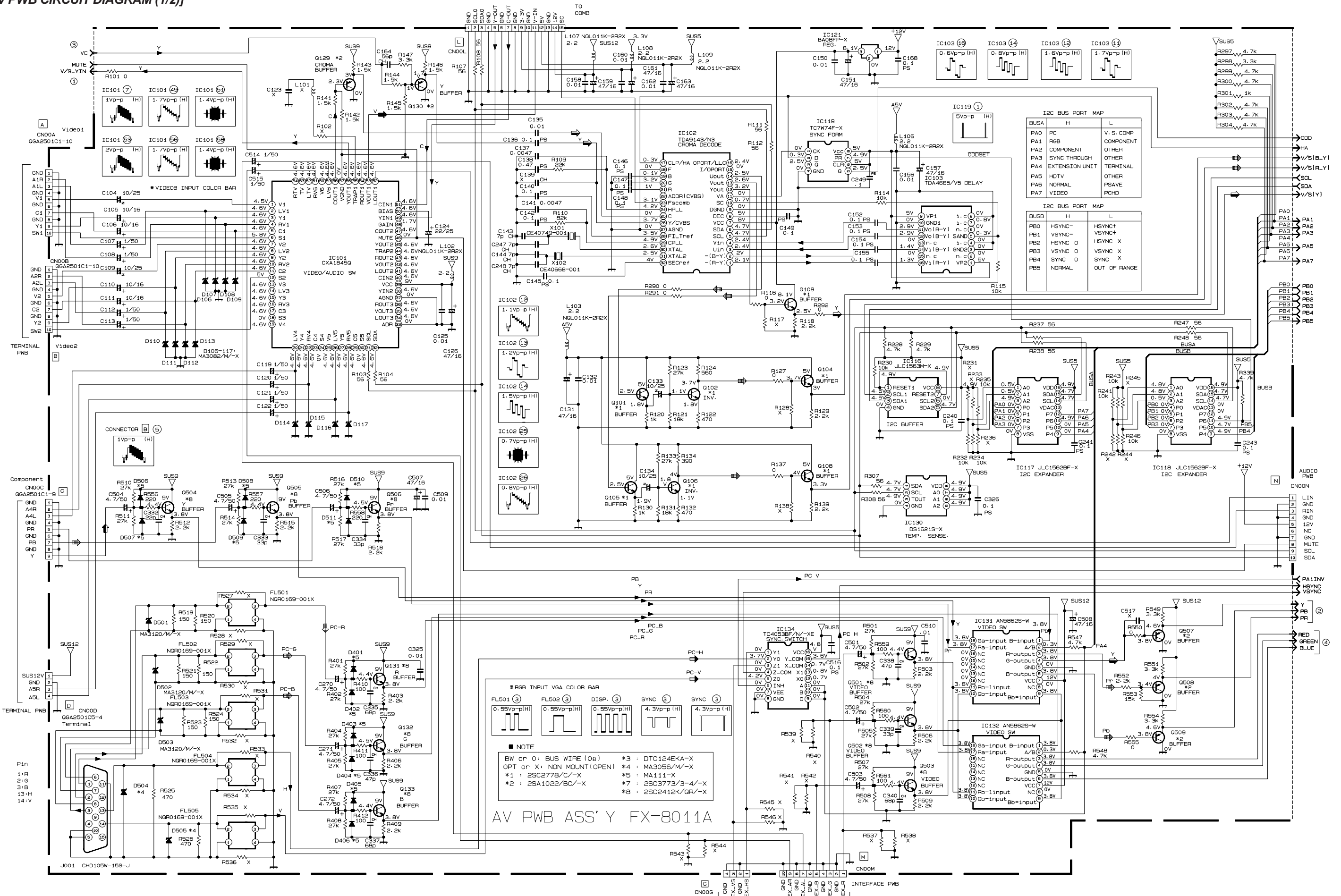
CIRCUIT DIAGRAMS [FILTER PWB CIRCUIT DIAGRAM]



[TERMINAL PWB CIRCUIT DIAGRAM]

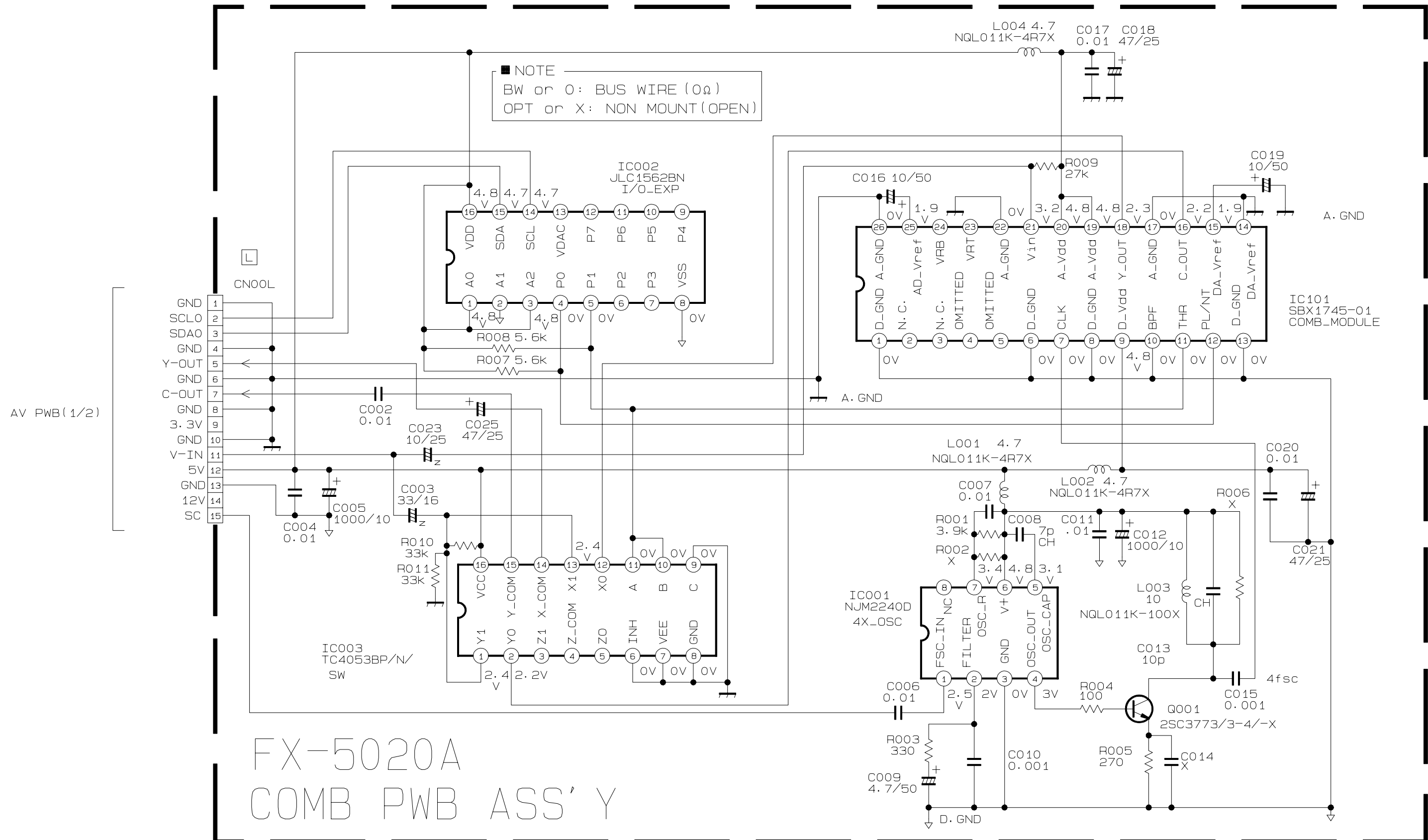


[AV PWB CIRCUIT DIAGRAM (1/2)]

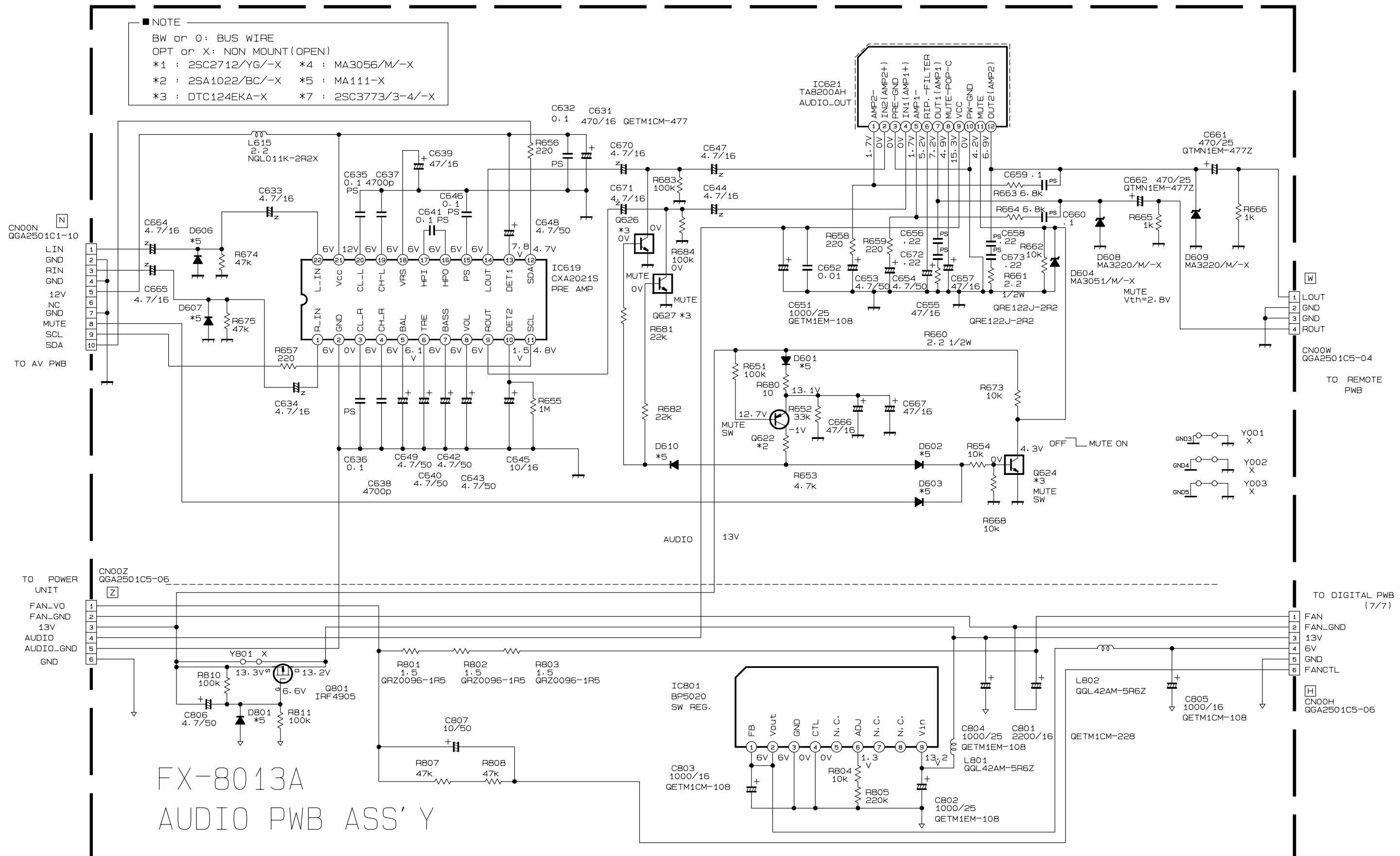


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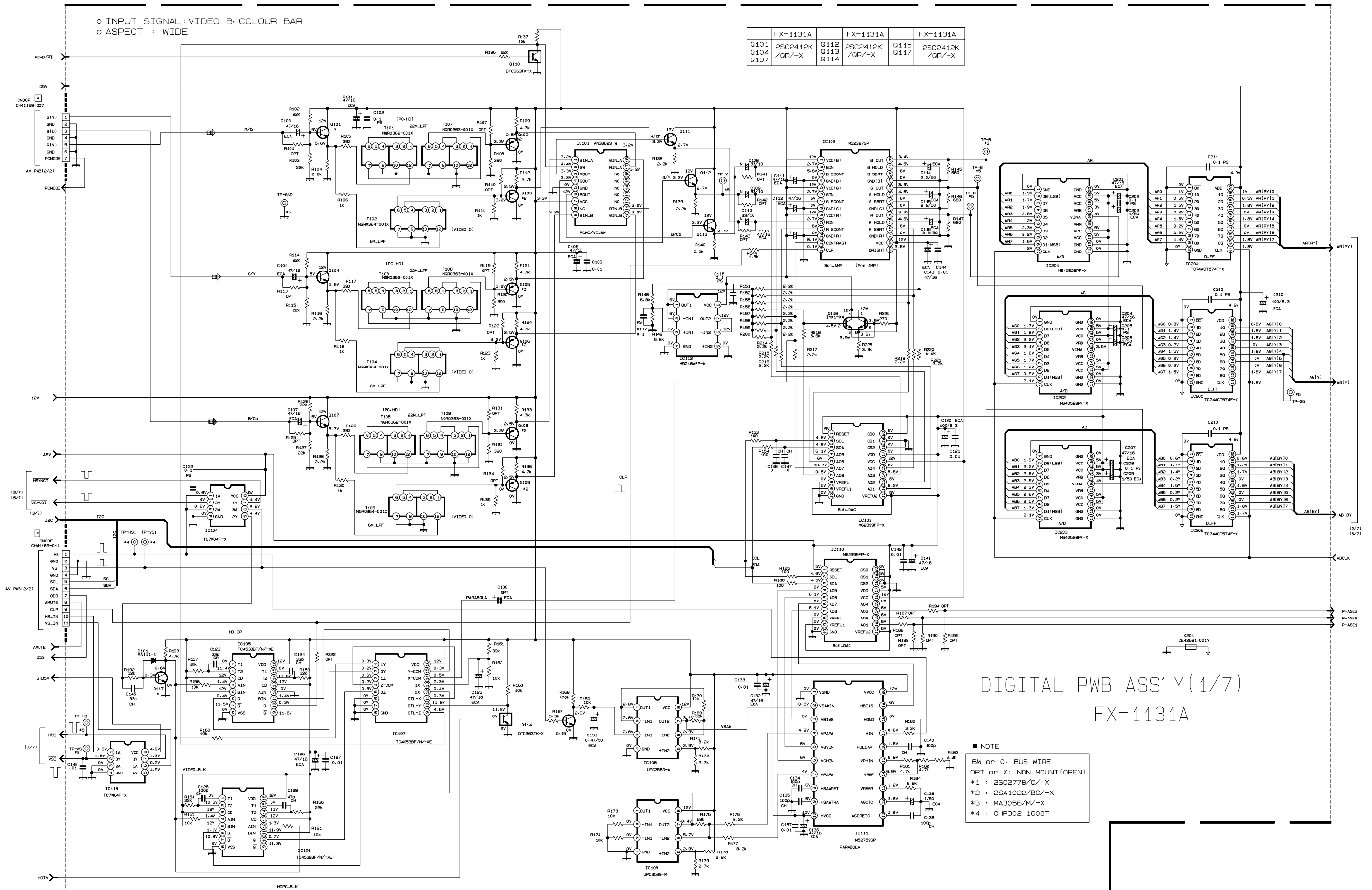
[COMB PWB CIRCUIT DIAGRAM]



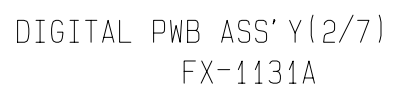
[AUDIO PWB CIRCUIT DIAGRAM]



[DIGITAL PWB CIRCUIT DIAGRAM (1/7)]



ANORAMA

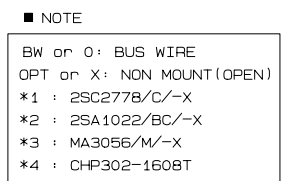


■ NOTE

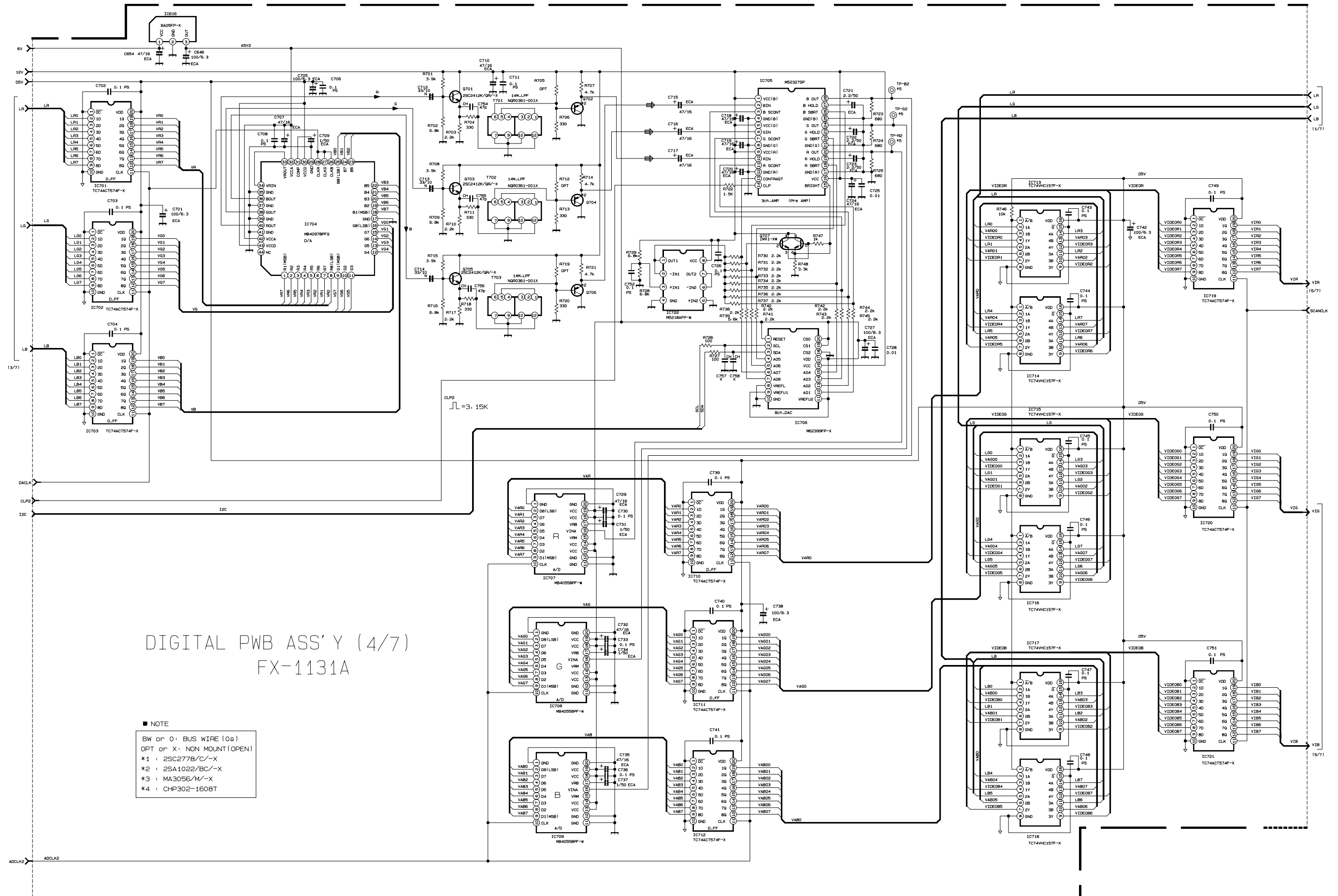
BW or 0: BUS WIRE (0 ϕ)
OPT or X: NON MOUNT (OPEN)

*1 : 2SC277B/C/-X
*2 : 2SA1022/BC/-X
*3 : MA3056/M/-X
*4 : CHP302-1608T

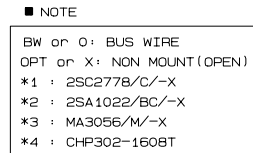
DIGITAL PWB ASS'Y(3/7)
FX-1131A



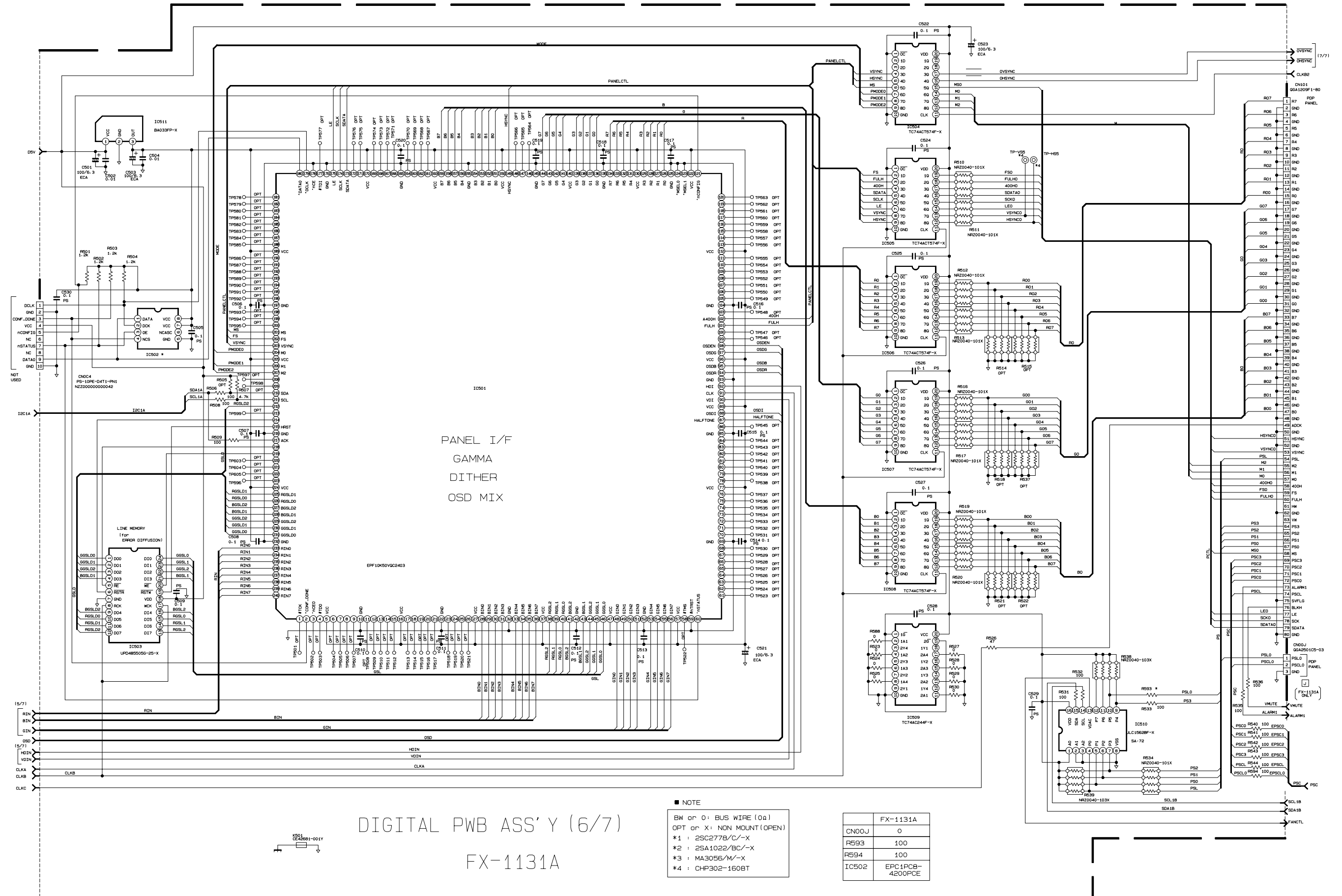
[DIGITAL PWB CIRCUIT DIAGRAM (4/7)]



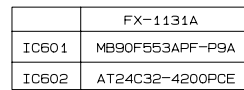
DIGITAL PWB ASS'Y(5/7)
FX-1131A



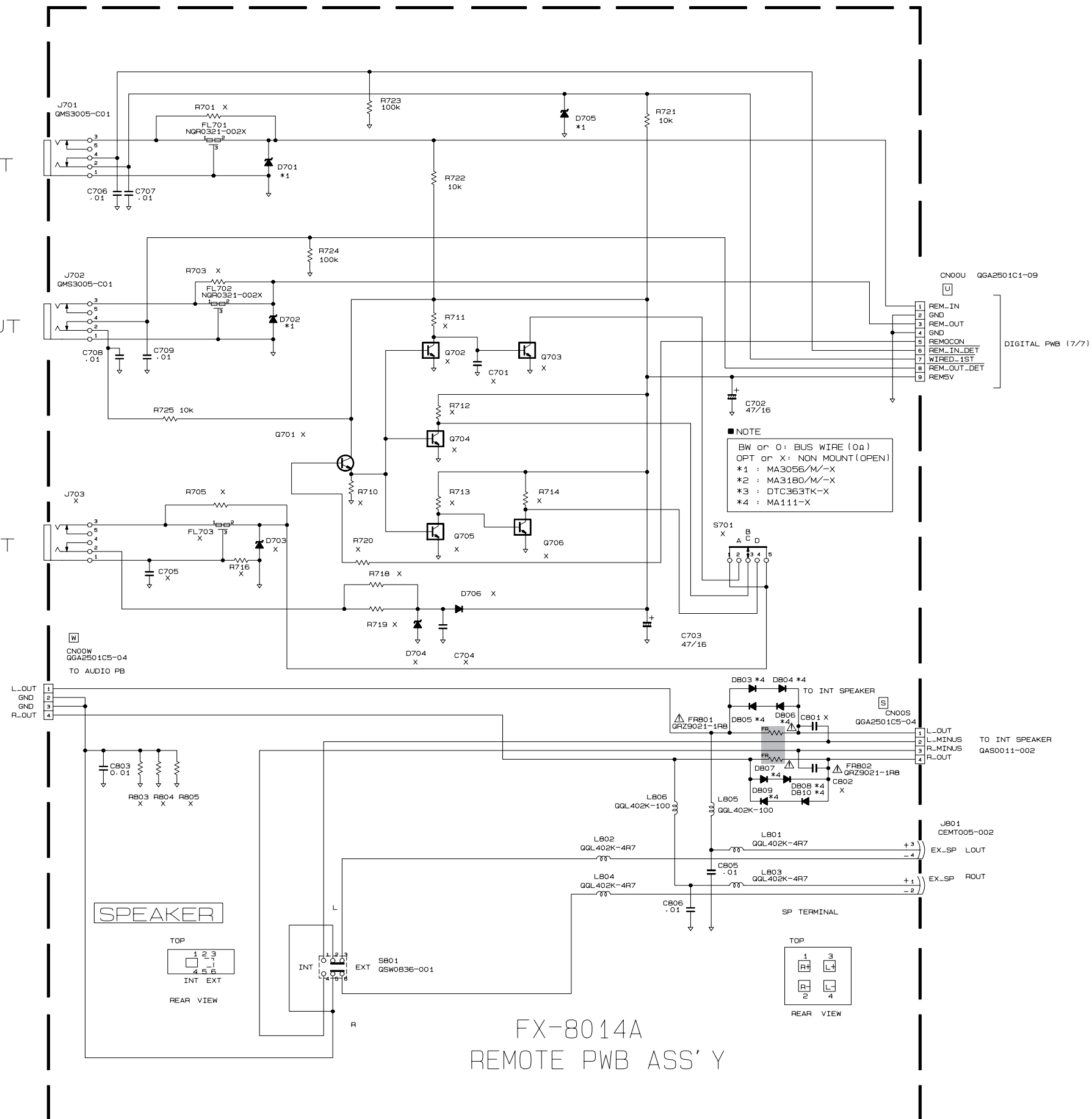
[DIGITAL PWB CIRCUIT DIAGRAM (6/7)]



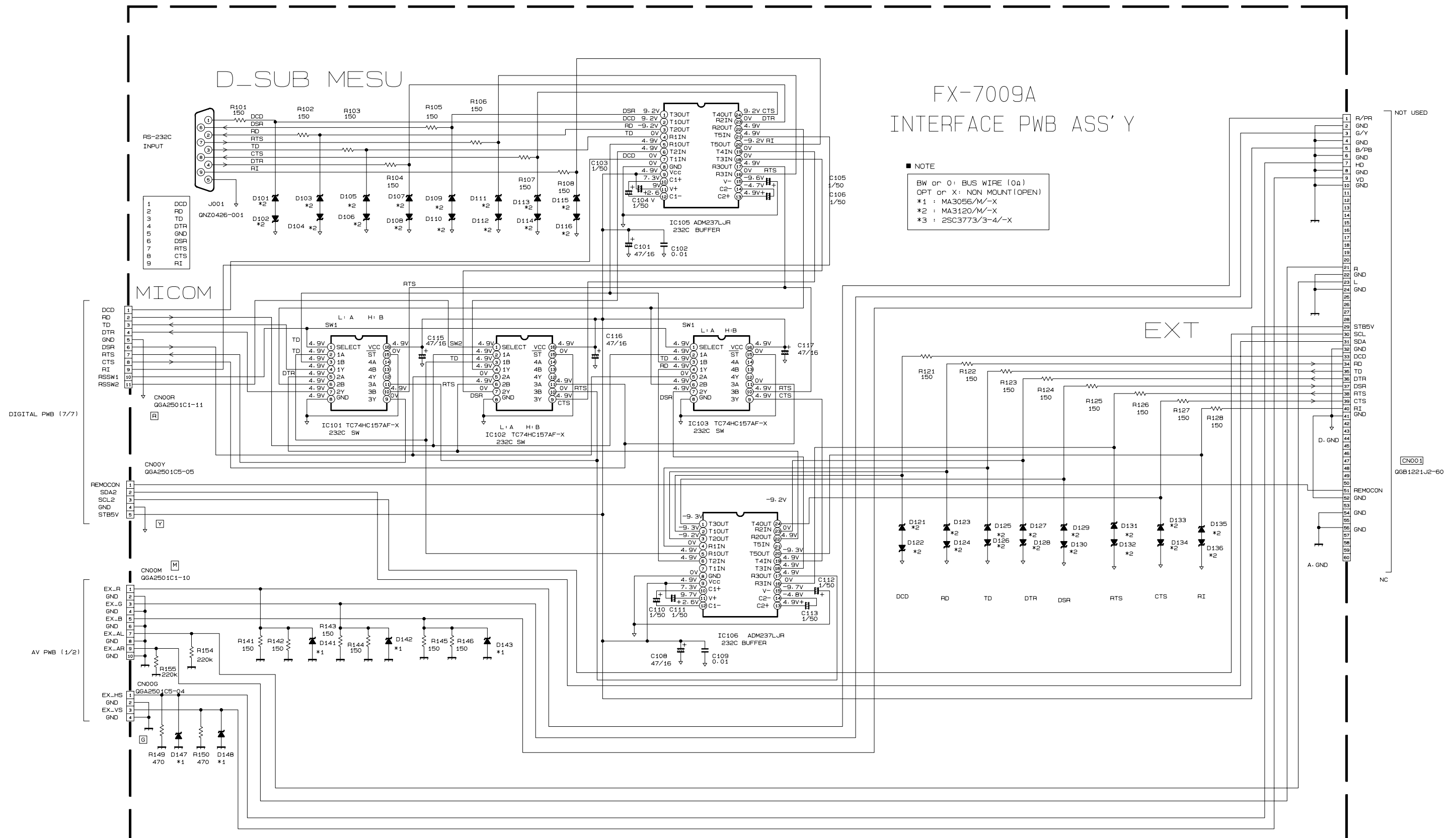
No.51732



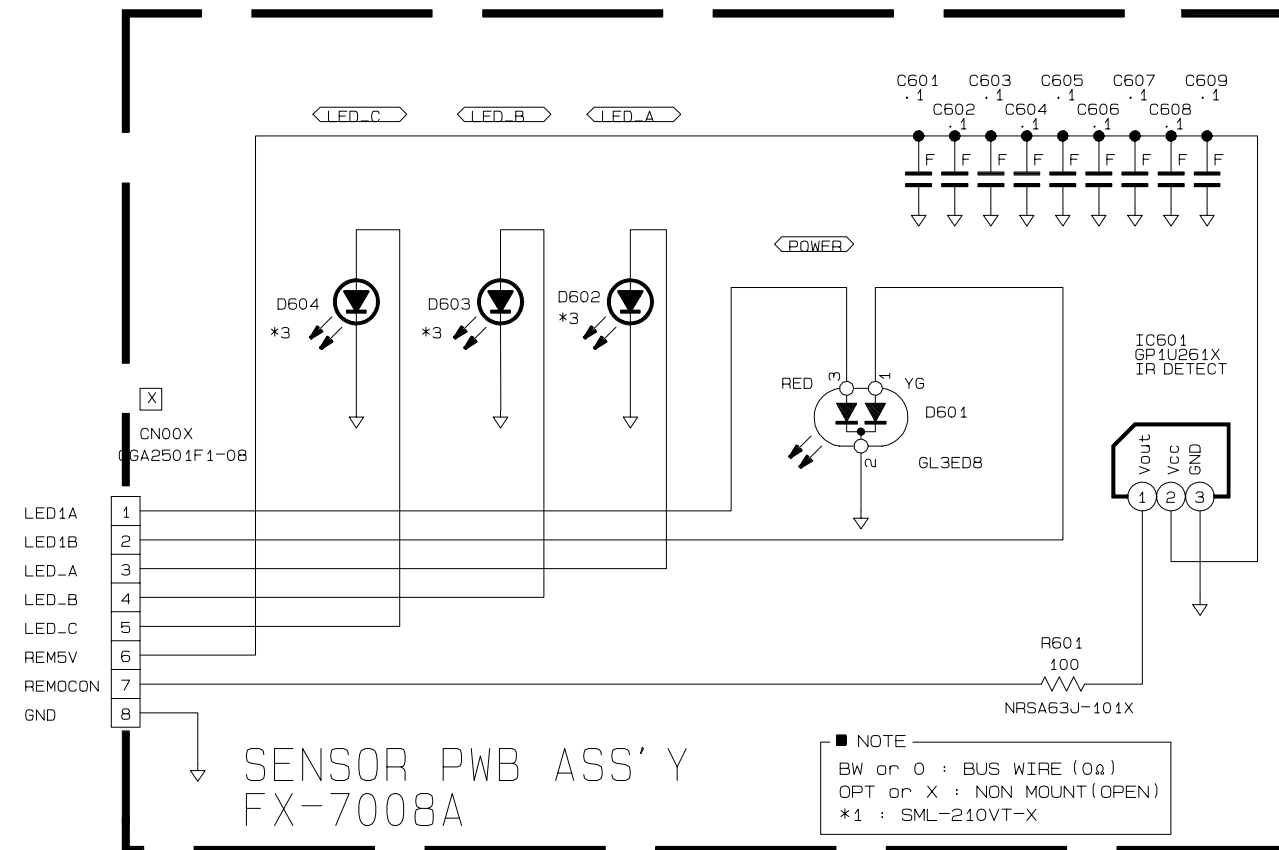
IR_REMOCON_OUT



[INTERFACE PWB CIRCUIT DIAGRAM]

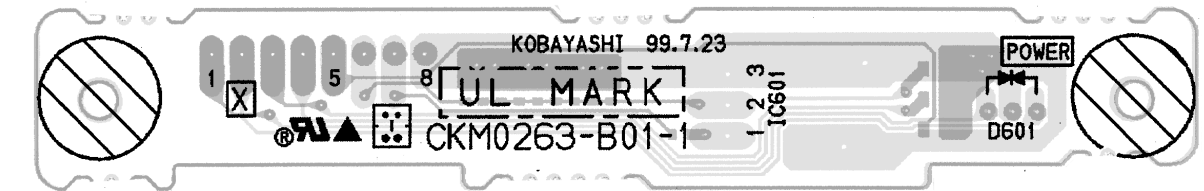
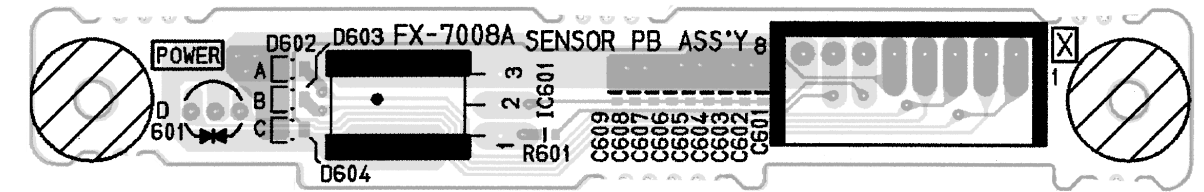


[SENSOR PWB CIRCUIT DIAGRAM]

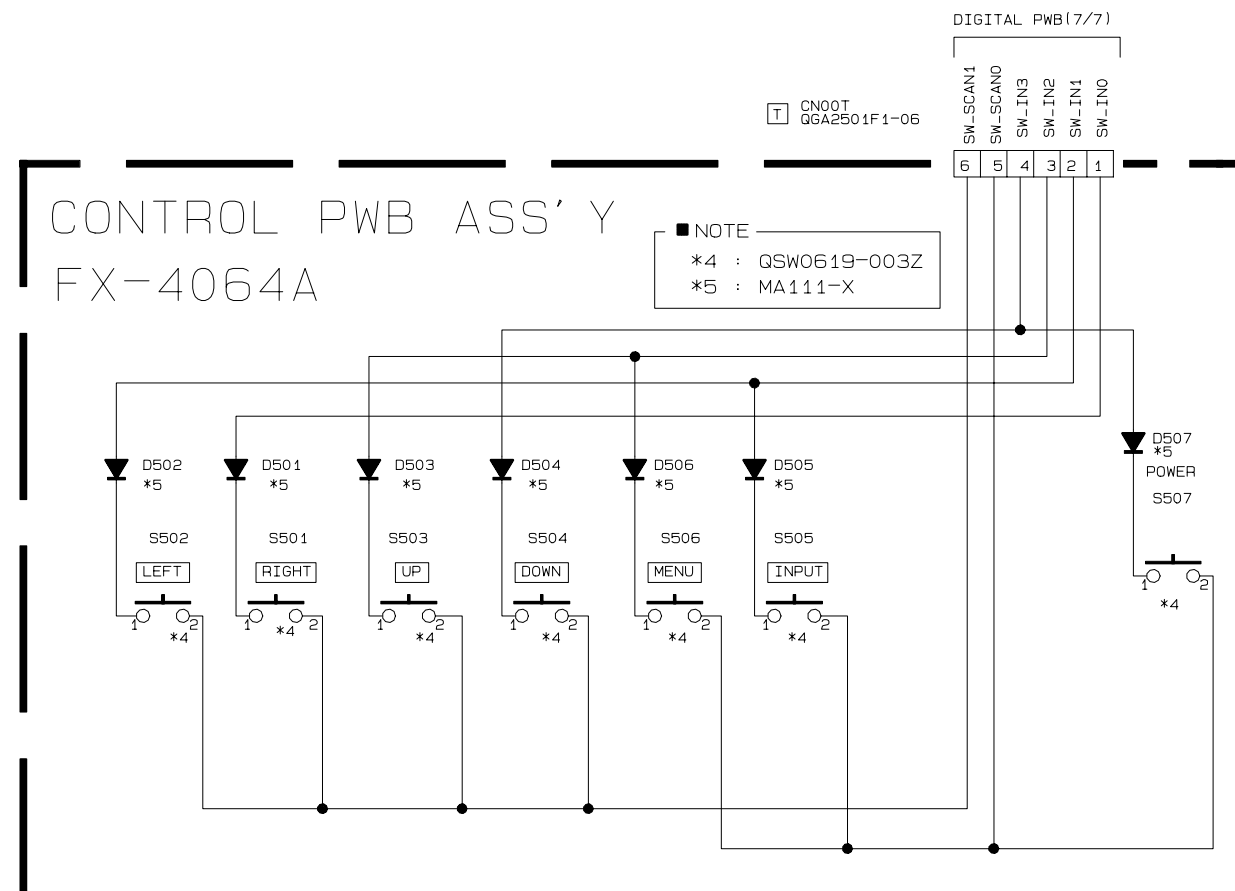


PATTERN DIAGRAMS

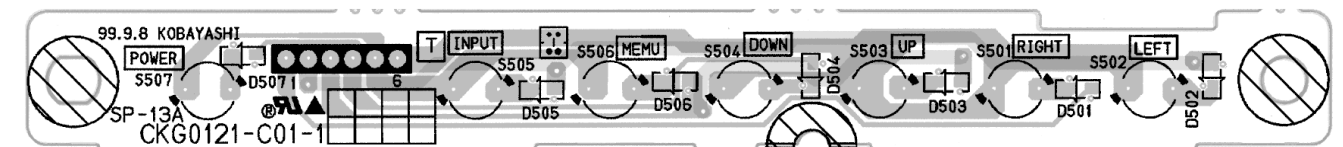
[SENSOR PWB PATTERN]



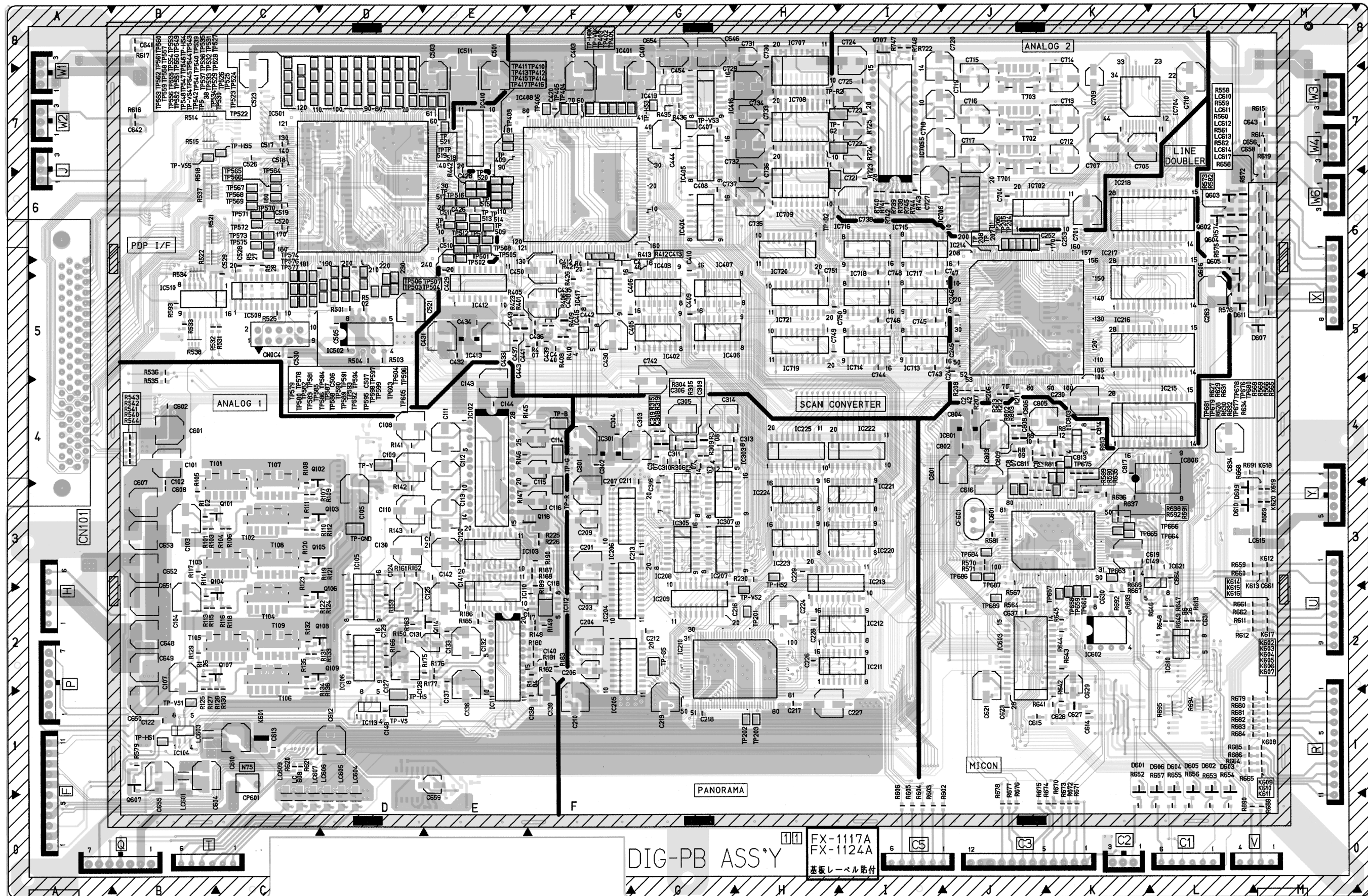
[CONTROL PWB CIRCUIT DIAGRAM]



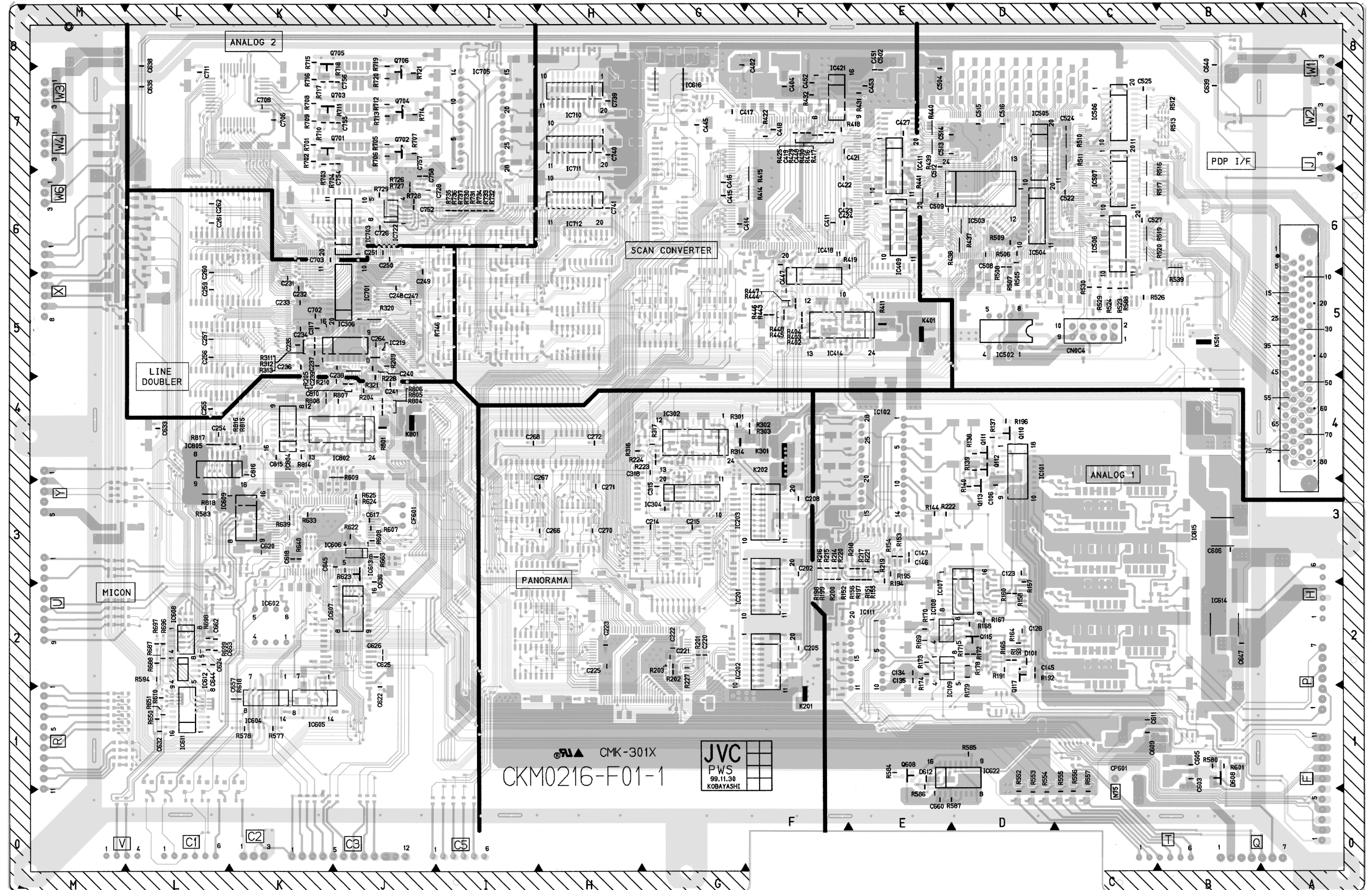
[CONTROL PWB PATTERN]



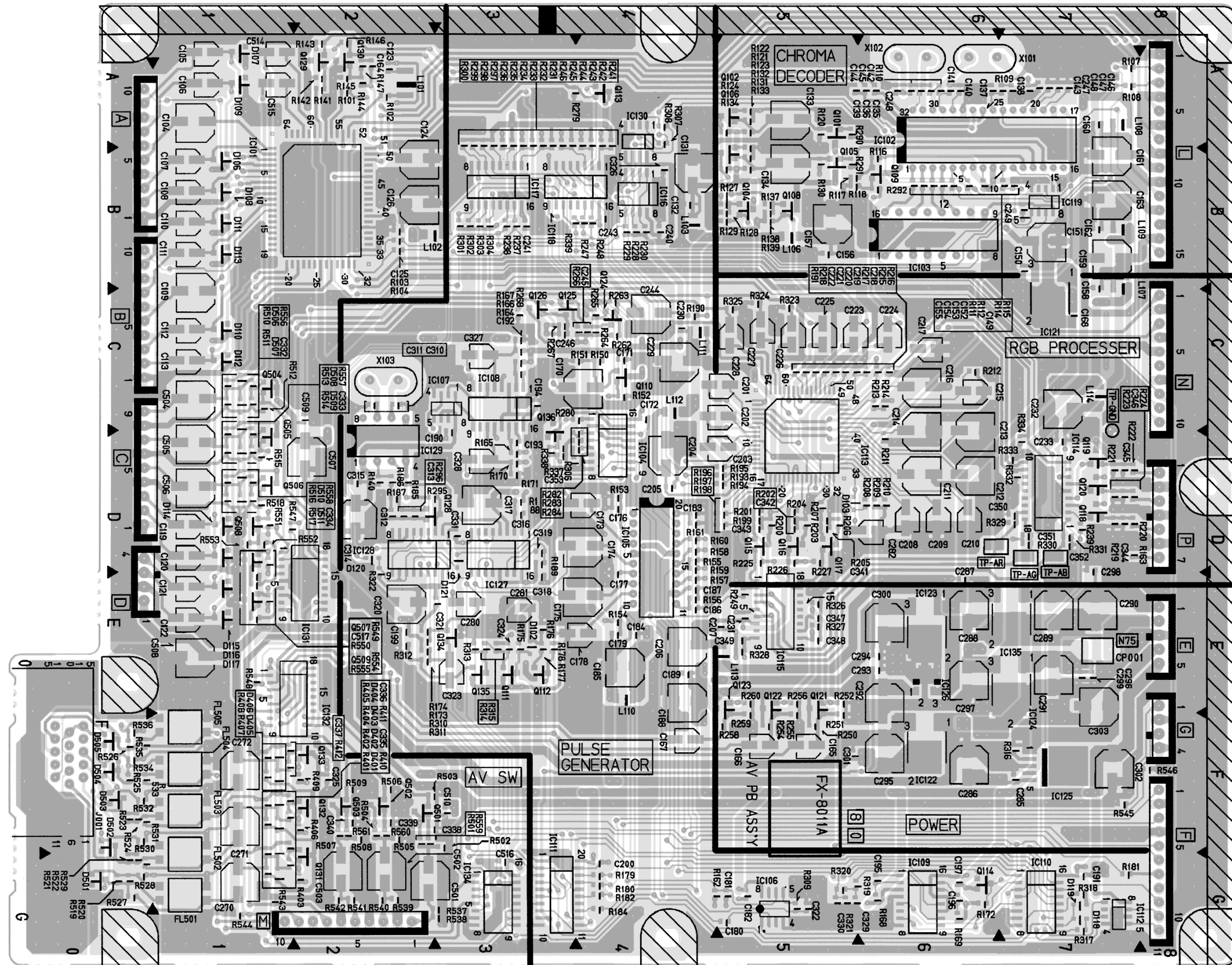
[DIGITAL PWB PATTERN] (side A)



[DIGITAL PWB PATTERN] (side B)



[AV PWB PATTERN]



TOP



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VICTOR COMPANY OF JAPAN, LIMITED

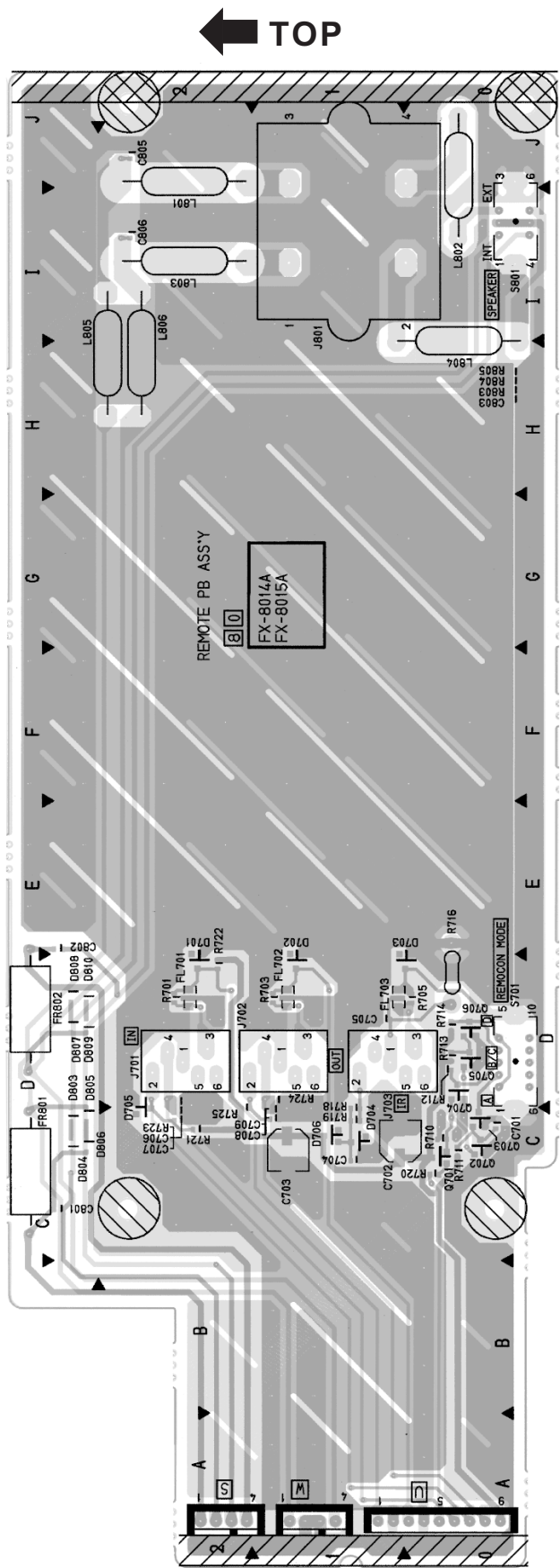
HOME AV NETWORK BUSINESS UNIT 1106 Heta, Iwai-city, Ibaraki-prefecture, 306-0698, Japan

NV-PD4200 #3
NV-PD4200/A #4

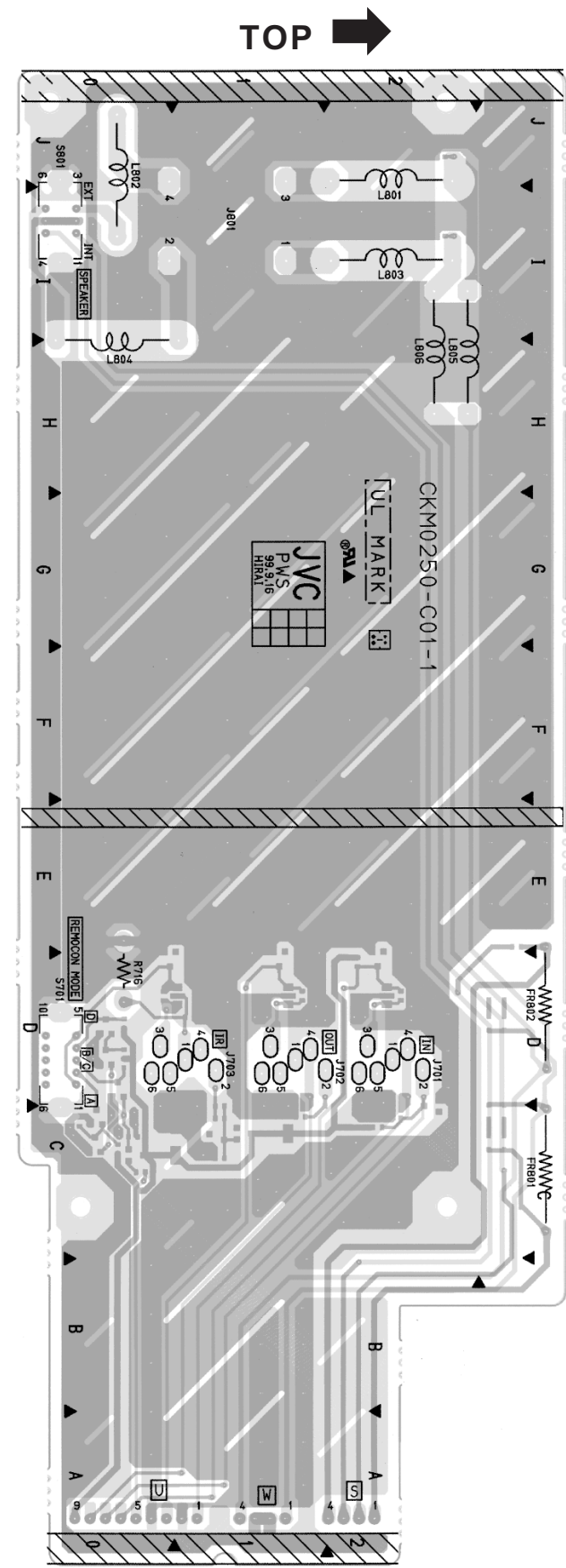


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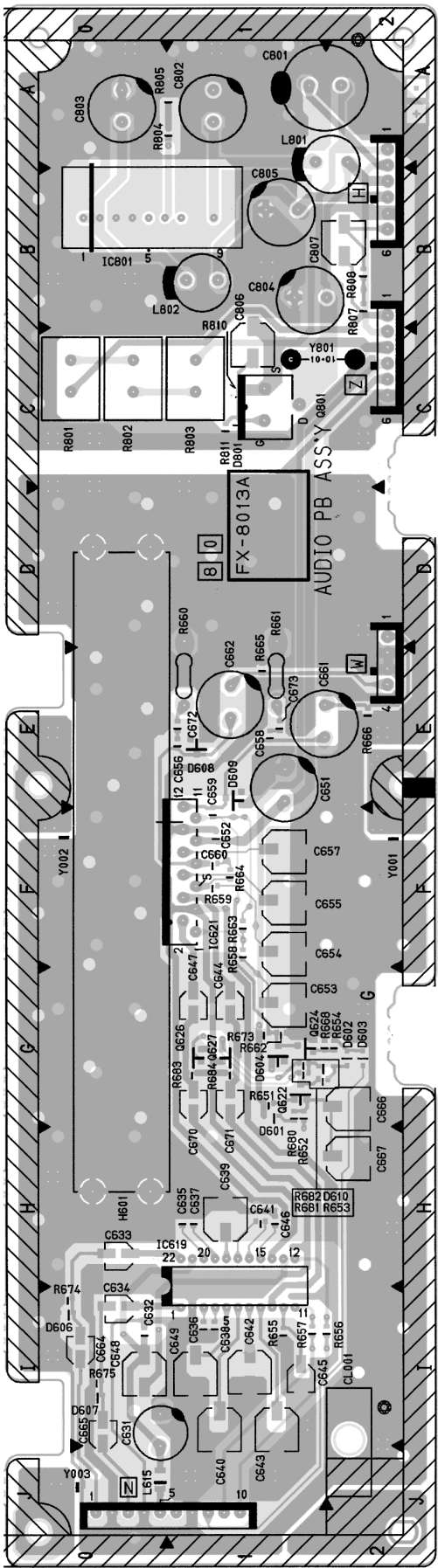
[REMOTE PWB PATTERN] (side A)



[REMOTE PWB PATTERN] (side B)



[AUDIO PWB PATTERN] (side A)



[AUDIO PWB PATTERN] (side B)

TOP

